

# **Excellent Integrated System Limited**

Stocking Distributor

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Diodes Incorporated DMG3418L-7

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**Features** 

Low On-Resistance Low Input Capacitance Fast Switching Speed

**Mechanical Data** 

Case: SOT23





## DMG3418L

### N-CHANNEL ENHANCEMENT MODE MOSFET

Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3) Qualified to AEC-Q101 standards for High Reliability

Case Material: Molded Plastic, "Green" Molding Compound. UL

Terminals: Finish — Matte Tin annealed over Copper leadframe.

### **Product Summary**

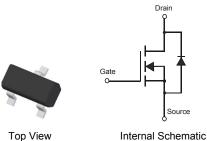
V <sub>(BR)DSS</sub>	R <sub>DS(ON) max</sub>	Ι <sub>D</sub> T <sub>A</sub> = +25°C
30V	60mΩ @V <sub>GS</sub> = 10V	4 A
300	70mΩ @V <sub>GS</sub> = 4.5V	3 A

### Description

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### Applications

- Backlighting
- **Power Management Functions**
- **DC-DC Converters**
- Motor Control



D G S

Flammability Classification Rating 94V-0 Moisture Sensitivity: Level 1 per J-STD-020

Solderable per MIL-STD-202, Method 208 @3

Terminals Connections: See Diagram Below

Weight: 0.008 grams (approximate)

Top View

### Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMG3418L-7	Standard	SOT23	3000/Tape & Reel
DMG3418L-13	Standard	SOT23	10000/Tape & Reel

Drain

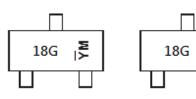
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### Marking Information



18G = Product Type Marking Code YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or  $\overline{Y}$  = Year (ex: A = 2013) M = Month (ex: 9 = September)

Chengdu A/T Site

Shanghai A/T Site

Σ

Date	Code	Key
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Notes:

Year	2012		2013		2014	201	5	2016		2017	2	018
Code	Z		А		В	C		D		E		F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D





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# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

	Characteristic	Symbol	Value	Unit
Drain Source Voltage		V <sub>DSS</sub>	30	V
Gate-Source Voltage		V <sub>GSS</sub>	±12	V
Drain Current (Note 5)	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	4.0 3.1	А
Drain Current (Note 6)	Pulsed	I <sub>DM</sub>	15	A

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	PD	1.4 0.9	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> =	$R_{ ext{ heta}JA}$	90	°C/W	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

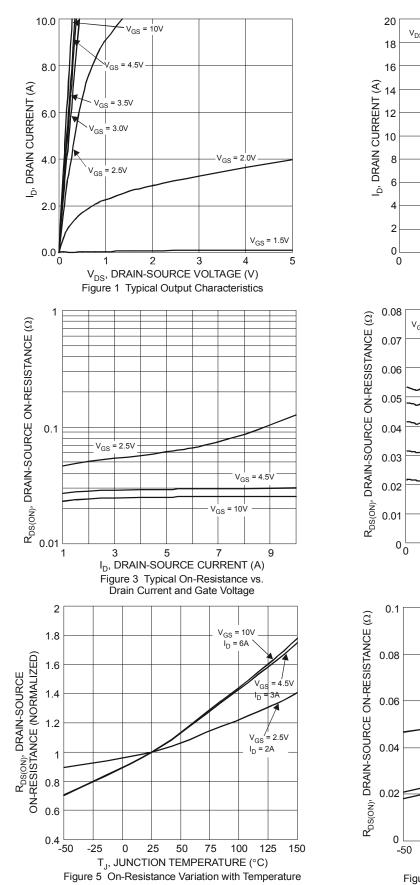
Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)				•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	
Gate-Body Leakage	I <sub>GSS</sub>	_	_	±100	nA	V <sub>GS</sub> = ±12V, V <sub>DS</sub> = 0V	
ON CHARACTERISTICS (Note 7)				•	•		
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	_	1.5	V	$V_{DS}$ = $V_{GS}$ , $I_D$ = 250 $\mu$ A	
		_	25	60		V <sub>GS</sub> = 10V, I <sub>D</sub> = 4A	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	30	70	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3A	
		—	50	150		$V_{GS}$ = 2.5V, $I_{D}$ = 2A	
Source-Drain Diode Forward Voltage	V <sub>SD</sub>		_	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2.0A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>		464.3	_	pF		
Output Capacitance	C <sub>oss</sub>	_	49.5	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		43.8	_	pF	1 1.000112	
Total Gate Charge	Qg	_	5.5	_			
Gate-Source Charge	Q <sub>gs</sub>	_	1.1	_	nC $V_{GS} = 4.5V, V_{DS} = 18$		
Gate-Drain Charge	Q <sub>gd</sub>	_	1.8	_		$I_D = 4A$	
Turn-On Delay Time	t <sub>D(on)</sub>	_	1.9	—	ns		
Turn-On Rise Time	tr		1.6	_	ns	V <sub>DD</sub> = 15V, V <sub>GEN</sub> = 10V,	
Turn-Off Delay Time	t <sub>D(off)</sub>		10.3	_	ns	R <sub>GEN</sub> = 3Ω, R <sub>L</sub> = 3.75Ω	
Turn-Off Fall Time	t <sub>f</sub>		2.0	_	ns		

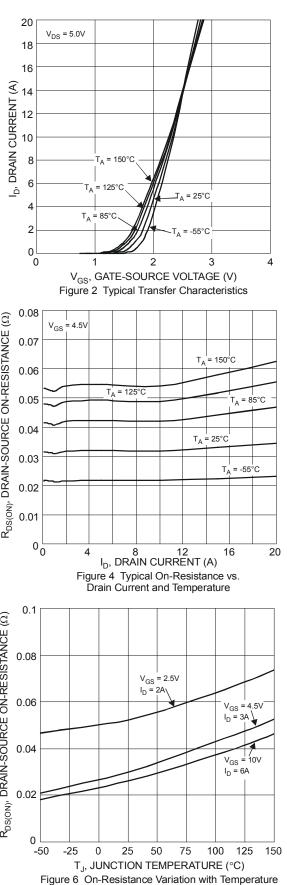
Notes:

Device mounted on FR-4 PCB with 2oz. Copper and test pulse width t ≤ 10s.
Repetitive rating, pulse width limited by junction temperature.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.





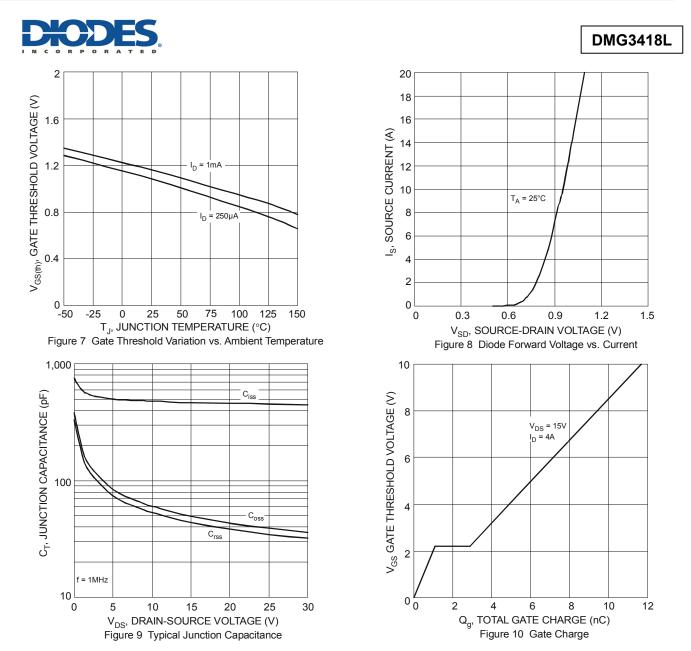


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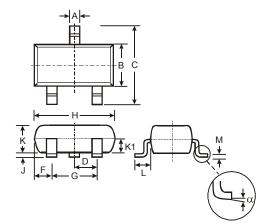
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### **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
с	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
ĸ	0.903	1.10	1.00				
K1	-	-	0.400				
L	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All	All Dimensions in mm						

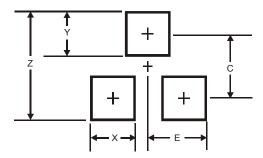




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### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
ш	1.35

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