

# **Excellent Integrated System Limited**

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<u>Vishay Semiconductor/Diodes Division</u> <u>VS-30BQ100-M3/9AT</u>

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Datasheet of VS-30BQ100-M3/9AT - DIODE SCHOTTKY 3.0A SMC





#### VS-30BQ100-M3

### Vishay Semiconductors

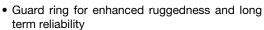
# Schottky Rectifier, 3 A



PRODUCT SUMMARY				
Package	SMC			
I <sub>F(AV)</sub>	3.0 A			
$V_R$	100 V			
V <sub>F</sub> at I <sub>F</sub>	0.62 V			
I <sub>RM</sub>	5 mA at 125 °C			
T <sub>J</sub> max.	175 °C			
Diode variation	Single die			
E <sub>AS</sub>	3.0 mJ			

#### **FEATURES**

• Low forward voltage drop





FREE

- Halogen-free according to IEC 61249-2-21 definition
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC

#### **DESCRIPTION**

The VS-30BQ100-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	3.0	А		
V <sub>RRM</sub>		100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	800	Α		
V <sub>F</sub>	3.0 Apk, T <sub>J</sub> = 125 °C	0.62	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-30BQ100-M3	UNITS	
Maximum DC reverse voltage	$V_{R}$	100	V	
Maximum working peak reverse voltage	$V_{RWM}$	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum avarage forward aurrent		50 % duty cycle at T <sub>L</sub> = 148 °C, rectangular waveform		3.0	
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 138 °C, rectangular waveform		4.0	
Maximum peak one cycle	l=	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	800	А
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	70	
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 1.0  \text{A},  L = 6  \text{mH}$ 3.0 mJ		mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical  0.5		А	



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		3 A	T 05 00	0.79	V
Maxima was famuand valled a duar	V (1)	6 A	T <sub>J</sub> = 25 °C	0.90	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	3 A	T <sub>J</sub> = 125 °C	0.62	
		6 A		0.70	
Maximum roveree leekage current		T <sub>J</sub> = 25 °C	V Dated V	0.5	mA
Maximum reverse leakage current I <sub>RM</sub>		T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	5.0	MA
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz), 25 °C		115	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 3.0 n		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs		V/µs	

#### Note

<sup>(1)</sup> Pulse width = 300  $\mu$ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 55 to 175	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>	DC operation	12	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	46	
Approximate weight			0.24	g
Approximate weight			0.008	OZ.
Marking device		Case style SMC (similar to DO-214AB) 3J		

#### Notes

<sup>(1)</sup>  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

<sup>(2)</sup> Mounted 1" square PCB



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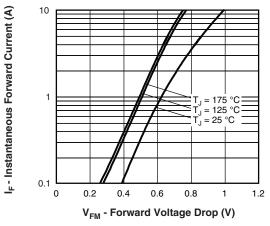


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

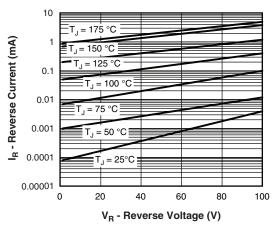


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

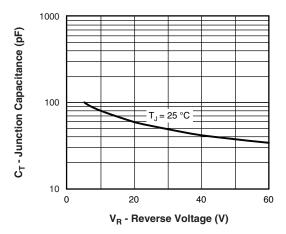


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

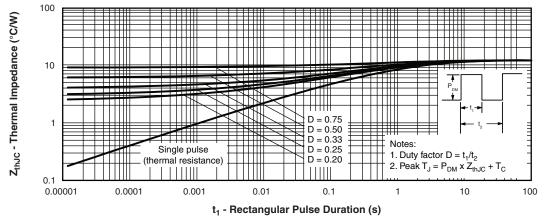


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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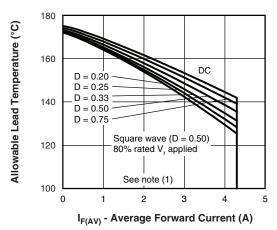


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

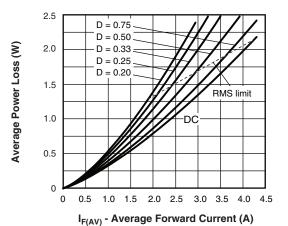


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

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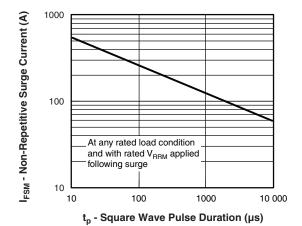


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ; Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss =  $V_{R1} \times I_{R}$  (1 - D);  $I_{R}$  at  $V_{R1}$  = 80 % rated  $V_{R}$ 

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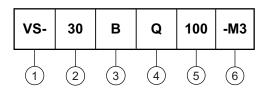
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#### **ORDERING INFORMATION TABLE**





Vishay Semiconductors product suffix

2 - Current rating

- B = SMC

4 - Q = Schottky "Q" series

5 - Voltage rating (100 = 100 V)

6 - Environmental digit:

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	RRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-30BQ100-M3/9AT	9AT	3500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95402</u>			
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		



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