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Vishay Semiconductor/Diodes Division VS-4CSH01HM3/86A

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Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of VS-4CSH01HM3/86A - DIODE HFAST REC 100V 2A TO277A

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VS-4CSH01HM3

RoHS

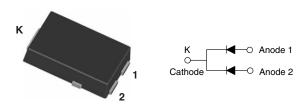
COMPLIANT

HALOGEN

FREE

Vishay Semiconductors

Hyperfast Rectifier, 2 x 2 A FRED Pt®



TO-277A (SMPC)

PRODUCT SUMMARY					
Package	TO-277A (SMPC)				
I _{F(AV)}	2 x 2 A				
V _R	100 V				
V _F at I _F	0.75 V				
t _{rr} (typ.)	24 ns				
T _J max.	175 °C				
Diode variation	Dual die				

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RA	TINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage		V _{RRM}		100	V	
Average restified forward ourrent	per device	I	T 165 °C	4		
Average rectified forward current	per diode	IF(AV)	T _{Sp} = 165 °C	2	A	
Non-repetitive peak surge current	per device	1	T _ 25 °C	90		
	per diode	IFSM	T _J = 25 °C	50		
Operating junction and storage temp	eratures	T _J , T _{Stg}		-65 to +175	°C	

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	100	-	-	
Forward voltage, par diada	VF	I _F = 2 A	-	0.88	0.95	V
Forward voltage, per diode	۷F	I _F = 2 A, T _J = 125 °C	-	0.75	0.82	
Deverse leekene eurrent ner diede		$V_{R} = V_{R}$ rated	-	-	2	
Reverse leakage current, per diode	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	0.5	8	μA
Junction capacitance	CT	V _R = 100 V	-	8	-	pF

Revision: 16-Jul-15

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		I _F = 1.0 A, dI _F /dt = 5	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		24	-	
Reverse recovery time	+	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	25	ns
Reverse recovery time t _{rr}	۲r	T _J = 25 °C		-	16	-	115
		T _J = 125 °C	$I_F = 2 A$ dI _F /dt = 200 A/µs V _R = 160 V	-	22	-	
Deals receivers comment		T _J = 25 °C		-	2	-	А
Peak recovery current I _{RRM}	IRRM	T _J = 125 °C		-	3	-	
Reverse recovery charge	0	T _J = 25 °C		-	16	-	nC
	Q _{rr}	T _J = 125 °C		-	30	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	+175	°C
Thermal resistance, junction to solder pad, per diode	R _{thJ-Sp}		-	4.5	5.5	°C/W
Approximate weight				0.1		g
				0.0035		oz.
Marking device		Case style TO-277A (SMPC)		JC	H1	

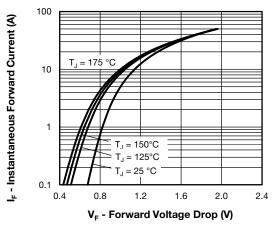


Fig. 1 - Typical Forward Voltage Drop Characteristics

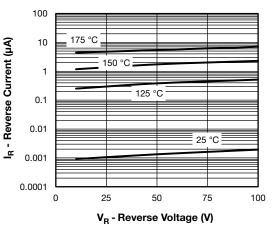


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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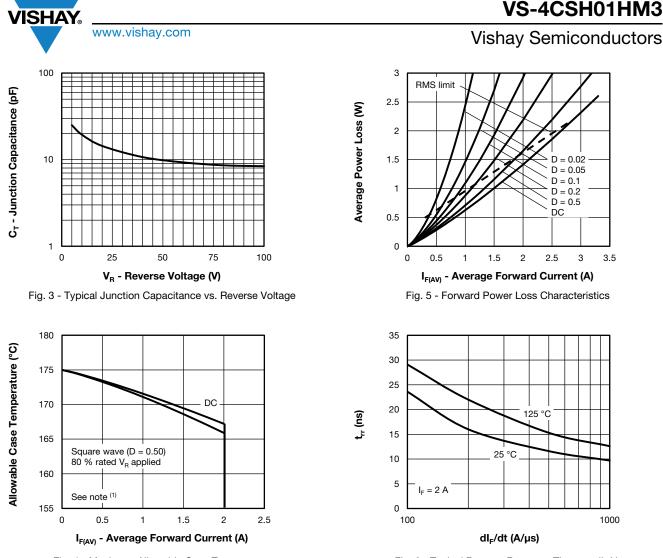


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current



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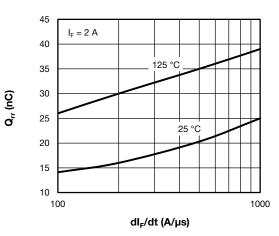


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

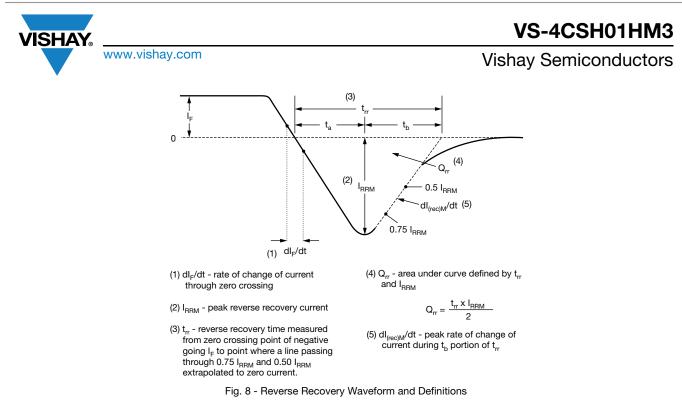
- Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; (1)
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig. 5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE

Device code	VS-	4	С	S	н	01	Н	М3
	1	2	3	4	5	6	7	8
	1 · 2 ·			niconduo ng (4 = 4		oduct		
	3 -			iguration				
	4 -			package	9			
	5 -		cess typ hyperfa	oe, ist recov	/ery			
	6 -		0	de (01 =	,			
	7 - 8 -			101 qua en-free,		complia	nt, and	termina

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-4CSH01HM3/86A	1500	1500	7" diameter plastic tape and reel			
VS-4CSH01HM3/87A	6500	6500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95570				
Part marking information	www.vishay.com/doc?95565				
Packaging information	www.vishay.com/doc?88869				

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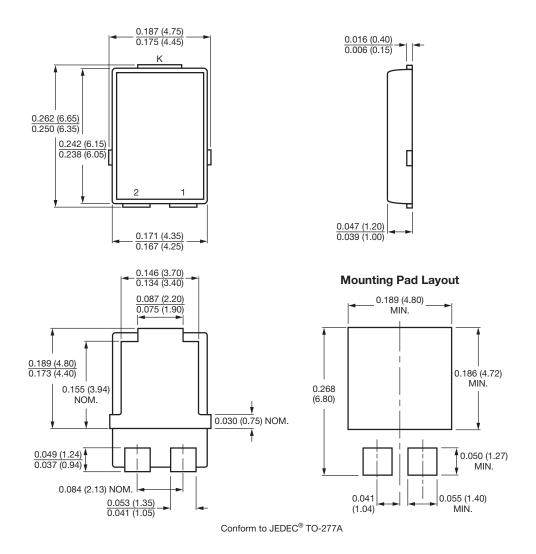
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Outline Dimensions

Vishay Semiconductors

TO-277A (SMPC)

DIMENSIONS in inches (millimeters)



Revision: 03-Sep-14

Document Number: 95570

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