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NXP Semiconductors/Freescale Semiconductor, Inc. PMEG045V150EPDAZ

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# PMEG045V150EPD

45 V, 15 A low VF MEGA Schottky barrier rectifier 22 January 2015 Prod

Product data sheet

### 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Average forward current:  $I_{F(AV)} \le 15 \text{ A}$
- Reverse voltage: V<sub>R</sub> ≤ 45 V
- Extremely low forward voltage
- High power capability due to clip-bonding technology and heat sink
- Small and thin SMD power plastic package, typical height 0.78 mm
- AEC-Q101 qualified

### 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

### 4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5; f = 20 kHz; T <sub>sp</sub> ≤ 160 °C; square wave	-	-	15	A
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C	-	-	45	V
V <sub>F</sub>	forward voltage	$\begin{split} I_F &= 15 \text{ A};  t_p \leq 300 \; \mu\text{s};  \overline{o} \leq 0.02; \\ T_j &= 25 \; ^\circ\text{C};  \text{pulsed} \end{split}$	-	430	490	mV
I <sub>R</sub>	reverse current	$V_R$ = 10 V; $t_p \le 3$ ms; $\delta \le 0.3$ ; T <sub>j</sub> = 25 °C; pulsed	-	30	70	μA
		$\label{eq:VR} \begin{split} V_{R} &= 45 \text{ V}; \ t_{p} \leq 3 \text{ ms}; \ \delta \leq 0.3; \\ T_{j} &= 25 \ ^{\circ}\text{C}; \ \text{pulsed} \end{split}$	-	420	900	μA







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### 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	А	anode		
2	A anode	anode		A aaa-009063
3	к	cathode		
			CFP15 (SOT1289)	

# 6. Ordering information

Table 3. Ordering in	formation					
Type number	Package	è				
	Name	Description	Version			
PMEG045V150EPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 leads; body: 5.8 x 4.3 x 0.78 mm	SOT1289			

## 7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG045V150EPD	045V 150E



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### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	45	V
I <sub>F</sub>	forward current	T <sub>sp</sub> = 155 °C; δ = 1		-	21	А
I <sub>F(AV)</sub>	average forward current	δ = 0.5; f = 20 kHz; T <sub>sp</sub> ≤ 160 °C; square wave		-	15	A
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	270	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
			[3]	-	3.75	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

<sup>[2]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[3] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.



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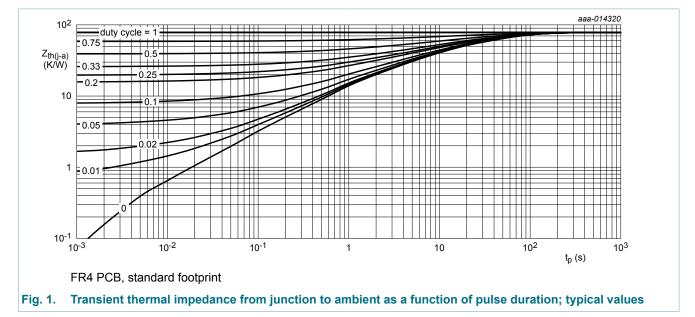
45 V, 15 A low VF MEGA Schottky barrier rectifier

### 9. Thermal characteristics

Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1][2]	-	-	90	K/W
			[1][3]	-	-	70	K/W
			[1][4]	-	-	40	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[5]	-	-	3	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses  $P_R$  are a significant part of the total power losses.

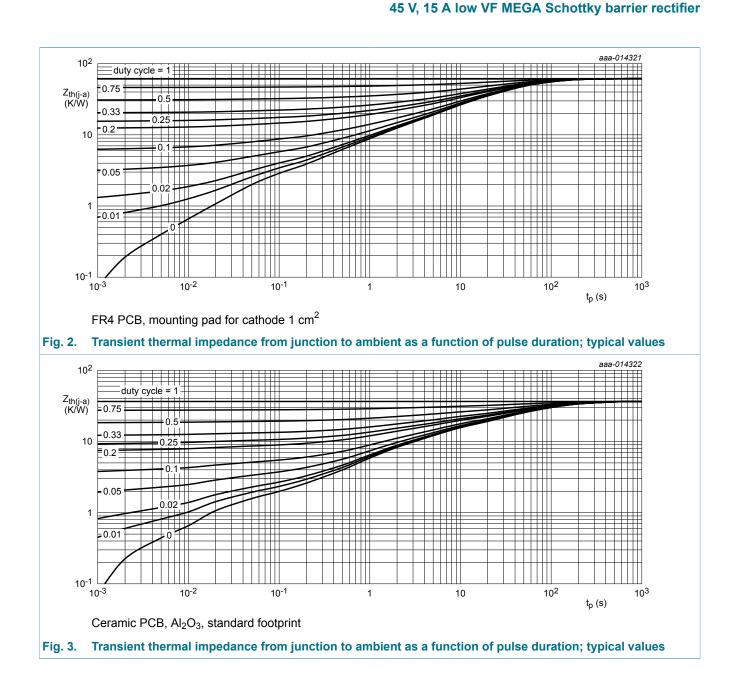
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- <sup>[3]</sup> Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [4] Device mounted on a ceramic PCB,  $AI_2O_3$ , standard footprint.
- [5] Soldering point of cathode tab.





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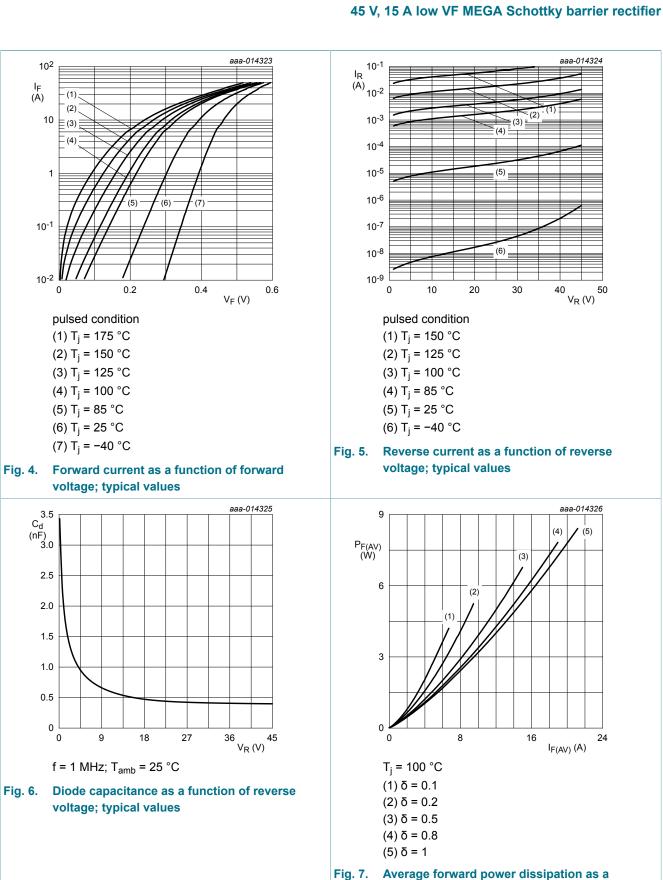
### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>(BR)R</sub>	reverse breakdown voltage	$I_R$ = 5 mA; $T_j$ = 25 °C; $t_p \le$ 1.2 ms; $\delta \le$ 0.12; pulsed	45	-	-	V
V <sub>F</sub>	forward voltage	$\begin{split} I_F &= 1 \text{ A}; \ t_p \leq 300 \ \mu\text{s}; \ \overline{o} \leq 0.02; \\ T_j &= 25 \ ^\circ\text{C}; \ \text{pulsed} \end{split}$	-	305	350	mV
		$I_F$ = 5 A; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 25 °C; pulsed	-	360	410	mV
		$I_F$ = 10 A; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 25 °C; pulsed	-	400	-	mV
		$I_F = 15 \text{ A}; t_p \le 300  \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}; \text{ pulsed}$	-	430	490	mV
		$I_F$ = 15 A; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>j</sub> = 125 °C; pulsed	-	335	-	mV
I <sub>R</sub>	reverse current	$V_R$ = 5 V; $t_p \le$ 3 ms; $\delta \le$ 0.3; $T_j$ = 25 °C; pulsed	-	20	-	μA
		$V_R$ = 10 V; $t_p \le 3$ ms; $\delta \le 0.3$ ; T <sub>j</sub> = 25 °C; pulsed	-	30	70	μA
		$V_R$ = 30 V; $t_p \le$ 3 ms; $\delta \le$ 0.3; T <sub>j</sub> = 25 °C; pulsed	-	90	-	μA
		$V_R$ = 45 V; $t_p \le 3$ ms; $\delta \le 0.3$ ; T <sub>j</sub> = 25 °C; pulsed	-	420	900	μA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	1870	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C	-	610	-	pF
t <sub>rr</sub>	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 \text{ °C}$	-	54	-	ns
t <sub>rr</sub>	reverse recovery time ramp recovery	dI <sub>F</sub> /dt = 200 A/µs; T <sub>j</sub> = 25 °C; I <sub>F</sub> = 6 A; V <sub>R</sub> = 26 V	-	19	-	ns
V <sub>FRM</sub>	peak forward recovery voltage	I <sub>F</sub> = 0.5 A; dI <sub>F</sub> /dt = 20 A/μs; T <sub>j</sub> = 25 °C	-	294	-	mV



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values

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function of average forward current; typical

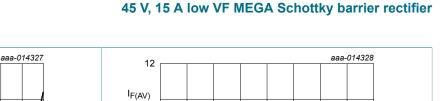


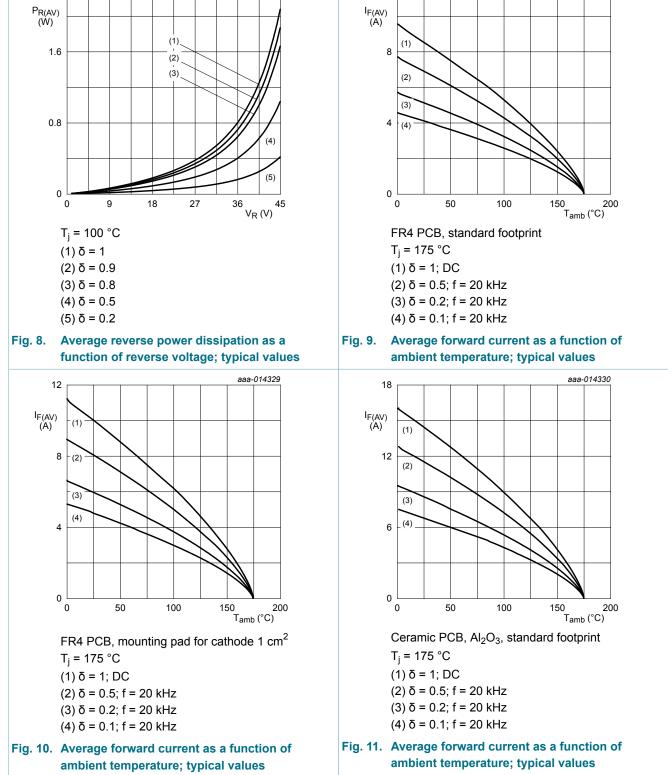
2.4

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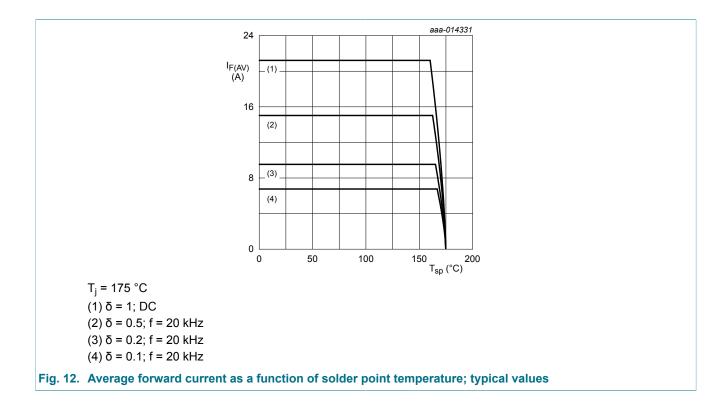
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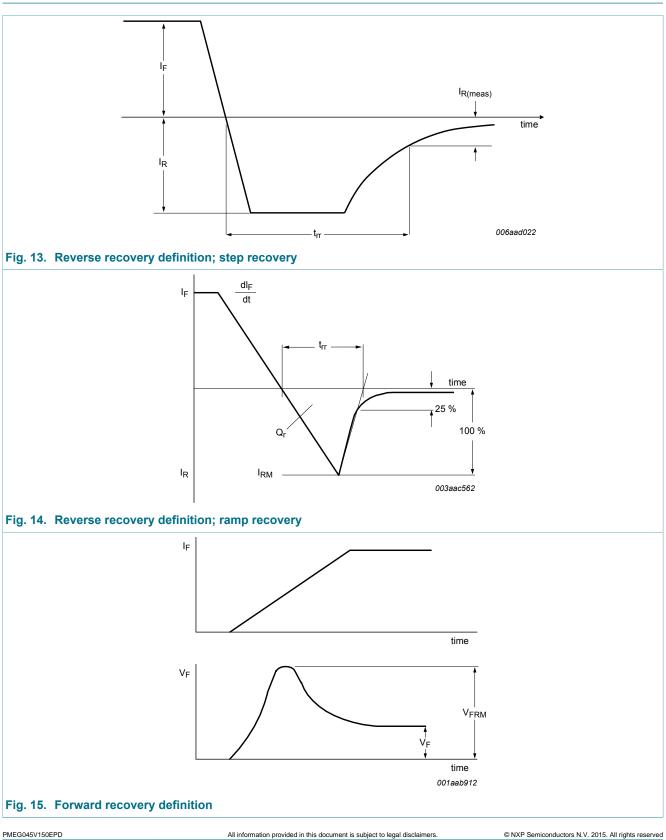


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### 11. Test information

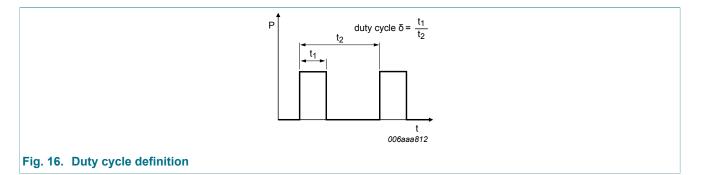




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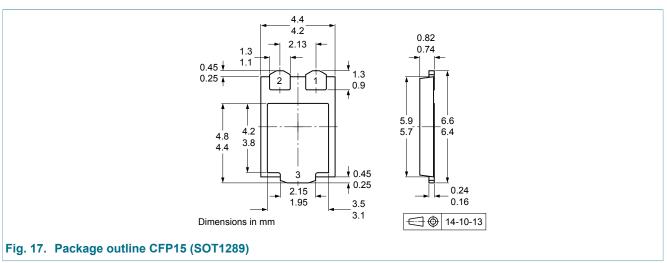


The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

#### 11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.





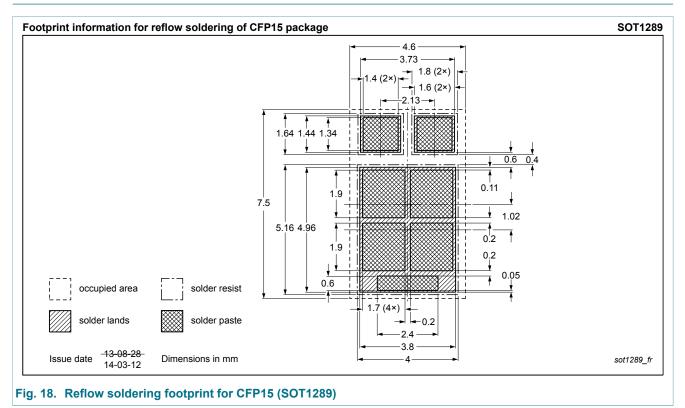


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## 13. Soldering





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### 14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG045V150EPD v.4	20150122	Product data sheet	-	PMEG045V150EPD v.3
Modifications:	Table characteristic	s: corrected t <sub>rr</sub> value		
PMEG045V150EPD v.3	20150121	Product data sheet	-	PMEG045V150EPD v.2
PMEG045V150EPD v.2	20140704	Preliminary data sheet	-	PMEG045V150EPD v.1
PMEG045V150EPD v.1	20140519	Objective data sheet	-	-

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## 15. Legal information

#### **15.1 Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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