

## Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

[NXP Semiconductors/Freescale Semiconductor, Inc.](#)  
[PBSS4240ZF](#)

For any questions, you can email us directly:

[sales@integrated-circuit.com](mailto:sales@integrated-circuit.com)



# PBSS4240Z

40 V, 2 A NPN low V<sub>CEsat</sub> (BISS) transistor

16 October 2014

Product data sheet

## 1. General description

NPN low V<sub>CEsat</sub> Breakthrough In Small Signal (BISS) transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5240Z

## 2. Features and benefits

- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- High energy efficiency due to less heat generation
- AEC-Q101 qualified

## 3. Applications

- DC-to-DC conversion
- Supply line switching
- Battery charger
- LCD backlighting
- Driver in low supply voltage applications (e.g. lamps and LEDs)
- Inductive load driver (e.g. relays, buzzers and motors)

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	40	V
I <sub>C</sub>	collector current		-	-	2	A
I <sub>CM</sub>	peak collector current	t <sub>p</sub> ≤ 1 ms; single pulse	-	-	3	A
R <sub>CEsat</sub>	collector-emitter saturation resistance	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; pulsed; t <sub>p</sub> ≤ 300 μs; δ ≤ 0.02; T <sub>amb</sub> = 25 °C	-	-	275	mΩ



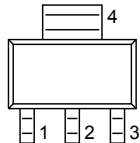
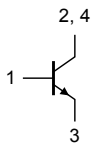
## NXP Semiconductors

## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

### 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 <p><b>SC-73 (SOT223)</b></p>	 <p><i>sym016</i></p>
2	C	collector		
3	E	emitter		
4	C	collector		

### 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4240Z	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223

### 7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4240Z	S4240Z

## NXP Semiconductors

## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

## 8. Limiting values

**Table 5. Limiting values**

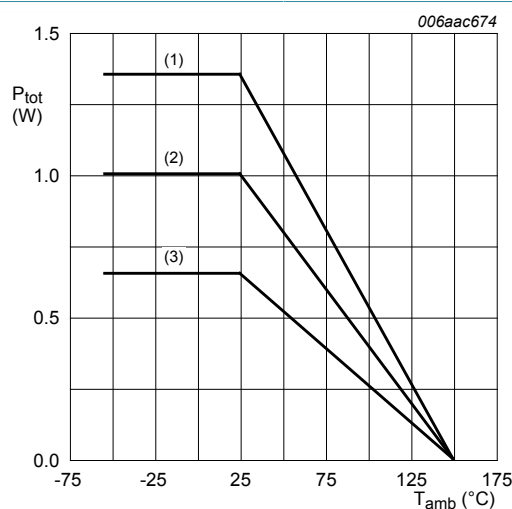
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	40	V
$V_{CEO}$	collector-emitter voltage	open base		-	40	V
$V_{EBO}$	emitter-base voltage	open collector		-	7	V
$I_C$	collector current			-	2	A
$I_{CM}$	peak collector current	$t_p \leq 1$ ms; single pulse		-	3	A
$I_B$	base current			-	300	mA
$I_{BM}$	peak base current	$t_p \leq 1$ ms; single pulse		-	1	A
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	0.65	W
			[2]	-	1	W
			[3]	-	1.35	W
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-55	150	°C
$T_{stg}$	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



(1) FR4 PCB, mounting pad for collector 6 cm<sup>2</sup>

(2) FR4 PCB, mounting pad for collector 1 cm<sup>2</sup>

(3) FR4 PCB, standard footprint

**Fig. 1. Power derating curves**

## NXP Semiconductors

## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

### 9. Thermal characteristics

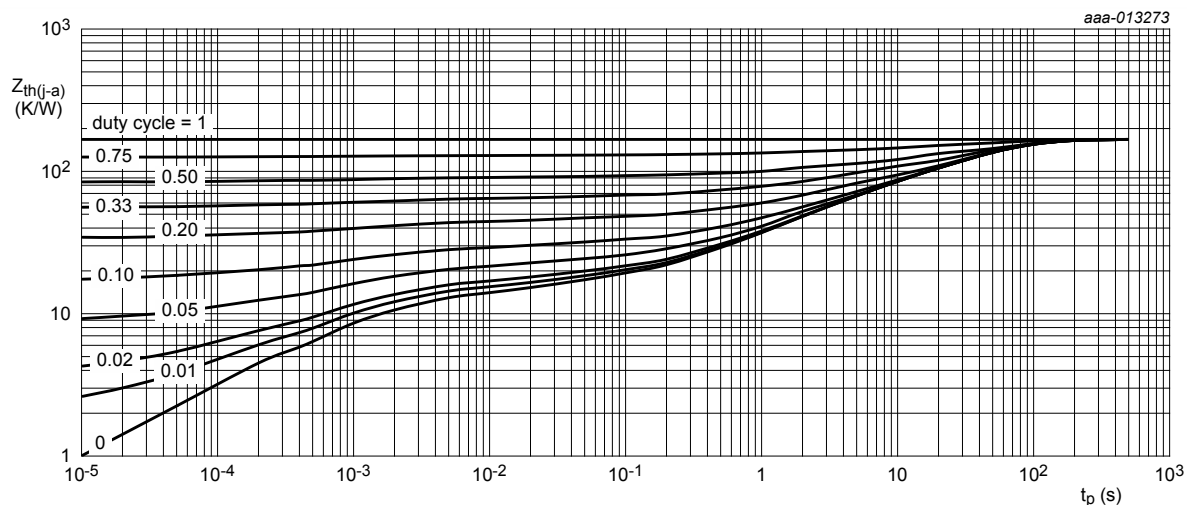
**Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]	-	-	125	K/W
			[3]	-	-	93	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



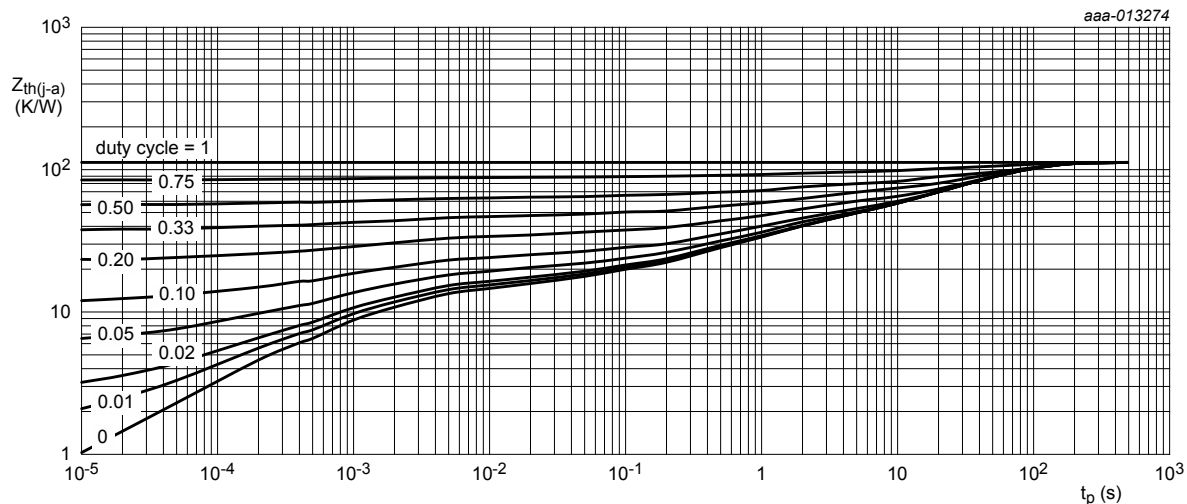
FR4 PCB, single-sided copper, tin-plated and standard footprint.

**Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**

**NXP Semiconductors**

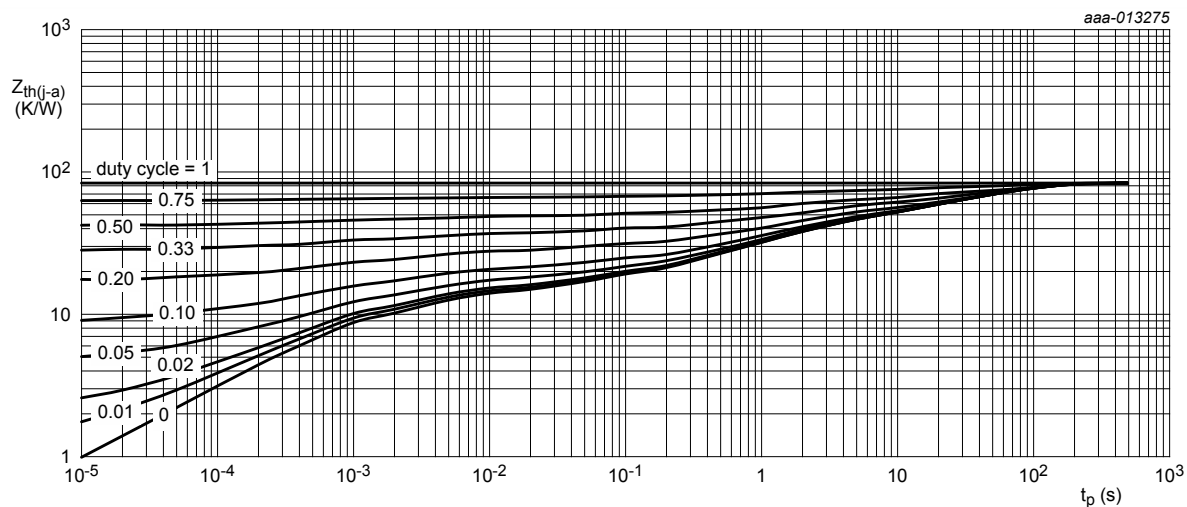
**PBSS4240Z**

40 V, 2 A NPN low VCEsat (BISS) transistor



FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

**Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**



FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

**Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values**

## NXP Semiconductors

## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

### 10. Characteristics

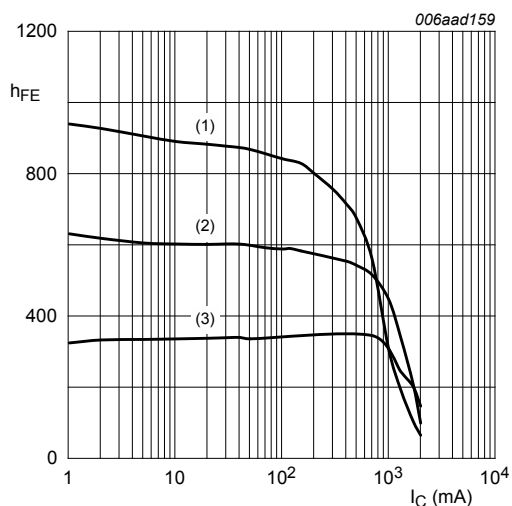
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$I_{CBO}$	collector-base cut-off current	$V_{CB} = 32 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	100	nA
		$V_{CB} = 32 \text{ V}; I_E = 0 \text{ A}; T_j = 150 \text{ }^{\circ}\text{C}$	-	-	50	$\mu\text{A}$
$I_{CES}$	collector-emitter cut-off current	$V_{CE} = 32 \text{ V}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	100	nA
$I_{EBO}$	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	100	nA
$h_{FE}$	DC current gain	$V_{CE} = 5 \text{ V}; I_C = 1 \text{ mA}; T_{amb} = 25 \text{ }^{\circ}\text{C}$	300	-	-	
		$V_{CE} = 5 \text{ V}; I_C = 500 \text{ mA}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	300	-	-	
		$V_{CE} = 5 \text{ V}; I_C = 1 \text{ A}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	200	-	-	
		$V_{CE} = 5 \text{ V}; I_C = 2 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	75	-	-	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 100 \text{ mA}; I_B = 1 \text{ mA}; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	80	mV
		$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	150	mV
		$I_C = 1 \text{ A}; I_B = 100 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	275	mV
		$I_C = 2 \text{ A}; I_B = 200 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	550	mV
$R_{CEsat}$	collector-emitter saturation resistance	$I_C = 1 \text{ A}; I_B = 100 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	275	m $\Omega$
$V_{BEsat}$	base-emitter saturation voltage	$I_C = 1 \text{ A}; I_B = 100 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	1.2	V
$V_{BEon}$	base-emitter turn-on voltage	$V_{CE} = 5 \text{ V}; I_C = 1 \text{ A}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	1.1	V
$f_T$	transition frequency	$V_{CE} = 10 \text{ V}; I_C = 50 \text{ mA}; f = 100 \text{ MHz}; T_{amb} = 25 \text{ }^{\circ}\text{C}$	150	-	-	MHz
$C_c$	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	10	pF

## NXP Semiconductors

## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor



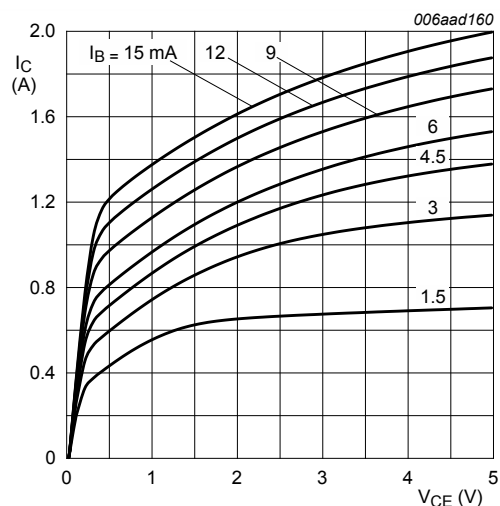
$V_{CE} = 5 \text{ V}$

(1)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$

(2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$

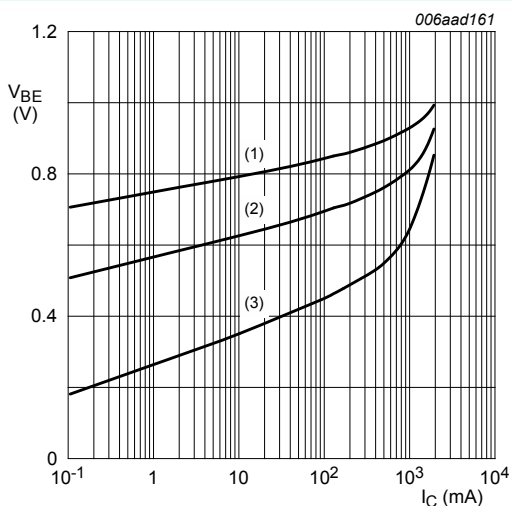
(3)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

**Fig. 5. DC current gain as a function of collector current; typical values**



$T_{amb} = 25 \text{ }^{\circ}\text{C}$

**Fig. 6. Collector current as a function of collector-emitter voltage; typical values**



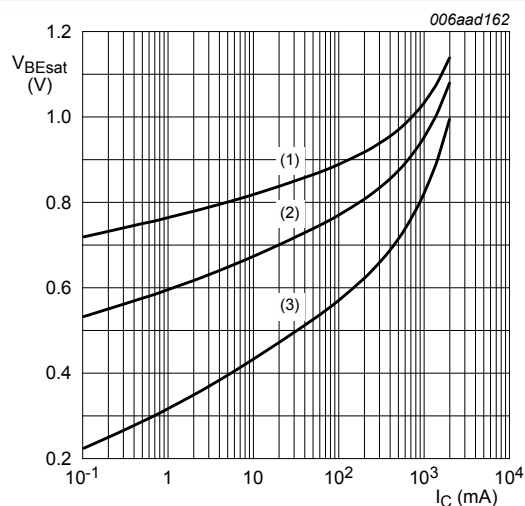
$V_{CE} = 5 \text{ V}$

(1)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

(2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$

(3)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$

**Fig. 7. Base-emitter voltage as a function of collector current; typical values**



$I_C/I_B = 20$

(1)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

(2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$

(3)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$

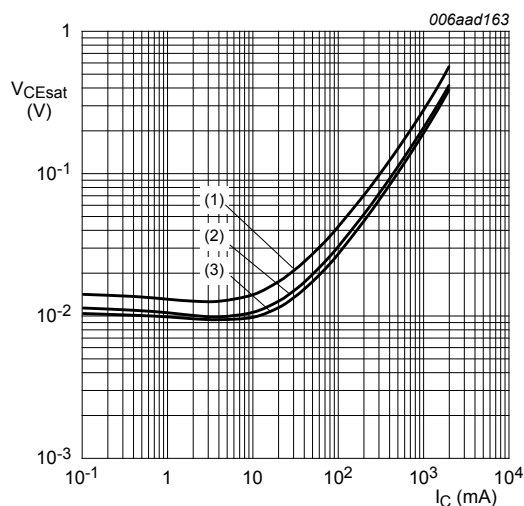
**Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values**



## NXP Semiconductors

## PBSS4240Z

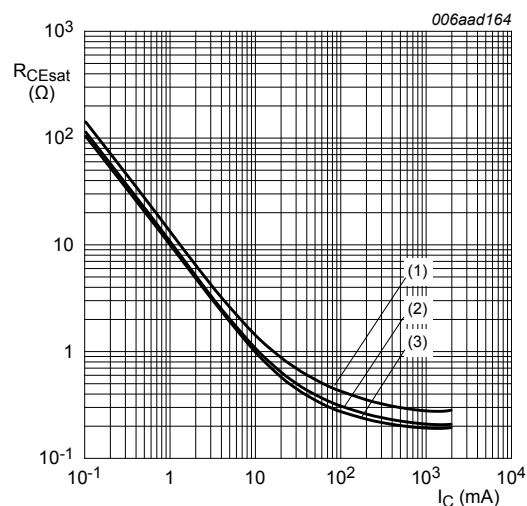
40 V, 2 A NPN low VCEsat (BISS) transistor



$$I_C/I_B = 20$$

- (1)  $T_{amb} = 150\text{ }^{\circ}\text{C}$
- (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$

**Fig. 9.** Collector-emitter saturation voltage as a function of collector current; typical values



$$I_C/I_B = 20$$

- (1)  $T_{amb} = 150\text{ }^{\circ}\text{C}$
- (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$

**Fig. 10.** Collector-emitter saturation resistance as a function of collector current; typical values

## 11. Test information

### 11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

## NXP Semiconductors

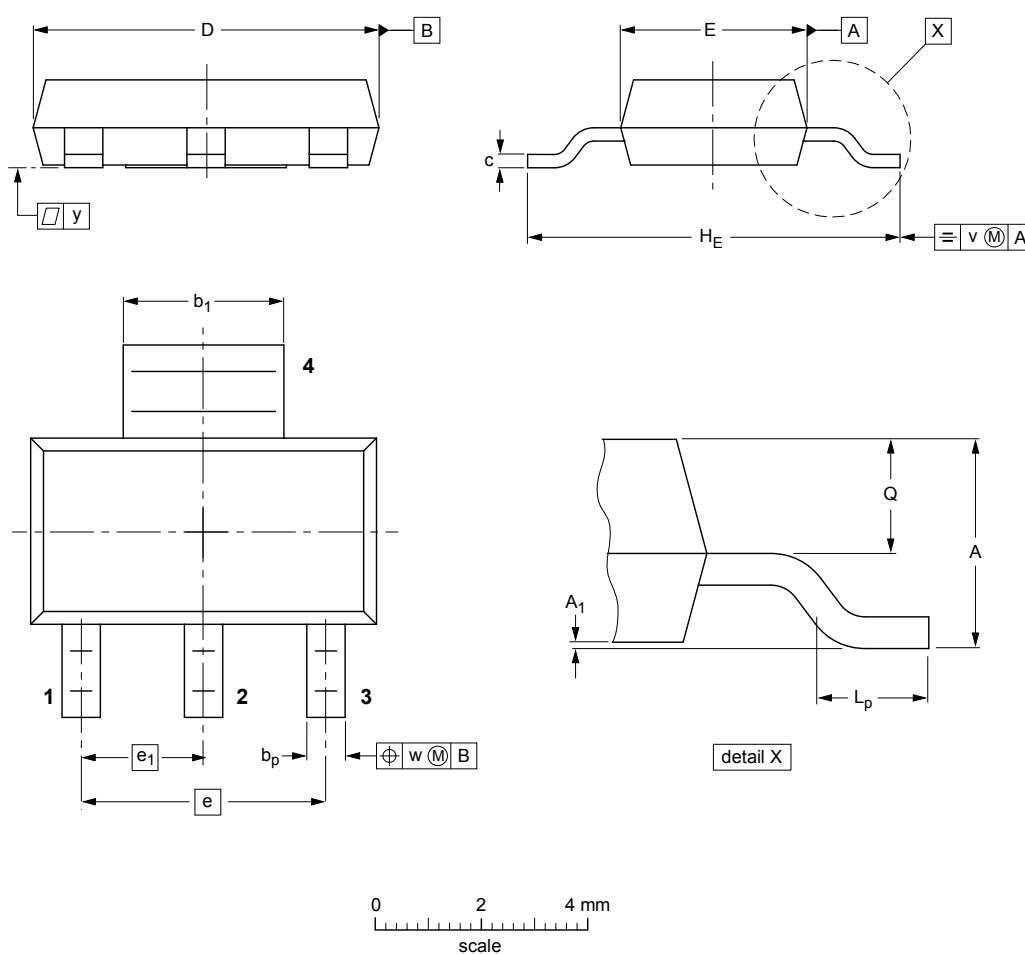
## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

### 12. Package outline

Plastic surface-mounted package with increased heatsink; 4 leads

SOT223



DIMENSIONS (mm are the original dimensions)

UNIT	A	A <sub>1</sub>	b <sub>p</sub>	b <sub>1</sub>	c	D	E	e	e <sub>1</sub>	H <sub>E</sub>	L <sub>p</sub>	Q	v	w	y
mm	1.8 1.5	0.10 0.01	0.80 0.60	3.1 2.9	0.32 0.22	6.7 6.3	3.7 3.3	4.6	2.3	7.3 6.7	1.1 0.7	0.95 0.85	0.2	0.1	0.1

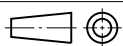
OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT223			SC-73			04-11-10 06-03-16

Fig. 11. Package outline SC-73 (SOT223)

## NXP Semiconductors

## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

### 13. Soldering

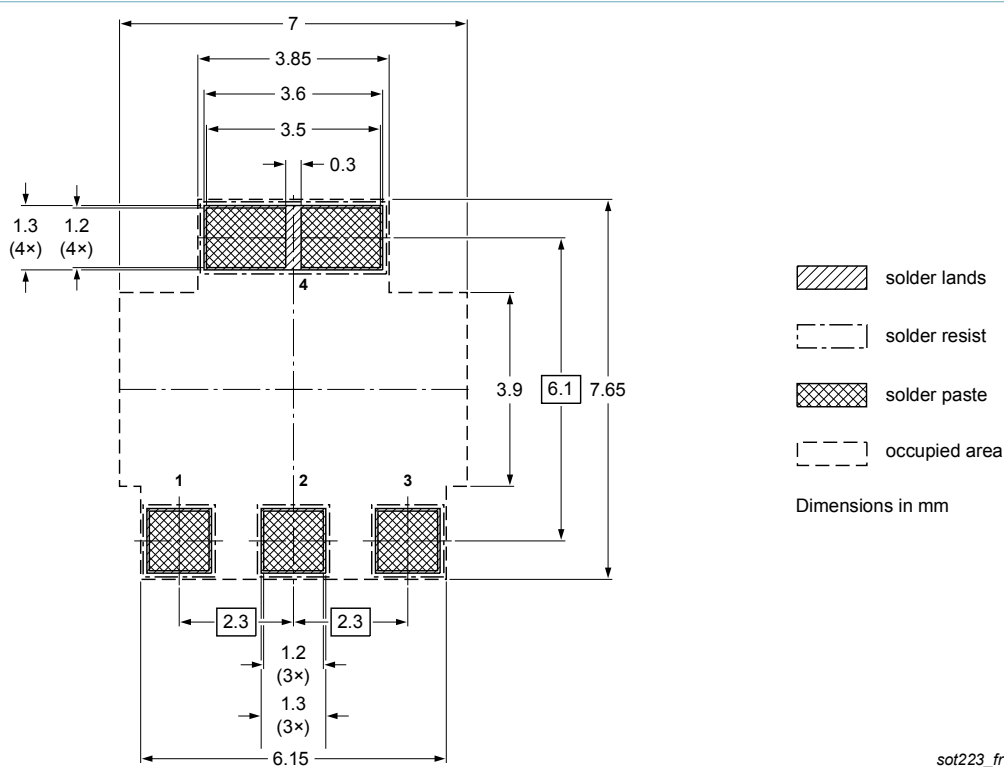


Fig. 12. Reflow soldering footprint for SC-73 (SOT223)

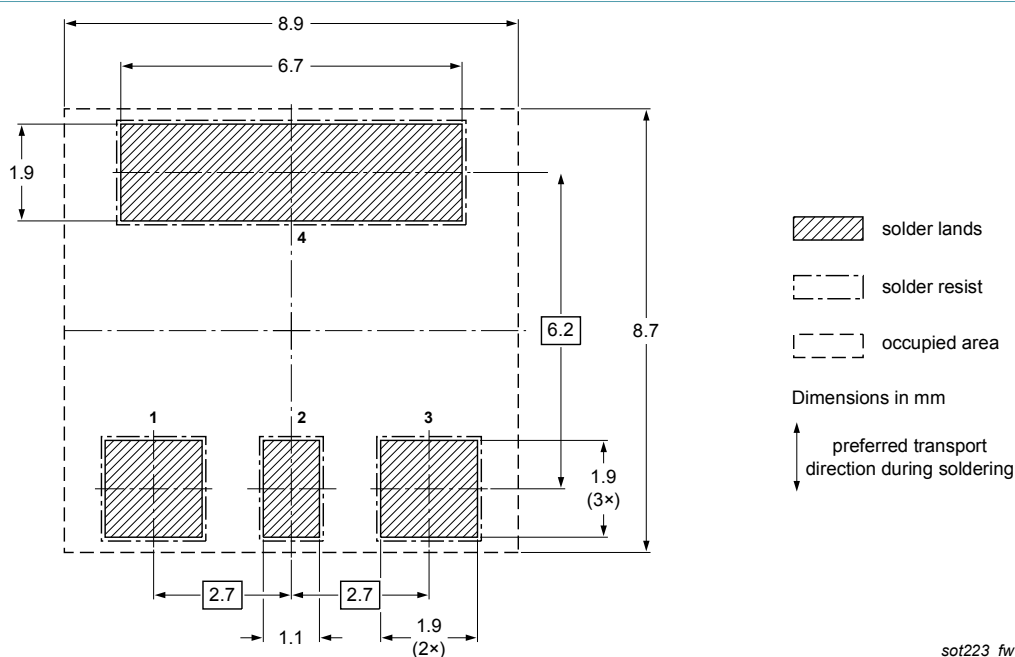


Fig. 13. Wave soldering footprint for SC-73 (SOT223)

NXP Semiconductors

PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

## 14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4240Z v. 1	20141016	Product data sheet	-	-

## NXP Semiconductors

## PBSS4240Z

### 40 V, 2 A NPN low VCEsat (BISS) transistor

## 15. Legal information

### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 15.2 Definitions

**Preview** — The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### 15.3 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use in automotive applications** — This NXP Semiconductors product has been qualified for use in automotive applications. Unless otherwise agreed in writing, the product is not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

## NXP Semiconductors

## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

### 15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

**BitSound, CoolFlux, CoReUse, DESFire, FabKey, GreenChip, HiPerSmart, HITAG, I<sup>2</sup>C-bus logo, ICODE, I-CODE, ITEC, MIFARE, MIFARE Plus, MIFARE Ultralight, SmartXA, STARplug, TOPFET, TrenchMOS, TriMedia and UCODE** — are trademarks of NXP Semiconductors N.V.

**HD Radio** and **HD Radio** logo — are trademarks of iBiquity Digital Corporation.

## NXP Semiconductors

## PBSS4240Z

40 V, 2 A NPN low VCEsat (BISS) transistor

## 16. Contents

1	General description .....	1
2	Features and benefits .....	1
3	Applications .....	1
4	Quick reference data .....	1
5	Pinning information .....	2
6	Ordering information .....	2
7	Marking .....	2
8	Limiting values .....	3
9	Thermal characteristics .....	4
10	Characteristics .....	6
11	Test information .....	8
11.1	Quality information .....	8
12	Package outline .....	9
13	Soldering .....	10
14	Revision history .....	11
15	Legal information .....	12
15.1	Data sheet status .....	12
15.2	Definitions .....	12
15.3	Disclaimers .....	12
15.4	Trademarks .....	13

© NXP Semiconductors N.V. 2014. All rights reserved

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 16 October 2014