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**CMPT5086
CMPT5087**

**SURFACE MOUNT
PNP SILICON TRANSISTOR**



SOT-23 CASE



www.centrasemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPT5086 and CMPT5087 are PNP silicon transistors manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for applications requiring high gain and low noise.

**MARKING CODES: CMPT5086: C2P
CMPT5087: C2Q**

MAXIMUM RATINGS: (T_A=25°C)

Collector-Base Voltage
Collector-Emitter Voltage
Emitter-Base Voltage
Continuous Collector Current
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

SYMBOL

V_{CBO} 50
V_{CEO} 50
V_{EBO} 3.0
I_C 50
P_D 350
T_J, T_{stg} -65 to +150
θ_{JA} 357

UNITS

V
V
V
mA
mW
°C
°C/W

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	CMPT5086		CMPT5087		UNITS
		MIN	MAX	MIN	MAX	
I _{CBO}	V _{CB} =10V	-	10	-	10	nA
I _{CBO}	V _{CB} =35V	-	50	-	50	nA
BV _{CBO}	I _C =100μA	50	-	50	-	V
BV _{CEO}	I _C =1.0mA	50	-	50	-	V
BV _{EBO}	I _E =100μA	3.0	-	3.0	-	V
V _{CE(SAT)}	I _C =10mA, I _B =1.0mA	-	0.30	-	0.30	V
V _{BE(SAT)}	I _C =10mA, I _B =1.0mA	-	0.85	-	0.85	V
h _{FE}	V _{CE} =5.0V, I _C =0.1mA	150	500	250	800	
h _{FE}	V _{CE} =5.0V, I _C =1.0mA	150	-	250	-	
h _{FE}	V _{CE} =5.0V, I _C =10mA	150	-	250	-	
f _T	V _{CE} =5.0V, I _C =500μA, f=20MHz	40	-	40	-	MHz
C _{ob}	V _{CB} =5.0V, I _E =0, f=1.0MHz	-	4.0	-	4.0	pF
h _{fe}	V _{CE} =5.0V, I _C =1.0mA, f=1.0kHz	150	600	250	900	
NF	V _{CE} =5.0V, I _C =20mA, R _S =10kΩ, f=10Hz to 15.7kHz	-	3.0	-	2.0	dB
NF	V _{CE} =5.0V, I _C =100μA, R _S =3.0kΩ, f=1.0kHz	-	3.0	-	2.0	dB

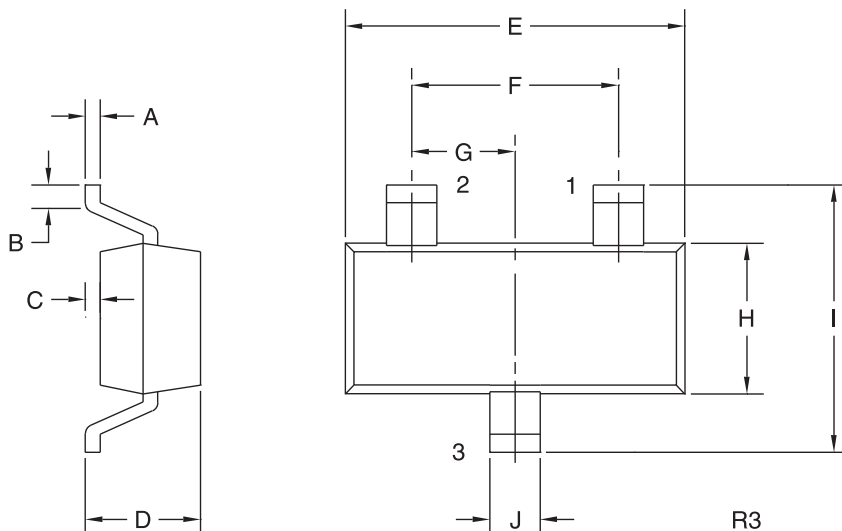
R5 (1-February 2010)

**CMPT5086
CMPT5087**

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SOT-23 CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Base
- 2) Emitter
- 3) Collector

MARKING CODES:

CMPT5086: C2P
CMPT5087: C2Q

DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.003	0.007	0.08	0.18
B	0.006	-	0.15	-
C	-	0.005	-	0.13
D	0.035	0.043	0.89	1.09
E	0.110	0.120	2.80	3.05
F	0.075		1.90	
G	0.037		0.95	
H	0.047	0.055	1.19	1.40
I	0.083	0.098	2.10	2.49
J	0.014	0.020	0.35	0.50

SOT-23 (REV: R3)

R5 (1-February 2010)

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- Inventory bonding
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- Custom bar coding for shipments
- Custom product packing

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- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.
145 Adams Avenue
Hauppauge, NY 11788 USA
Main Tel: (631) 435-1110
Main Fax: (631) 435-1824
Support Team Fax: (631) 435-3388
www.centra-semi.com

Worldwide Field Representatives:

www.centra-semi.com/wwreps

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