

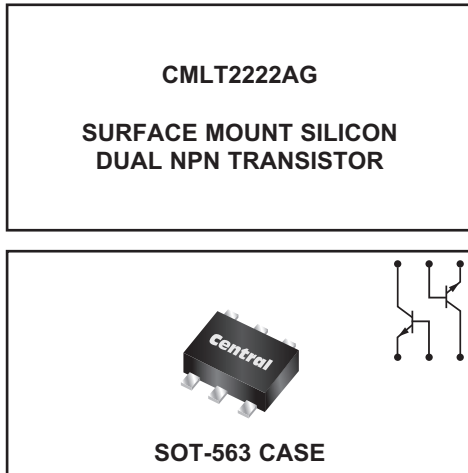
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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT2222AG consists of two (2) isolated 2222A NPN silicon transistors, manufactured by the epitaxial planar process and epoxy molded in an SOT-563 surface mount package. These devices have been designed for small signal general purpose and switching applications.

MARKING CODE: 2CG

• Device is **Halogen Free** by design

MAXIMUM RATINGS: (T_A=25°C)

Collector-Base Voltage	V _{CB0}	75	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Base Voltage	V _{EBO}	6.0	V
Continuous Collector Current	I _C	600	mA
Power Dissipation (Note 1)	P _D	350	mW
Power Dissipation (Note 2)	P _D	300	mW
Power Dissipation (Note 3)	P _D	150	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	θ _{JA}	357	°C/W

SYMBOL

V _{CB0}	75	V
V _{CEO}	40	V
V _{EBO}	6.0	V
I _C	600	mA
P _D	350	mW
P _D	300	mW
P _D	150	mW
T _J , T _{stg}	-65 to +150	°C
θ _{JA}	357	°C/W

UNITS

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CB0}	V _{CB} =60V		10	nA
I _{CB0}	V _{CB} =60V, T _A =125°C		10	µA
I _{CEV}	V _{CE} =60V, V _{EB} =3.0V		10	nA
I _{EBO}	V _{EB} =3.0V		10	nA
BV _{CB0}	I _C =10µA	75		V
BV _{CEO}	I _C =10mA	40		V
BV _{EBO}	I _E =10µA	6.0		V
V _{CE(SAT)}	I _C =150mA, I _B =15mA		0.3	V
V _{CE(SAT)}	I _C =500mA, I _B =50mA		1.0	V
V _{BE(SAT)}	I _C =150mA, I _B =15mA	0.6	1.2	V
V _{BE(SAT)}	I _C =500mA, I _B =50mA		2.0	V
h _{FE}	V _{CE} =10V, I _C =0.1mA	35		
h _{FE}	V _{CE} =10V, I _C =1.0mA	50		
h _{FE}	V _{CE} =10V, I _C =10mA	75		
h _{FE}	V _{CE} =1.0V, I _C =150mA	50		
h _{FE}	V _{CE} =10V, I _C =150mA	100	300	
h _{FE}	V _{CE} =10V, I _C =500mA	40		

Notes: (1) Ceramic or aluminum core PC Board with copper mounting pad area of 4.0mm²
 (2) FR-4 Epoxy PC Board with copper mounting pad area of 4.0mm²
 (3) FR-4 Epoxy PC Board with copper mounting pad area of 1.4mm²

R5 (29-June 2015)

CMLT2222AG

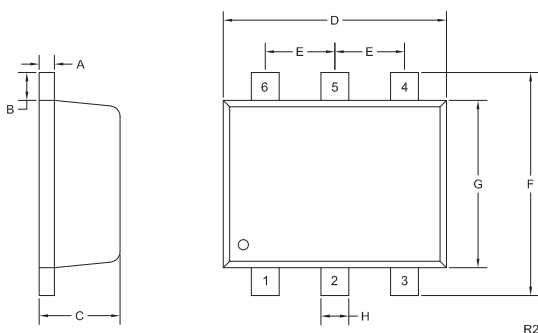
SURFACE MOUNT SILICON
DUAL NPN TRANSISTOR



ELECTRICAL CHARACTERISTICS PER TRANSISTOR - Continued: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
f_T	$V_{CE}=20\text{V}, I_C=20\text{mA}, f=100\text{MHz}$	300		MHz
C_{ob}	$V_{CB}=10\text{V}, I_E=0, f=1.0\text{MHz}$		8.0	pF
C_{ib}	$V_{EB}=0.5\text{V}, I_C=0, f=1.0\text{MHz}$		25	pF
h_{ie}	$V_{CE}=10\text{V}, I_C=1.0\text{mA}, f=1.0\text{kHz}$	2.0	8.0	k Ω
h_{ie}	$V_{CE}=10\text{V}, I_C=10\text{mA}, f=1.0\text{kHz}$	0.25	1.25	k Ω
h_{re}	$V_{CE}=10\text{V}, I_C=1.0\text{mA}, f=1.0\text{kHz}$		8.0	$\times 10^{-4}$
h_{re}	$V_{CE}=10\text{V}, I_C=10\text{mA}, f=1.0\text{kHz}$		4.0	$\times 10^{-4}$
h_{fe}	$V_{CE}=10\text{V}, I_C=1.0\text{mA}, f=1.0\text{kHz}$	50	300	
h_{fe}	$V_{CE}=10\text{V}, I_C=10\text{mA}, f=1.0\text{kHz}$	75	375	
h_{oe}	$V_{CE}=10\text{V}, I_C=1.0\text{mA}, f=1.0\text{kHz}$	5.0	35	μS
h_{oe}	$V_{CE}=10\text{V}, I_C=10\text{mA}, f=1.0\text{kHz}$	25	200	μS
$r_b' C_C$	$V_{CB}=10\text{V}, I_E=20\text{mA}, f=31.8\text{MHz}$		150	ps
NF	$V_{CE}=10\text{V}, I_C=100\text{mA}, R_S=1.0\text{k}\Omega, f=1.0\text{kHz}$		4.0	dB
t_d	$V_{CC}=30\text{V}, V_{BE}=0.5, I_C=150\text{mA}, I_{B1}=15\text{mA}$		10	ns
t_r	$V_{CC}=30\text{V}, V_{BE}=0.5, I_C=150\text{mA}, I_{B1}=15\text{mA}$		25	ns
t_s	$V_{CC}=30\text{V}, I_C=150\text{mA}, I_{B1}=I_{B2}=15\text{mA}$		225	ns
t_f	$V_{CC}=30\text{V}, I_C=150\text{mA}, I_{B1}=I_{B2}=15\text{mA}$		60	ns

SOT-563 CASE - MECHANICAL OUTLINE



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.0027	0.007	0.07	0.18
B	0.008		0.20	
C	0.017	0.024	0.45	0.60
D	0.059	0.067	1.50	1.70
E	0.020		0.50	
F	0.059	0.067	1.50	1.70
G	0.043	0.051	1.10	1.30
H	0.006	0.012	0.15	0.30

SOT-563 (REV: R2)

LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

MARKING CODE: 2CG

R5 (29-June 2015)

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