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[Central Semiconductor Corp](#)
[CMXT2907A TR](#)

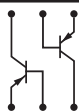
For any questions, you can email us directly:

sales@integrated-circuit.com

CMXT2907A

**SURFACE MOUNT
DUAL PNP
SILICON TRANSISTORS**

SUPERmini™



SOT-26 CASE



www.centrasemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMXT2907A type is a dual PNP silicon transistor manufactured by the epitaxial planar process, epoxy molded in a SUPERmini™ surface mount package, and designed for small signal general purpose and switching applications.

MARKING CODE: X2F

MAXIMUM RATINGS: (T_A=25°C)

Collector-Base Voltage
Collector-Emitter Voltage
Emitter-Base Voltage
Continuous Collector Current
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

SYMBOL

V_{CB0} 60
V_{CEO} 60
V_{EBO} 5.0
I_C 600
P_D 350
T_J, T_{stg} -65 to +150
θ_{JA} 357

UNITS

V
V
V
mA
mW
°C
°C/W

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: (T_A=25°C unless otherwise noted)

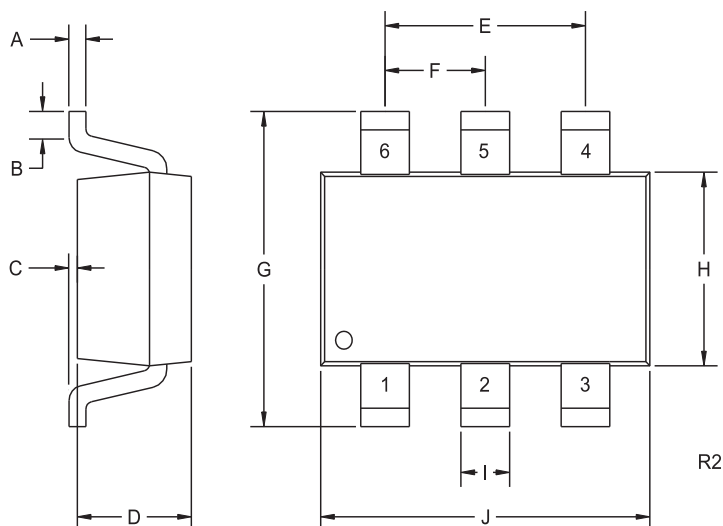
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =50V		10	nA
I _{CBO}	V _{CB} =50V, T _A =125°C		10	μA
I _{CEV}	V _{CE} =30V, V _{BE} =0.5V		50	nA
BV _{CBO}	I _C =10μA	60		V
BV _{CEO}	I _C =10mA	60		V
BV _{EBO}	I _E =10μA	5.0		V
V _{CE(SAT)}	I _C =150mA, I _B =15mA		0.4	V
V _{CE(SAT)}	I _C =500mA, I _B =50mA		1.6	V
V _{BE(SAT)}	I _C =150mA, I _B =15mA		1.3	V
V _{BE(SAT)}	I _C =500mA, I _B =50mA		2.6	V
h _{FE}	V _{CE} =10V, I _C =0.1mA	75		
h _{FE}	V _{CE} =10V, I _C =1.0mA	100		
h _{FE}	V _{CE} =10V, I _C =10mA	100		
h _{FE}	V _{CE} =10V, I _C =150mA	100	300	
h _{FE}	V _{CE} =10V, I _C =500mA	50		
f _T	V _{CE} =20V, I _C =50mA, f=100MHz	200		MHz
C _{ob}	V _{CB} =10V, I _E =0, f=1.0MHz		8.0	pF
C _{ib}	V _{BE} =2.0V, I _C =0, f=1.0MHz		30	pF
t _{on}	V _{CC} =30V, V _{BE} =0.5V, I _C =150mA, I _{B1} =15mA	45	ns	
t _d	V _{CC} =30V, V _{BE} =0.5V, I _C =150mA, I _{B1} =15mA	10	ns	
t _r	V _{CC} =30V, V _{BE} =0.5V, I _C =150mA, I _{B1} =15mA	40	ns	
t _{off}	V _{CC} =6.0V, I _C =150mA, I _{B1} =I _{B2} =15mA		100	ns
t _s	V _{CC} =6.0V, I _C =150mA, I _{B1} =I _{B2} =15mA		80	ns
t _f	V _{CC} =6.0V, I _C =150mA, I _{B1} =I _{B2} =15mA		30	ns

R3 (12-February 2010)

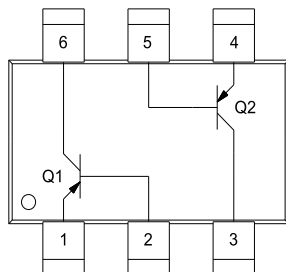
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SOT-26 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

MARKING CODE: X2F

SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.004	0.007	0.11	0.19
B	0.016	-	0.40	-
C	-	0.004	-	0.10
D	0.039	0.047	1.00	1.20
E	0.074	0.075	1.88	1.92
F	0.037	0.038	0.93	0.97
G	0.102	0.118	2.60	3.00
H	0.059	0.067	1.50	1.70
I	0.016		0.41	
J	0.110	0.118	2.80	3.00

SOT-26 (REV: R2)

R3 (12-February 2010)

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- Custom product packing

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- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
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- Application notes
- Application and design sample kits
- Custom product and package development

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