Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

<u>Vishay Semiconductor/Opto Division</u> <u>VSMY12850</u>

For any questions, you can email us directly: sales@integrated-circuit.com

Distributor of Vishay Semiconductor/Opto Division: Excellent Integrated System LimitedDatasheet of VSMY12850 - EMITTER IR 850NM 70MA SMD

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VSMY12850

RoHS

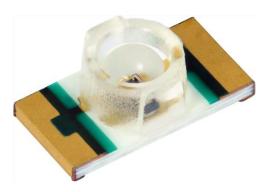
COMPLIANT

HALOGEN FREE

GREEN

Vishay Semiconductors

High Speed Infrared Emitting Diodes, 850 nm, Surface Emitter Technology



FEATURES

- Package type: surface mount
- · Package form: top view
- Dimensions (L x W x H in mm): 3.2 x 1.6 x 1.1
- Peak wavelength: λ_p = 850 nm
- · High reliability
- High radiant power
- · Very high radiant intensity
- Angle of half intensity: $\phi = \pm 40^{\circ}$
- · Suitable for high pulse current operation
- Floor life: 168 h, MSL 3, according to J-STD-020
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

As part of the <u>SurfLightTM</u> portfolio, the VSMY12850 is an infrared, 850 nm, top looking emitting diode based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, molded in clear, untinted PCB based package (with inner lens) for surface mounting (SMD).

APPLICATIONS

- · Emitter for proximity applications
- · IR touch panels
- Photointerrupters
- Optical switch

PRODUCT SUMMARY					
COMPONENT	I _e (mW/sr)	φ (deg)	λ _p (nm)	t _r (ns)	
VSMY12850	16	± 40	850	10	

Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY12850	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	Top view	

Note

MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	5	V	
Forward current		I _F	70	mA	
Surge forward current	t _p = 100 μs	I _{FSM}	1	Α	
Power dissipation		P _V	140	mW	
Junction temperature		T _j	100	°C	
Operating temperature range		T _{amb}	-40 to +85	°C	
Storage temperature range		T _{stg}	-40 to +100	°C	
Soldering temperature	acc. figure 10, J-STD-020	T _{sd}	260	°C	
Thermal resistance junction/ambient	J-STD-051, soldered on PCB	R _{thJA}	390	K/W	

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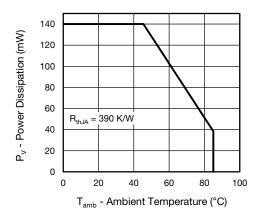


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

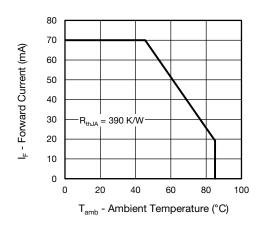


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	1.1	1.4	1.9	V
Forward voltage	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	V _F		1.65		V
	$I_F = 1 \text{ A}, t_p = 100 \mu \text{s}$	V _F		2.9		V
Temperature coefficient of V _F	I _F = 20 mA	TK _{VF}		-1.7		mV/K
Reverse current		I _R not designed		ed for reverse operation		μΑ
Junction capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz, E} = 0 \text{ mW/cm}^2$	СЈ		5		рF
	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	l _e	2.3	4.7		mW/sr
Radiant intensity	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	l _e		16		mW/sr
	$I_F = 1 \text{ A}, t_p = 100 \mu \text{s}$	l _e		130		mW/sr
Radiant power	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	фe		40		mW
Temperature coefficient of radiant power	I _F = 20 mA	TKφ _e		-0.19		%/K
Angle of half intensity		φ		± 40		deg
Peak wavelength	I _F = 20 mA	λ_{p}	830	850	870	nm
Spectral bandwidth	I _F = 20 mA	Δλ		35		nm
Temperature coefficient of λ _p	I _F = 20 mA	TKλ _p		0.25		nm/K
Rise time	I _F = 100 mA, 20 % to 80 %	t _r		10		ns
Fall time	I _F = 100 mA, 20 % to 80 %	t _f		10		ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

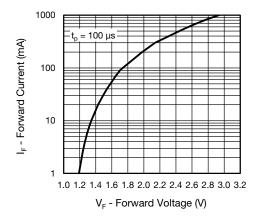


Fig. 3 - Forward Current vs. Forward Voltage

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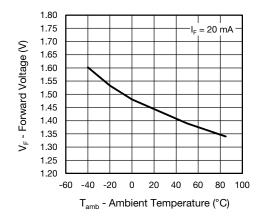


Fig. 4 - Forward Voltage vs. Ambient Temperature

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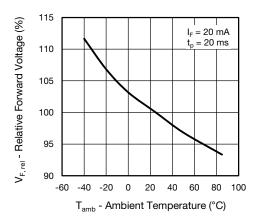


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

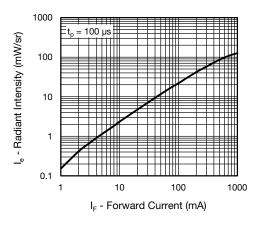


Fig. 6 - Radiant Intensity vs. Forward Current

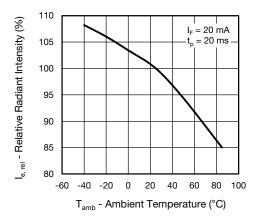


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature

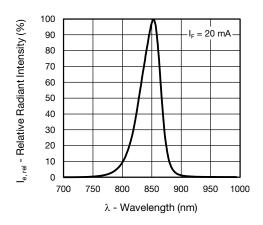


Fig. 8 - Relative Radiant Intensity vs. Wavelength

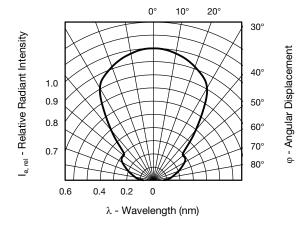


Fig. 9 - Relative Radiant Intensity vs. Angular Displacement

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SOLDER PROFILE

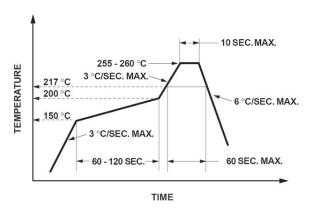


Fig. 10 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

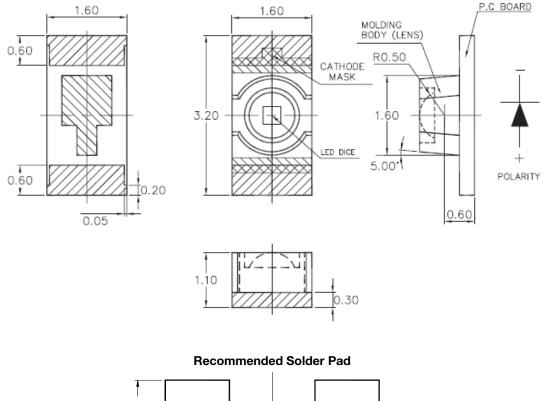
Conditions: $T_{amb} < 30$ °C, RH < 60 %

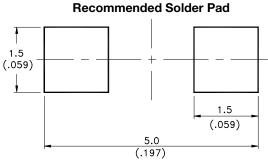
Moisture sensitivity level 3, according to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

PACKAGE DIMENSIONS in millimeters: VSMY12850





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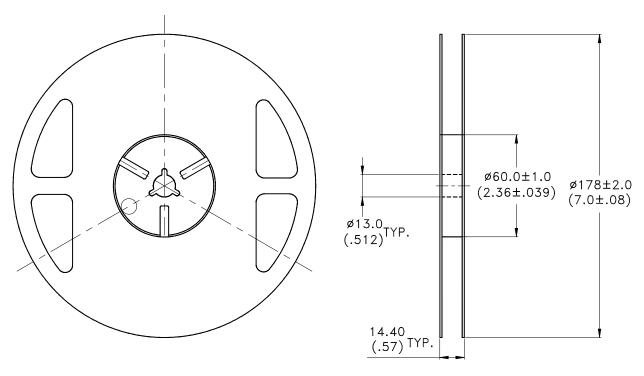
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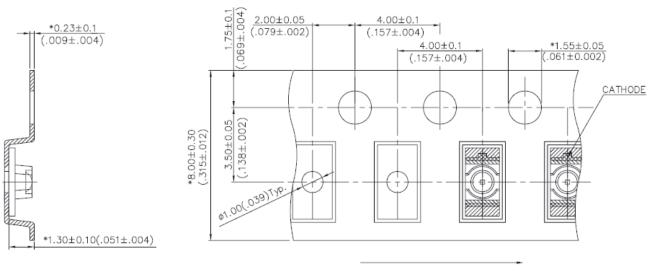


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TAPING AND REEL DIMENSIONS in millimeters: VSMY12850





User Feed Direction



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