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**IPB80N06S2L-07**

**IPP80N06S2L-07**

## OptiMOS® Power-Transistor

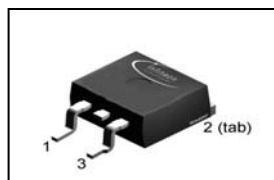
### Features

- N-channel Logic Level - Enhancement mode
- Automotive AEC Q101 qualified
- MSL1 up to 260°C peak reflow
- 175°C operating temperature
- Green package (lead free)
- Ultra low Rds(on)
- 100% Avalanche tested

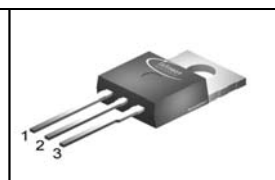
### Product Summary

|                                |     |    |
|--------------------------------|-----|----|
| $V_{DS}$                       | 55  | V  |
| $R_{DS(on),max}$ (SMD version) | 6.7 | mΩ |
| $I_D$                          | 80  | A  |

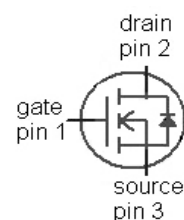
PG-TO263-3-2



PG-TO220-3-1



| Type           | Package      | Ordering Code | Marking |
|----------------|--------------|---------------|---------|
| IPB80N06S2L-07 | PG-TO263-3-2 | SP0002-18867  | 2N06L07 |
| IPP80N06S2L-07 | PG-TO220-3-1 | SP0002-18831  | 2N06L07 |



**Maximum ratings**, at  $T_j=25\text{ °C}$ , unless otherwise specified

| Parameter                                    | Symbol         | Conditions                                         | Value        | Unit |
|----------------------------------------------|----------------|----------------------------------------------------|--------------|------|
| Continuous drain current <sup>1)</sup>       | $I_D$          | $T_C=25\text{ °C}$ , $V_{GS}=10\text{ V}$          | 80           | A    |
|                                              |                | $T_C=100\text{ °C}$ ,<br>$V_{GS}=10\text{ V}^{2)}$ | 80           |      |
| Pulsed drain current <sup>2)</sup>           | $I_{D,pulse}$  | $T_C=25\text{ °C}$                                 | 320          |      |
| Avalanche energy, single pulse <sup>2)</sup> | $E_{AS}$       | $I_D=80\text{ A}$                                  | 450          | mJ   |
| Gate source voltage <sup>4)</sup>            | $V_{GS}$       |                                                    | ±20          | V    |
| Power dissipation                            | $P_{tot}$      | $T_C=25\text{ °C}$                                 | 210          | W    |
| Operating and storage temperature            | $T_j, T_{stg}$ |                                                    | -55 ... +175 | °C   |



**IPB80N06S2L-07**

**IPP80N06S2L-07**

| Parameter | Symbol | Conditions | Values |      |      | Unit |
|-----------|--------|------------|--------|------|------|------|
|           |        |            | min.   | typ. | max. |      |

#### Thermal characteristics<sup>2)</sup>

|                                                |            |                                              |   |   |     |     |
|------------------------------------------------|------------|----------------------------------------------|---|---|-----|-----|
| Thermal resistance, junction - case            | $R_{thJC}$ |                                              | - | - | 0.7 | K/W |
| Thermal resistance, junction - ambient, leaded | $R_{thJA}$ |                                              | - | - | 62  |     |
| SMD version, device on PCB                     | $R_{thJA}$ | minimal footprint                            | - | - | 62  |     |
|                                                |            | 6 cm <sup>2</sup> cooling area <sup>5)</sup> | - | - | 40  |     |

#### Electrical characteristics, at $T_j=25\text{ }^{\circ}\text{C}$ , unless otherwise specified

#### Static characteristics

|                                  |               |                                                                               |     |      |     |               |
|----------------------------------|---------------|-------------------------------------------------------------------------------|-----|------|-----|---------------|
| Drain-source breakdown voltage   | $V_{(BR)DSS}$ | $V_{GS}=0\text{ V}, I_D=1\text{ mA}$                                          | 55  | -    | -   | V             |
| Gate threshold voltage           | $V_{GS(th)}$  | $V_{DS}=V_{GS}, I_D=150\text{ }\mu\text{A}$                                   | 1.2 | 1.6  | 2.0 |               |
| Zero gate voltage drain current  | $I_{DSS}$     | $V_{DS}=55\text{ V}, V_{GS}=0\text{ V}, T_j=25\text{ }^{\circ}\text{C}$       | -   | 0.01 | 1   | $\mu\text{A}$ |
|                                  |               | $V_{DS}=55\text{ V}, V_{GS}=0\text{ V}, T_j=125\text{ }^{\circ}\text{C}^{2)}$ | -   | 1    | 100 |               |
| Gate-source leakage current      | $I_{GSS}$     | $V_{GS}=20\text{ V}, V_{DS}=0\text{ V}$                                       | -   | 1    | 100 | nA            |
| Drain-source on-state resistance | $R_{DS(on)}$  | $V_{GS}=4.5\text{ V}, I_D=60\text{ A}$                                        | -   | 7.1  | 10  | m $\Omega$    |
|                                  |               | $V_{GS}=4.5\text{ V}, I_D=60\text{ A},$<br>SMD version                        | -   | 6.8  | 9.7 |               |
| Drain-source on-state resistance | $R_{DS(on)}$  | $V_{GS}=10\text{ V}, I_D=60\text{ A},$                                        | -   | 5.6  | 7.0 | m $\Omega$    |
|                                  |               | $V_{GS}=10\text{ V}, I_D=60\text{ A},$<br>SMD version                         | -   | 5.3  | 6.7 |               |



**IPB80N06S2L-07**

**IPP80N06S2L-07**

| Parameter | Symbol | Conditions | Values |      |      | Unit |
|-----------|--------|------------|--------|------|------|------|
|           |        |            | min.   | typ. | max. |      |

#### Dynamic characteristics<sup>2)</sup>

|                              |              |                                                                          |   |      |   |    |
|------------------------------|--------------|--------------------------------------------------------------------------|---|------|---|----|
| Input capacitance            | $C_{iss}$    | $V_{GS}=0\text{ V}, V_{DS}=25\text{ V}, f=1\text{ MHz}$                  | - | 3160 | - | pF |
| Output capacitance           | $C_{oss}$    |                                                                          | - | 740  | - |    |
| Reverse transfer capacitance | $C_{rss}$    |                                                                          | - | 210  | - |    |
| Turn-on delay time           | $t_{d(on)}$  | $V_{DD}=30\text{ V}, V_{GS}=10\text{ V}, I_D=80\text{ A}, R_G=2\ \Omega$ | - | 18   | - | ns |
| Rise time                    | $t_r$        |                                                                          | - | 35   | - |    |
| Turn-off delay time          | $t_{d(off)}$ |                                                                          | - | 28   | - |    |
| Fall time                    | $t_f$        |                                                                          | - | 31   | - |    |

#### Gate Charge Characteristics<sup>2)</sup>

|                       |               |                                                                       |   |     |     |    |
|-----------------------|---------------|-----------------------------------------------------------------------|---|-----|-----|----|
| Gate to source charge | $Q_{gs}$      | $V_{DD}=44\text{ V}, I_D=80\text{ A}, V_{GS}=0\text{ to }10\text{ V}$ | - | 11  | 14  | nC |
| Gate to drain charge  | $Q_{gd}$      |                                                                       | - | 32  | 48  |    |
| Gate charge total     | $Q_g$         |                                                                       | - | 95  | 130 |    |
| Gate plateau voltage  | $V_{plateau}$ |                                                                       | - | 3.5 | -   | V  |

#### Reverse Diode

|                                                |               |                                                              |   |     |     |    |
|------------------------------------------------|---------------|--------------------------------------------------------------|---|-----|-----|----|
| Diode continuous forward current <sup>2)</sup> | $I_S$         | $T_C=25\text{ °C}$                                           | - | -   | 80  | A  |
| Diode pulse current <sup>2)</sup>              | $I_{S,pulse}$ |                                                              | - | -   | 320 |    |
| Diode forward voltage                          | $V_{SD}$      | $V_{GS}=0\text{ V}, I_F=80\text{ A}, T_J=25\text{ °C}$       | - | 0.9 | 1.3 | V  |
| Reverse recovery time <sup>2)</sup>            | $t_{rr}$      | $V_R=30\text{ V}, I_F=I_S, di_F/dt=100\text{ A}/\mu\text{s}$ | - | 59  | 75  | ns |
| Reverse recovery charge <sup>2)</sup>          | $Q_{rr}$      |                                                              | - | 80  | 100 |    |

<sup>1)</sup> Current is limited by bondwire; with an  $R_{thJC} = 0.7\text{ K/W}$  the chip is able to carry 121 A at 25°C. For detailed information see Application Note ANPS071E at [www.infineon.com/optimos](http://www.infineon.com/optimos)

<sup>2)</sup> Defined by design. Not subject to production test.

<sup>3)</sup> See diagram 13

<sup>4)</sup> Qualified at -20V and +20V.

<sup>5)</sup> Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm<sup>2</sup> (one layer, 70 μm thick) copper area for drain connection. PCB is vertical in still air.

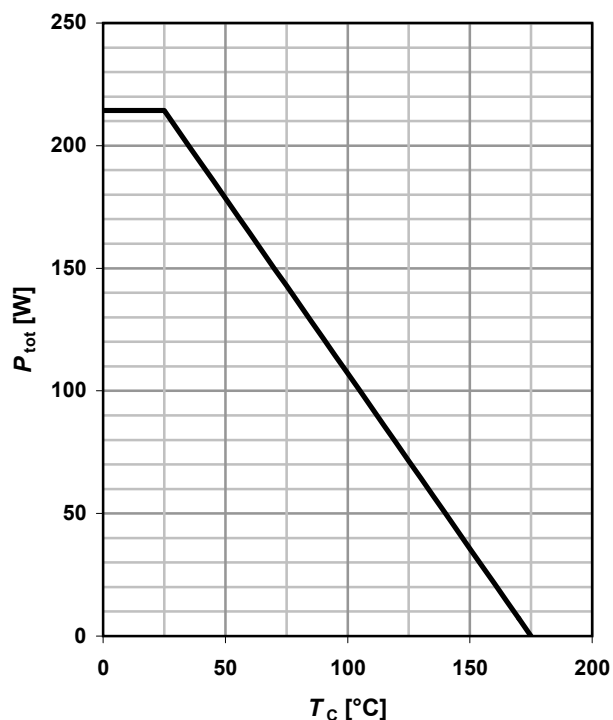


**IPB80N06S2L-07**

**IPP80N06S2L-07**

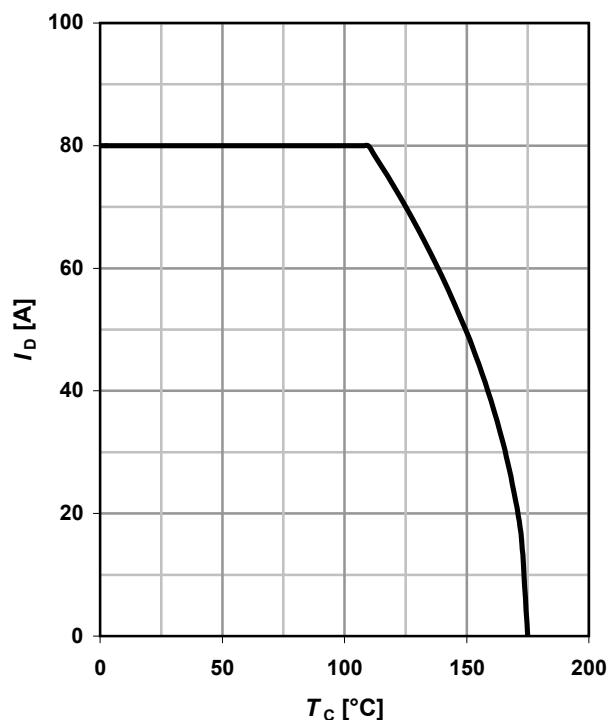
### 1 Power dissipation

$$P_{\text{tot}} = f(T_C); V_{\text{GS}} \geq 4 \text{ V}$$



### 2 Drain current

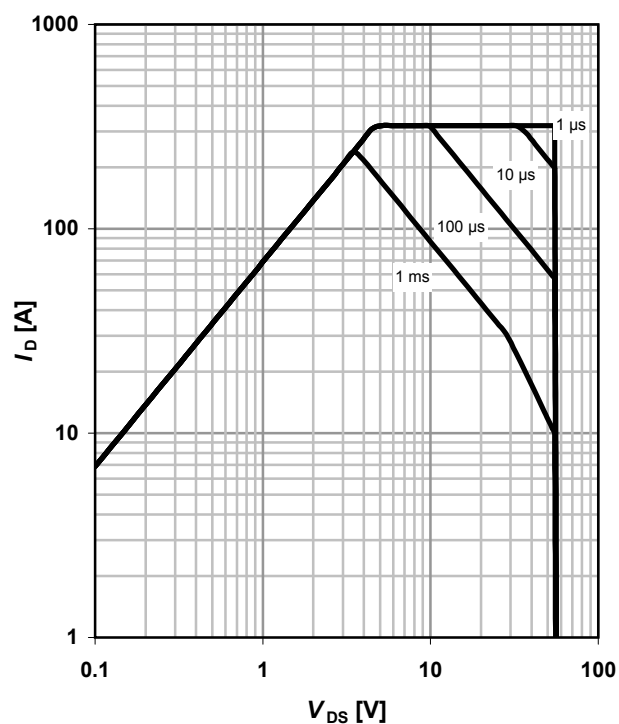
$$I_D = f(T_C); V_{\text{GS}} \geq 10 \text{ V}$$



### 3 Safe operating area

$$I_D = f(V_{\text{DS}}); T_C = 25 \text{ °C}; D = 0$$

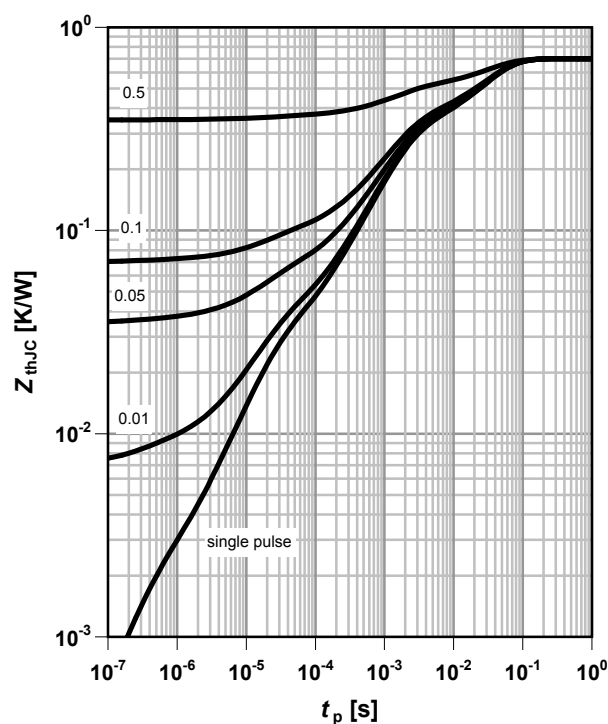
parameter:  $t_p$



### 4 Max. transient thermal impedance

$$Z_{\text{thJC}} = f(t_p)$$

parameter:  $D = t_p/T$





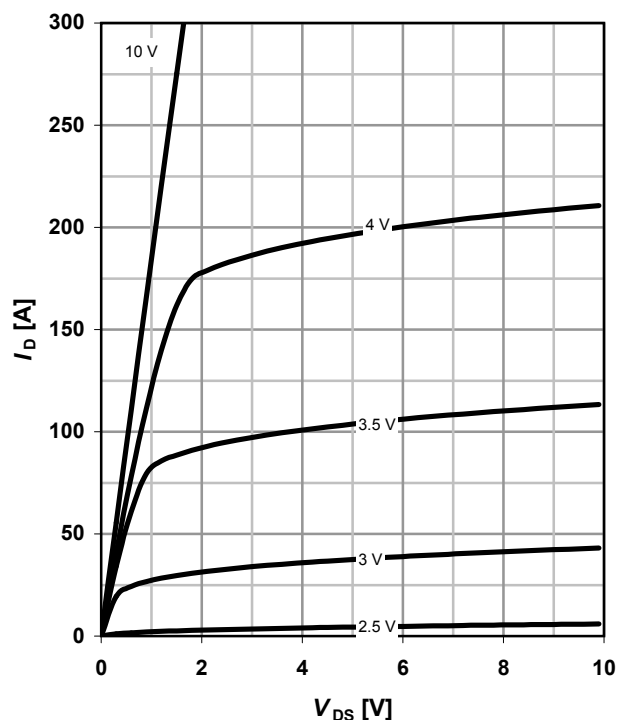
**IPB80N06S2L-07**

**IPP80N06S2L-07**

### 5 Typ. output characteristics

$$I_D = f(V_{DS}); T_j = 25^\circ\text{C}$$

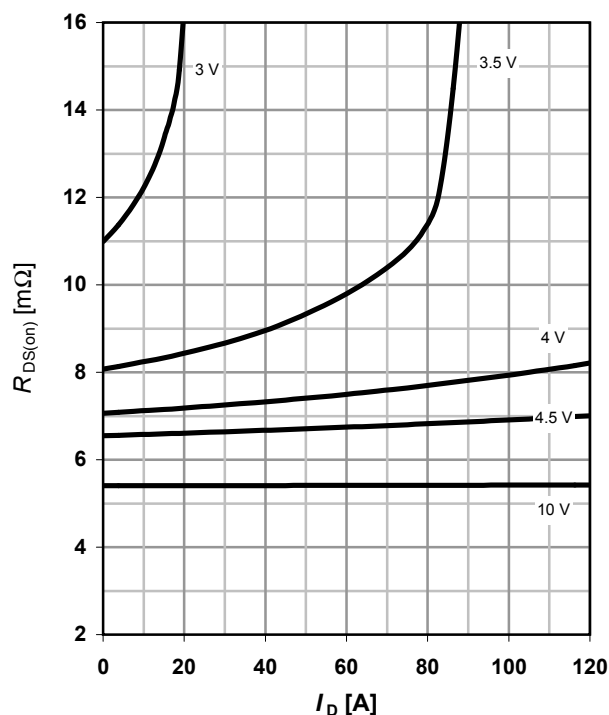
parameter:  $V_{GS}$



### 6 Typ. drain-source on-state resistance

$$R_{DS(on)} = f(I_D); T_j = 25^\circ\text{C}$$

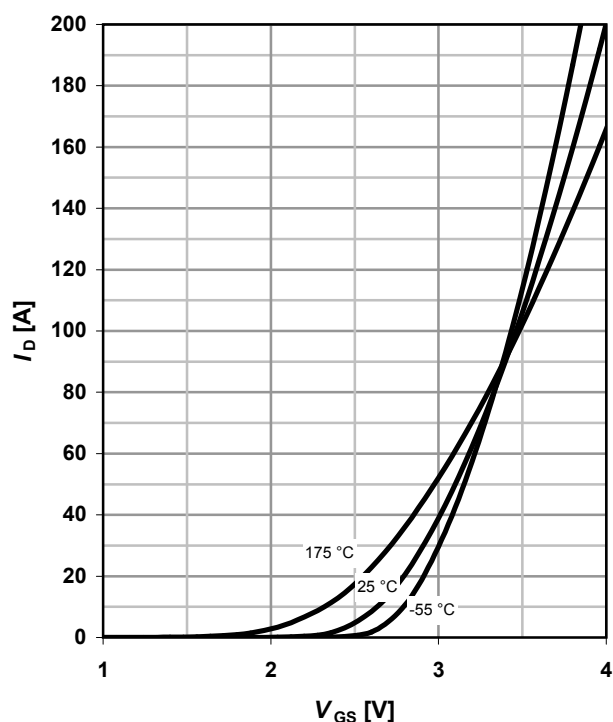
parameter:  $V_{GS}$



### 7 Typ. transfer characteristics

$$I_D = f(V_{GS}); V_{DS} = 6\text{V}$$

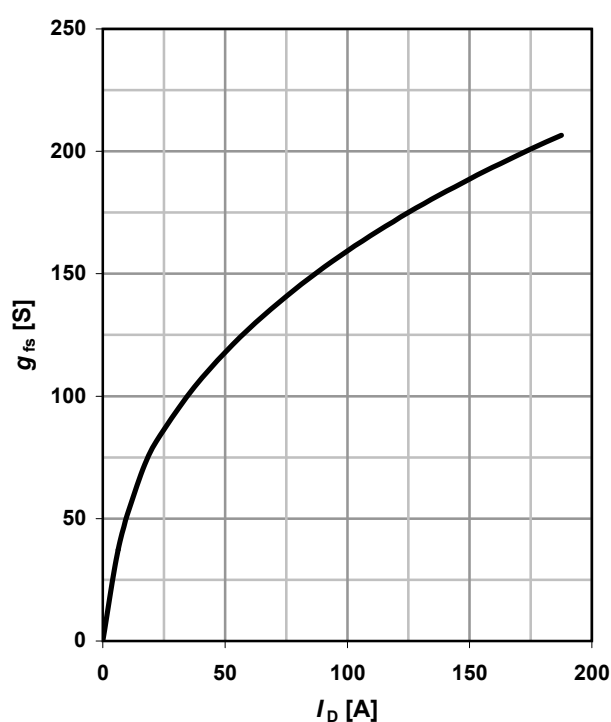
parameter:  $T_j$



### 8 Typ. Forward transconductance

$$g_{fs} = f(I_D); T_j = 25^\circ\text{C}$$

parameter:  $g_{fs}$





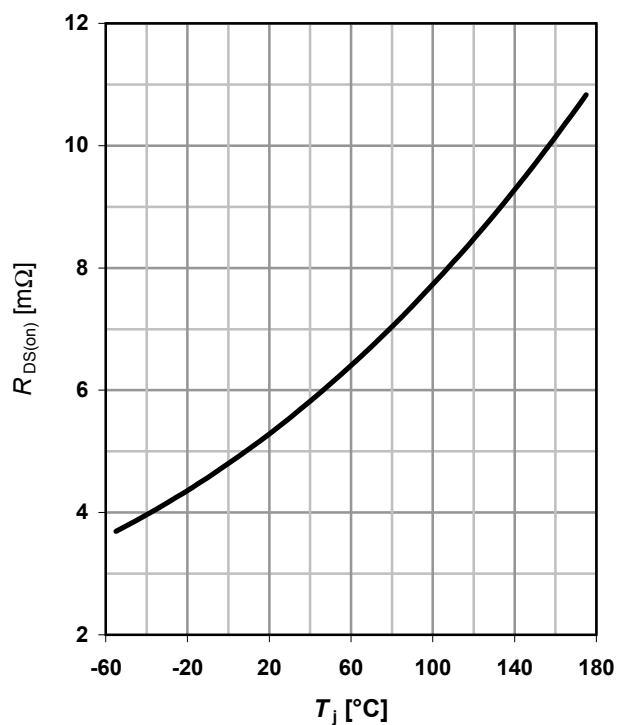
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**IPP80N06S2L-07**

### 9 Typ. Drain-source on-state resistance

$$R_{DS(on)} = f(T_j)$$

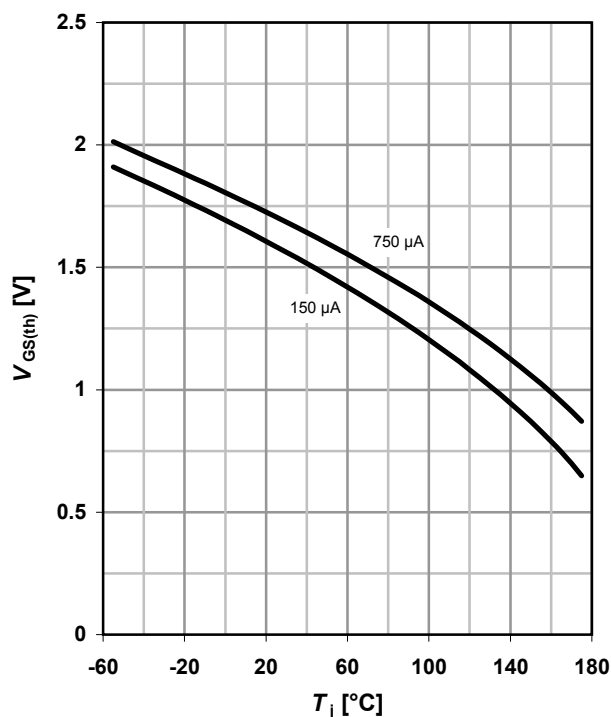
parameter:  $I_D = 80\text{ A}$ ;  $V_{GS} = 10\text{ V}$



### 10 Typ. gate threshold voltage

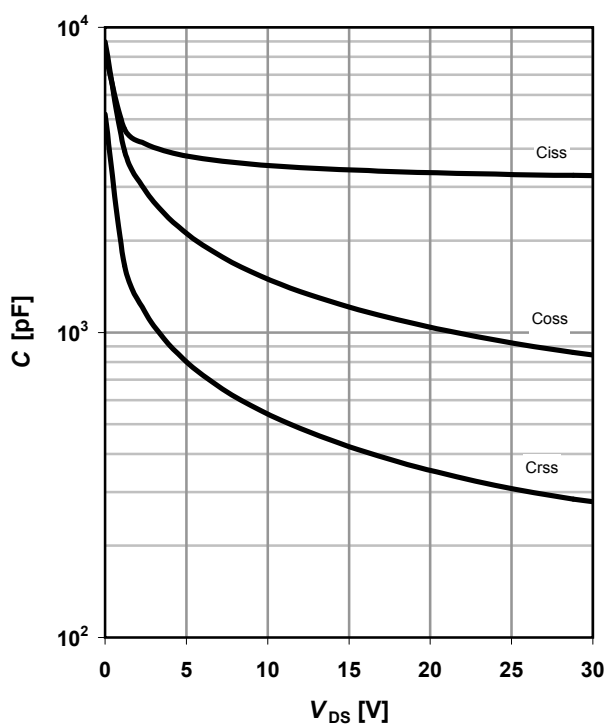
$$V_{GS(th)} = f(T_j); V_{GS} = V_{DS}$$

parameter:  $I_D$



### 11 Typ. capacitances

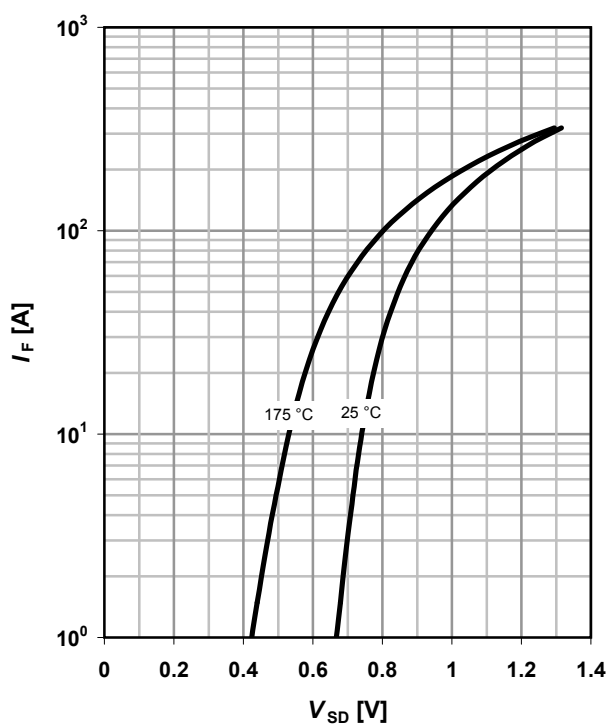
$$C = f(V_{DS}); V_{GS} = 0\text{ V}; f = 1\text{ MHz}$$



### 12 Typical forward diode characteristics

$$I_F = f(V_{SD})$$

parameter:  $T_j$





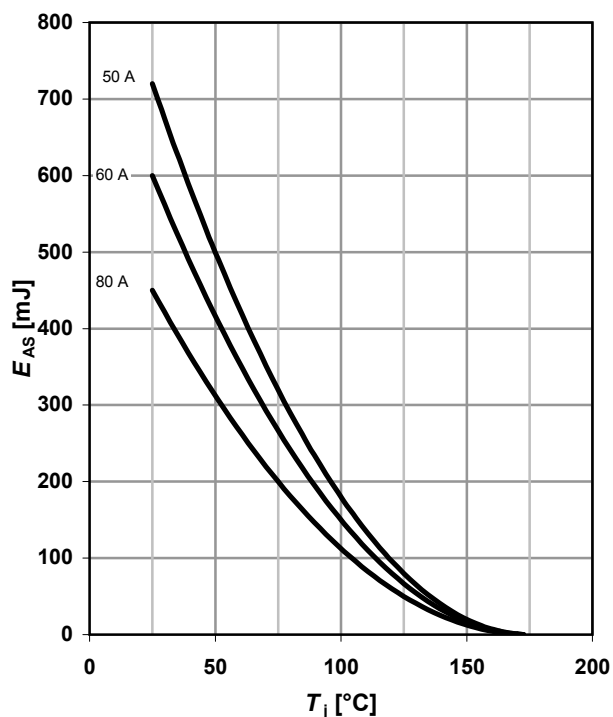
**IPB80N06S2L-07**

**IPP80N06S2L-07**

### 13 Typical avalanche energy

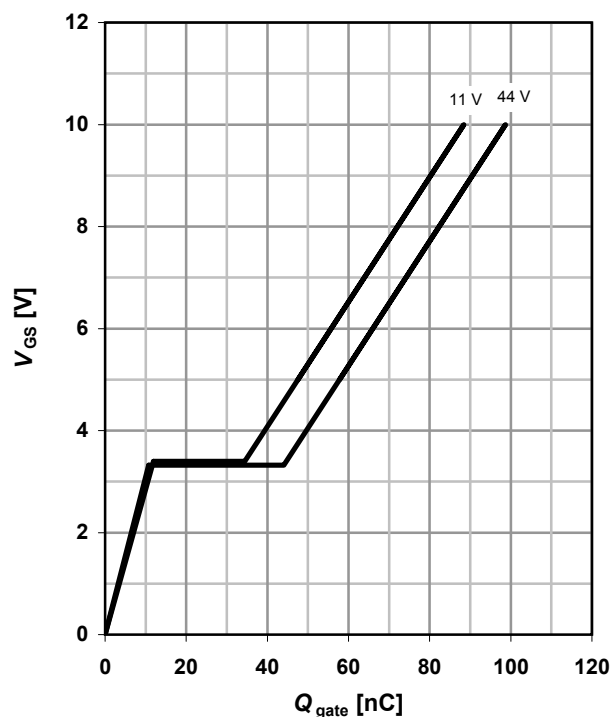
$$E_{AS} = f(T_j)$$

parameter:  $I_D$



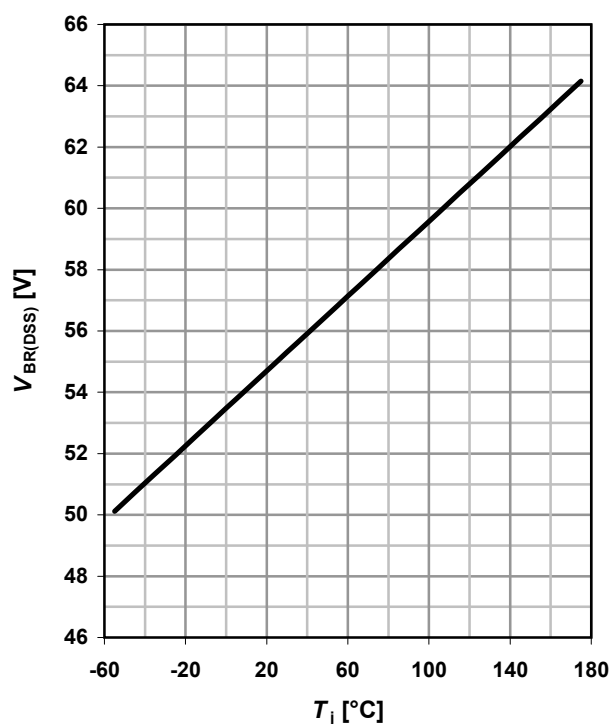
### 14 Typ. gate charge

$$V_{GS} = f(Q_{gate}); I_D = 80 \text{ A pulsed}$$

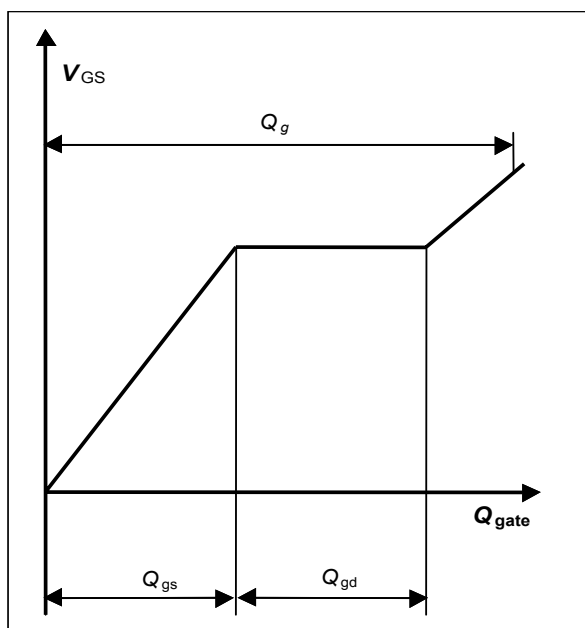


### 15 Typ. drain-source breakdown voltage

$$V_{BR(DSS)} = f(T_j); I_D = 1 \text{ mA}$$



### 16 Gate charge waveforms





**IPB80N06S2L-07**

**IPP80N06S2L-07**

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