



April 2015

FGH40N65UFD_F085

650 V, 40 A Field Stop IGBT

Features

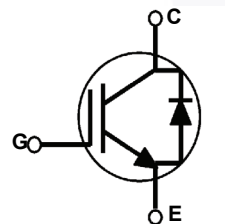
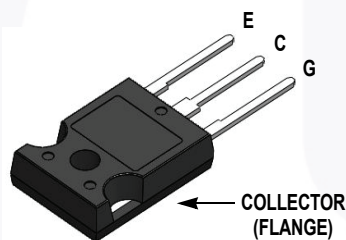
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)} = 1.8 \text{ V @ } I_C = 40 \text{ A}$
- High Input Impedance
- Fast Switching
- RoHS Compliant
- Qualified to Automotive Requirements of AEC-Q101

Applications

- Automotive Chargers, Converters, High Voltage Auxiliaries
- Inverters, PFC, UPS

General Description

Using novel field stop IGBT technology, Fairchild's field stop IGBTs offer the optimum performance for Automotive Chargers, Inverter, and other applications where low conduction and switching losses are essential.



Absolute Maximum Ratings

Symbol	Description	Ratings	Unit
V_{CES}	Collector to Emitter Voltage	650	V
V_{GES}	Gate to Emitter Voltage	± 20	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	80	A
	Collector Current @ $T_C = 100^\circ\text{C}$	40	A
$I_{CM(1)}$	Pulsed Collector Current @ $T_C = 25^\circ\text{C}$	120	A
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	290	W
	Maximum Power Dissipation @ $T_C = 100^\circ\text{C}$	116	W
T_J	Operating Junction Temperature	-55 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

Notes:

1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ	Unit
$R_{\theta JC}(\text{IGBT})$	Thermal Resistance, Junction to Case	0.43	$^\circ\text{C/W}$
$R_{\theta JC}(\text{Diode})$	Thermal Resistance, Junction to Case	1.45	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	$^\circ\text{C/W}$

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGH40N65UFDU_F085	FGH40N65UFD	TO-247	Tube	N/A	N/A	30

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV _{CES}	Collector to Emitter Breakdown Voltage	V _{GE} = 0 V, I _C = 250 μA	650	-	-	V
ΔBV _{CES} ΔT _J	Temperature Coefficient of Breakdown Voltage	V _{GE} = 0 V, I _C = 250 μA	-	0.6	-	V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0 V	-	-	250	μA
I _{GES}	G-E Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0 V	-	-	±400	nA
On Characteristics						
V _{GE(th)}	G-E Threshold Voltage	I _C = 250 μA, V _{CE} = V _{GE}	4.0	5.2	6.5	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 40 A, V _{GE} = 15 V	-	1.8	2.4	V
		I _C = 40 A, V _{GE} = 15 V, T _C = 125°C	-	2.0	-	V
Dynamic Characteristics						
C _{ies}	Input Capacitance	V _{CE} = 30 V, V _{GE} = 0 V, f = 1 MHz	-	1860	-	pF
C _{oes}	Output Capacitance		-	200	-	pF
C _{res}	Reverse Transfer Capacitance		-	65	-	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{CC} = 400 V, I _C = 40 A, R _G = 10 Ω, V _{GE} = 15 V, Inductive Load, T _C = 25°C	-	23	-	ns
t _r	Rise Time		-	35	-	ns
t _{d(off)}	Turn-Off Delay Time		-	126	-	ns
t _f	Fall Time		-	26	60	ns
E _{on}	Turn-On Switching Loss		-	1.28	-	mJ
E _{off}	Turn-Off Switching Loss		-	0.50	-	mJ
E _{ts}	Total Switching Loss	V _{CC} = 400 V, I _C = 40 A, R _G = 10 Ω, V _{GE} = 15 V, Inductive Load, T _C = 125°C	-	1.78	-	mJ
t _{d(on)}	Turn-On Delay Time		-	21	-	ns
t _r	Rise Time		-	39	-	ns
t _{d(off)}	Turn-Off Delay Time		-	131	-	ns
t _f	Fall Time		-	72	-	ns
E _{on}	Turn-On Switching Loss		-	1.62	-	mJ
E _{off}	Turn-Off Switching Loss	V _{CE} = 400 V, I _C = 40 A, V _{GE} = 15 V	-	0.79	-	mJ
E _{ts}	Total Switching Loss		-	2.41	-	mJ
Q _g	Total Gate Charge		-	119	-	nC
Q _{ge}	Gate to Emitter Charge	V _{CE} = 400 V, I _C = 40 A, V _{GE} = 15 V	-	14	-	nC
Q _{gc}	Gate to Collector Charge		-	64	-	nC

Electrical Characteristics of the Diode $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Typ.	Max	Unit
V_{FM}	Diode Forward Voltage	$I_F = 20\text{ A}$	$T_C = 25^\circ\text{C}$	-	1.80	2.6	V
			$T_C = 125^\circ\text{C}$	-	1.71	-	
t_{rr}	Diode Reverse Recovery Time	$I_F = 20\text{ A}, di_F/dt = 200\text{ A}/\mu\text{s}$	$T_C = 25^\circ\text{C}$	-	65	-	ns
			$T_C = 125^\circ\text{C}$	-	215	-	
Q_{rr}	Diode Reverse Recovery Charge	$I_F = 20\text{ A}, di_F/dt = 200\text{ A}/\mu\text{s}$	$T_C = 25^\circ\text{C}$	-	145	-	nC
			$T_C = 125^\circ\text{C}$	-	775	-	

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

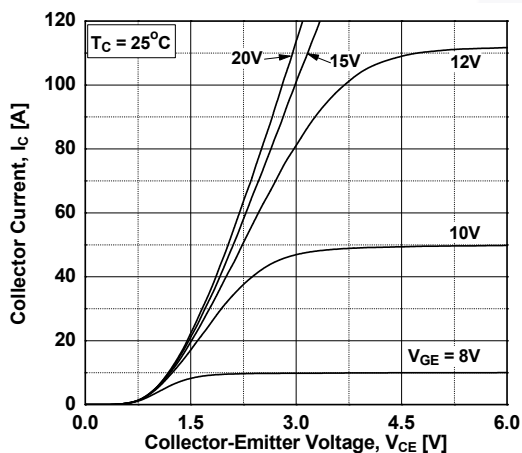


Figure 2. Typical Output Characteristics

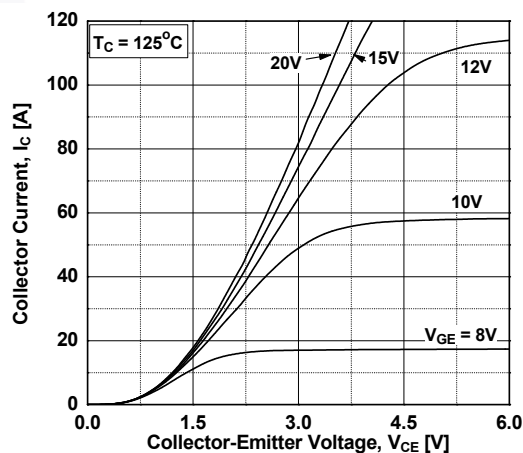


Figure 3. Typical Saturation Voltage Characteristics

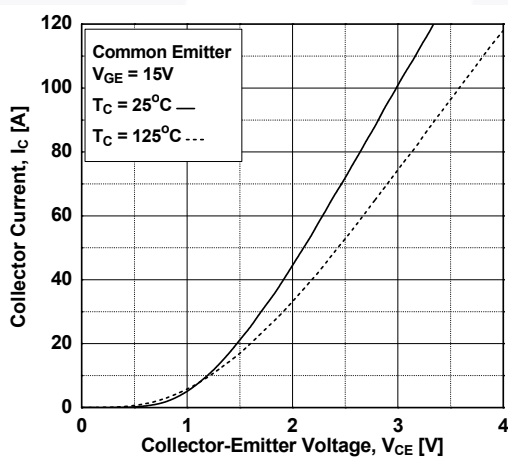


Figure 4. Transfer Characteristics

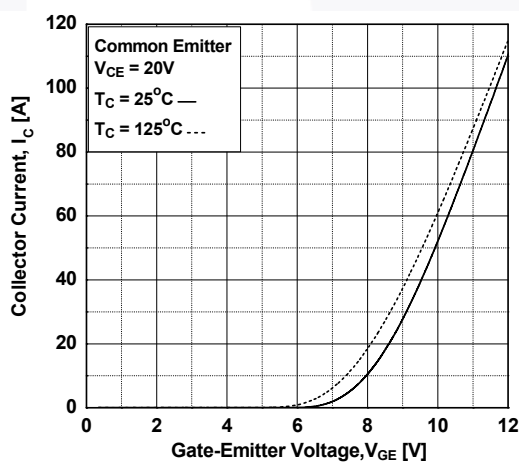


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

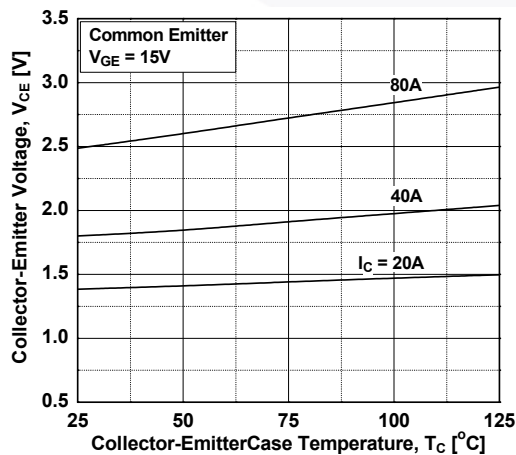
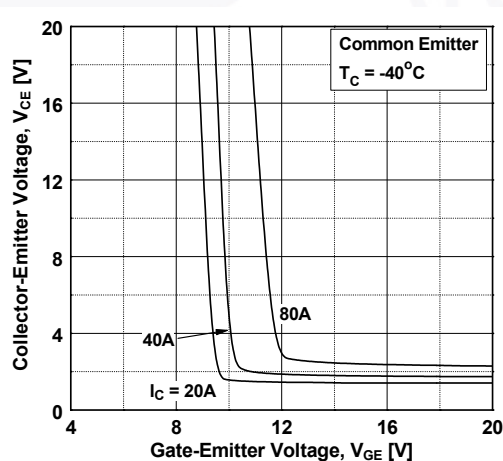


Figure 6. Saturation Voltage vs. Vge



Typical Performance Characteristics

Figure 7. Saturation Voltage vs. V_{GE}

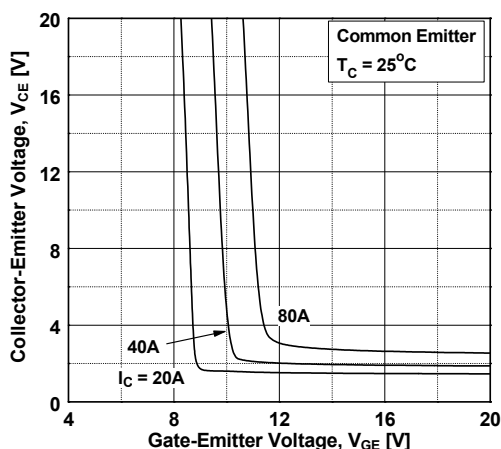


Figure 8. Saturation Voltage vs. V_{GE}

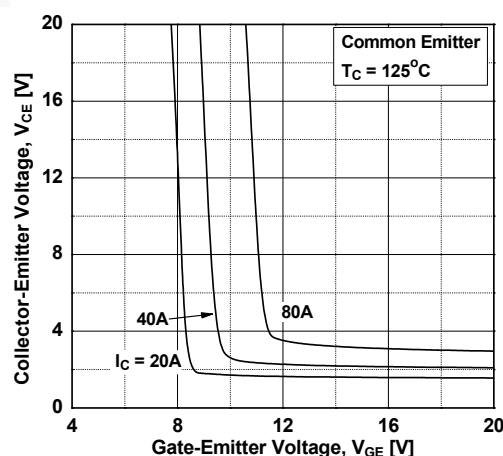


Figure 9. Capacitance Characteristics

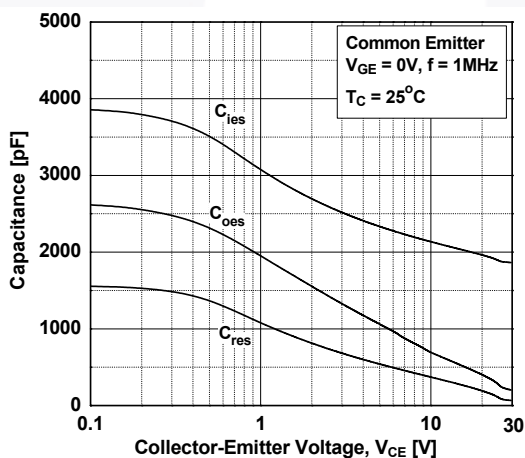


Figure 10. Gate charge Characteristics

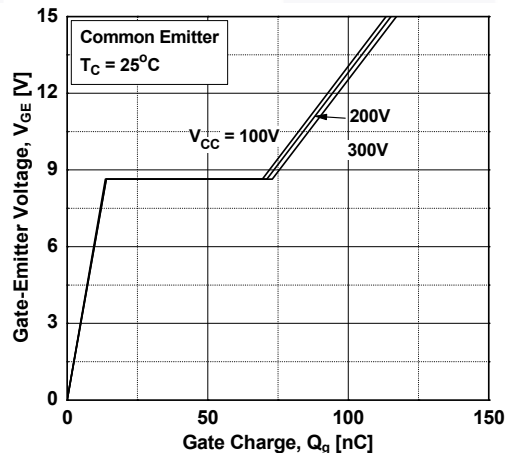


Figure 11. SOA Characteristics

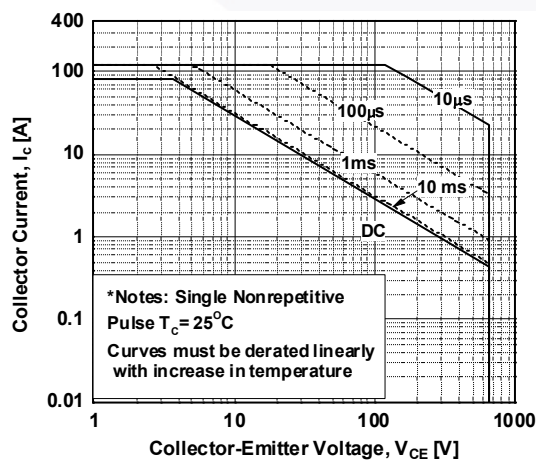
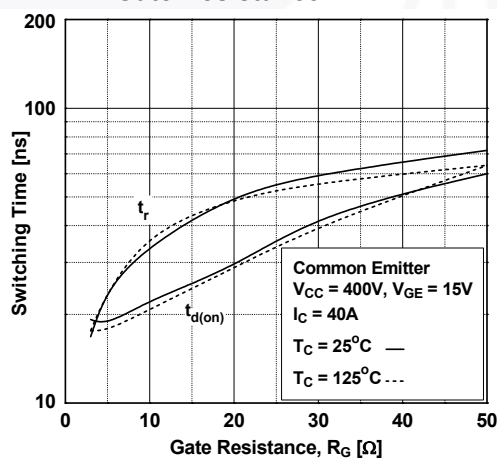


Figure 12. Turn-on Characteristics vs. Gate Resistance



Typical Performance Characteristics

Figure 13. Turn-off Characteristics vs. Gate Resistance

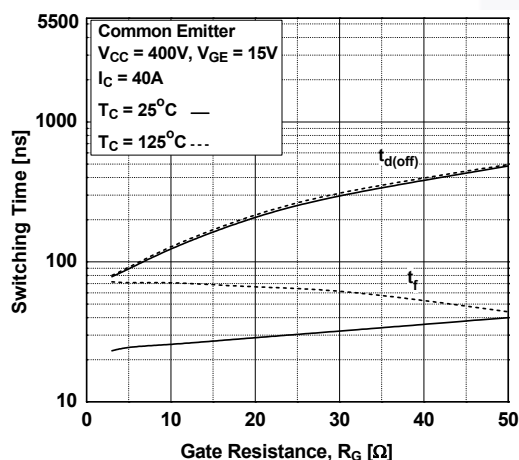


Figure 14. Turn-on Characteristics vs. Collector Current

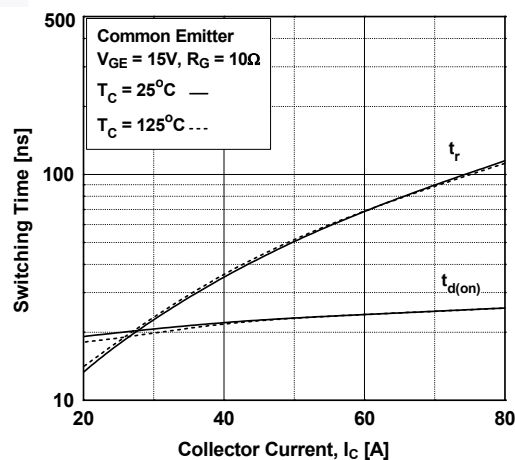


Figure 15. Turn-off Characteristics vs. Collector Current

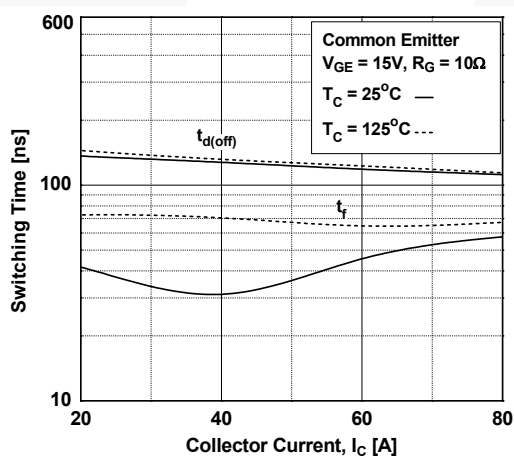


Figure 16. Switching Loss vs. Gate Resistance

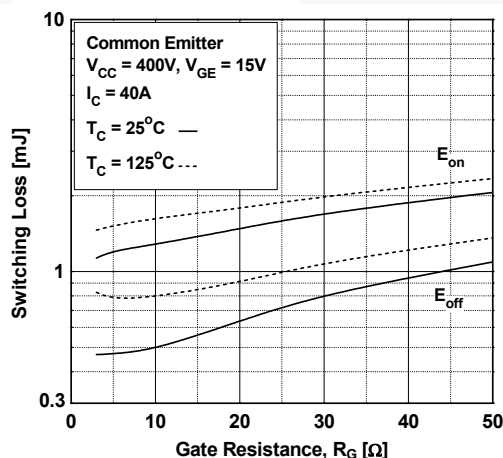


Figure 17. Switching Loss vs. Collector Current

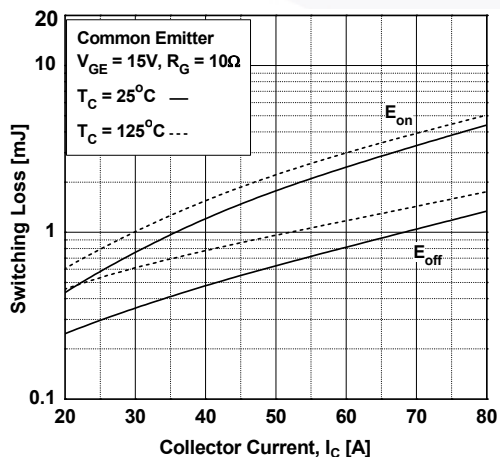
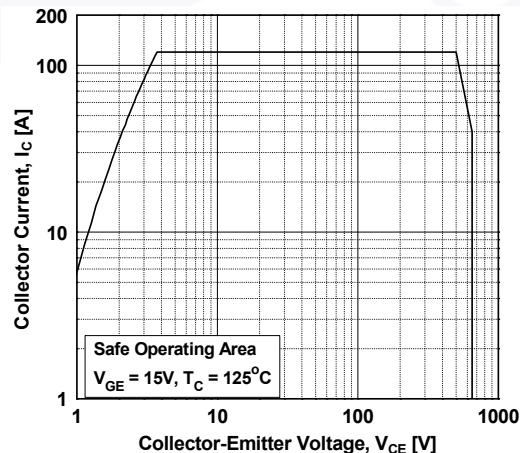


Figure 18. Turn off Switching SOA Characteristics



Typical Performance Characteristics

Figure 19. Forward Characteristics

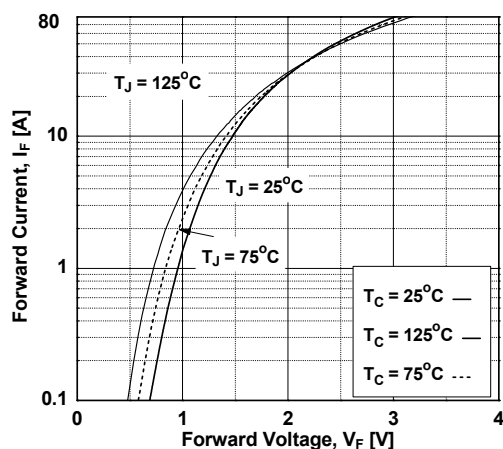


Figure 20. Reverse Current

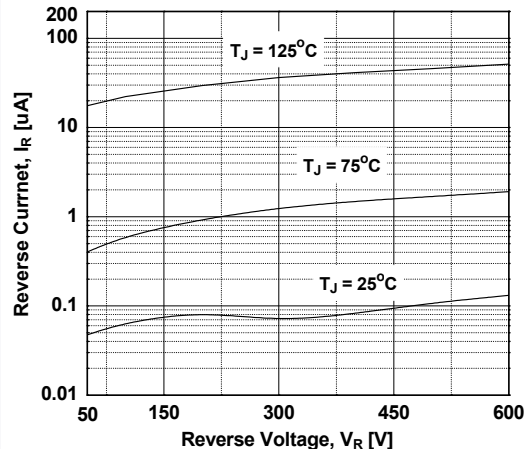


Figure 21. Stored Charge

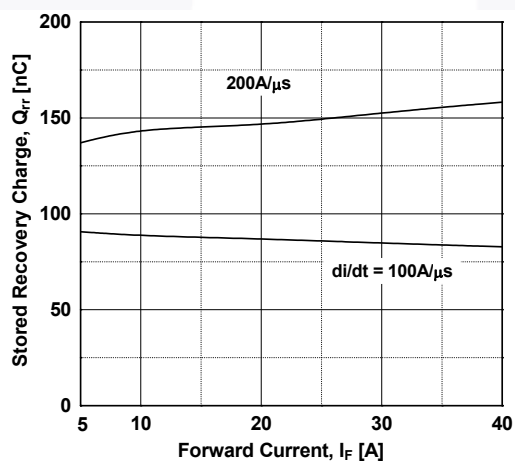


Figure 22. Reverse Recovery Time

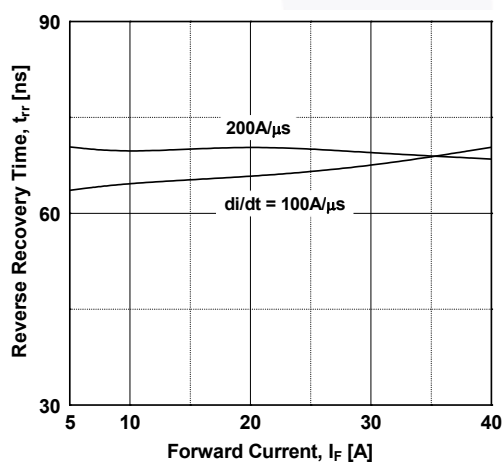
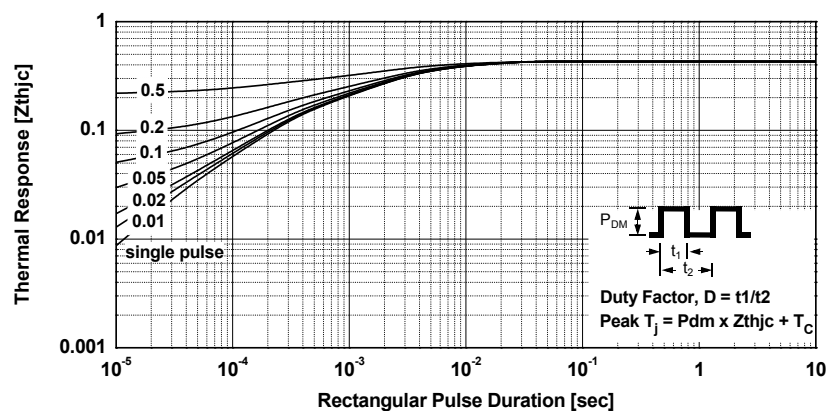
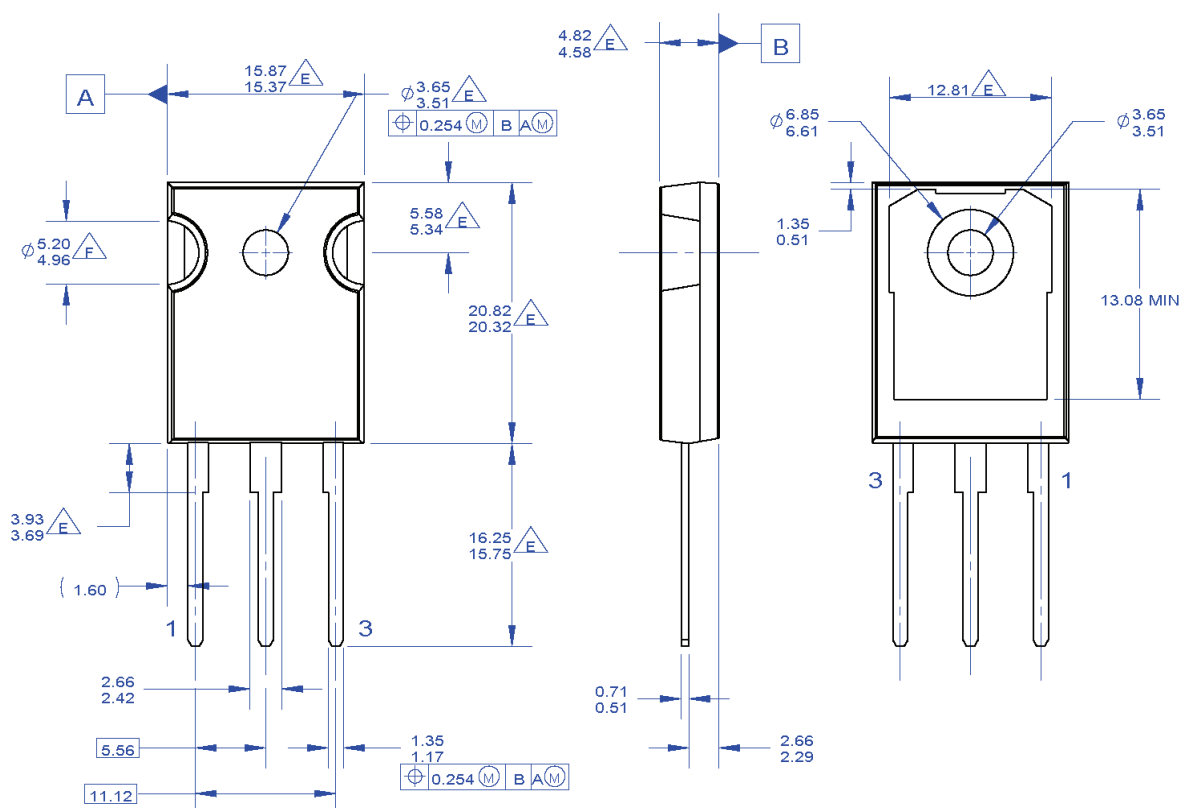


Figure 23. Transient Thermal Impedance of IGBT



Mechanical Dimensions



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DRAWING CONFORMS TO ASME Y14.5 - 1994

$\triangle$ E DOES NOT COMPLY JEDEC STANDARD VALUE

$\triangle$ F NOTCH MAY BE SQUARE

G. DRAWING FILENAME: MKT-TO247A03_REV03

Figure 24. TO-247 3L - TO-247,MOLDED,3 LEAD,JEDEC VARIATION AB

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DEUXPEED®	MICROCOUPLER™	SmartMax™	TRUECURRENT®
Dual Cool™	MicroFET™	SMART START™	µSerDes™
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FACT®	MTx®	SuperSOT™.8	XS™
FAST®	MVN®	SupreMOS®	Xsens™
FastvCore™	mWSaver®	SyncFET™	仙童™
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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