Excellent Integrated System Limited

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ON Semiconductor MPS6717

For any questions, you can email us directly: sales@integrated-circuit.com

Distributor of ON Semiconductor: Excellent Integrated System Limited

Datasheet of MPS6717 - TRANS NPN 80V 0.5A TO92

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

MPS6717

One Watt Amplifier Transistor

NPN Silicon

Features

Pb–Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Collector – Emitter Voltage	V _{CEO}	80	Vdc	
Collector - Base Voltage	V _{CBO}	80	Vdc	
Emitter – Base Voltage	V _{EBO}	5.0	Vdc	
Collector Current – Continuous	urrent – Continuous I _C 500			
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.0 8.0	W mW/°C	
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D 2.5 20		W mW/°C	
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C	

THERMAL CHARACTERISTICS

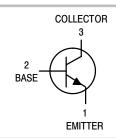
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction-to-Case	Raic	50	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



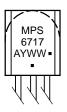
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



MPS6717 = Device Code = Assembly Location Α = Year

WW = Work Week = Pb-Free Package (Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MPS6717	TO-92	5000 Units / Bulk
MPS6717G	TO-92 (Pb-Free)	5000 Units / Bulk
MPS6717RLRA	TO-92	2000/Tape & Reel
MPS6717RLRAG	TO-92 (Pb-Free)	2000/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Symbol	Min	Max	Unit
V _{(BR)CEO}	80	_	Vdc
V _{(BR)CBO}	80	-	Vdc
V _{(BR)EBO}	5.0	-	Vdc
Ісво	-	0.1	μAdc
I _{EBO}	-	10	μAdc
h _{FE}	80 50	_ 250	_
V _{CE(sat)}	-	0.5	Vdc
V _{BE(on)}	-	1.2	Vdc
•		•	•
C _{cb}	_	30	pF
h _{fe}	2.5	25	-
	V(BR)CEO V(BR)CBO V(BR)EBO ICBO IEBO VCE(sat) VBE(on)	V(BR)CEO 80 V(BR)CBO 80 V(BR)EBO 5.0 ICBO - IEBO - VCE(sat) - VBE(on) -	V(BR)CEO 80 - V(BR)CBO 80 - V(BR)EBO 5.0 - ICBO - 0.1 IEBO - 10 hFE 80

^{1.} Pulse Test: Pulse Width $\leq 300 \,\mu s$; Duty Cycle $\leq 2.0\%$.

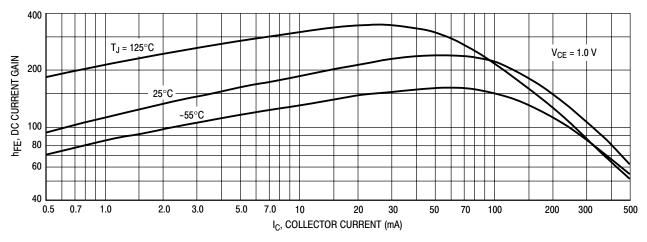


Figure 1. DC Current Gain

MPS6717

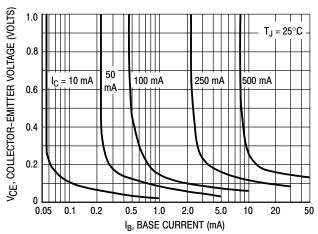


Figure 2. Collector Saturation Region

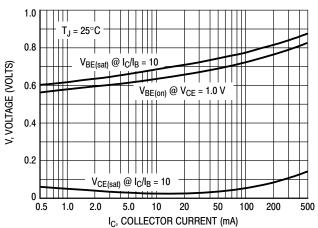


Figure 3. "On" Voltages

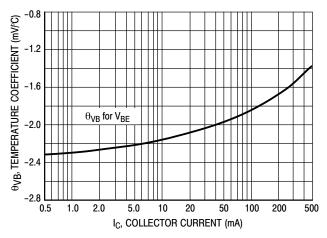


Figure 4. Base-Emitter Temperature Coefficient

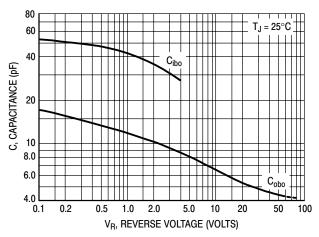


Figure 5. Capacitance

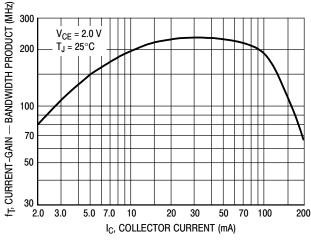


Figure 6. Current-Gain — Bandwidth Product

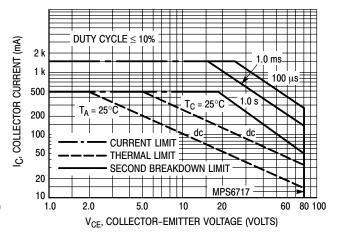


Figure 7. Active Region — Safe Operating Area



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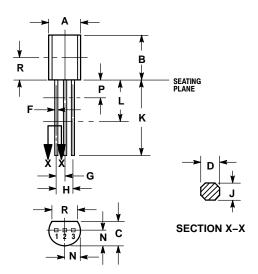
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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 ISSUE AL



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI

- Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

 3. CONTOUR OF PACKAGE BEYOND DIMENSION R
 IS UNCONTROLLED.

 4. DIMENSION F APPLIES BETWEEN P AND L.
 DIMENSIONS D AND J APPLY BETWEEN L AND K
 MIMIMUM. LEAD DIMENSION IS UNCONTROLLED. IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.44	5.21	
В	0.290	0.310	7.37	7.87	
С	0.125	0.165	3.18	4.19	
D	0.018	0.021	0.457	0.533	
F	0.016	0.019	0.407	0.482	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.018	0.024	0.46	0.61	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.135		3.43		

STYLE 1: PIN 1. EMITTER

BASE

COLLECTOR

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