

# **Excellent Integrated System Limited**

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ON Semiconductor MPS4124RLRA

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



**Distributor of ON Semiconductor: Excellent Integrated System Limited** Datasheet of MPS4124RLRA - TRANS NPN 25V 0.2A TO92 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

## **MPS4124**

# **Amplifier Transistor**

## **NPN Silicon**

#### Features

• Pb–Free Packages are Available\*

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CE</sub>	25	Vdc
Collector-Base Voltage	V <sub>CB</sub>	30	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current – Continuous	Ι <sub>C</sub>	200	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above 25°C	P <sub>D</sub>	625 5.0	W mW/°C
Total Device Dissipation @ $T_C = 25^{\circ}C$ Derate above 25°C	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

### THERMAL CHARACTERISTICS

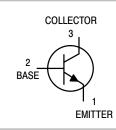
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



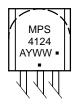
## **ON Semiconductor®**

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### MARKING DIAGRAM



A = Assembly Location

= Year

Υ

- WW = Work Week
- = Pb–Free Package

(Note: Microdot may be in either location)

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MPS4124RLRA	TO-92	2000/Tape & Reel
MPS4124RLRAG	TO–92 (Pb–Free)	2000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



## MPS4124

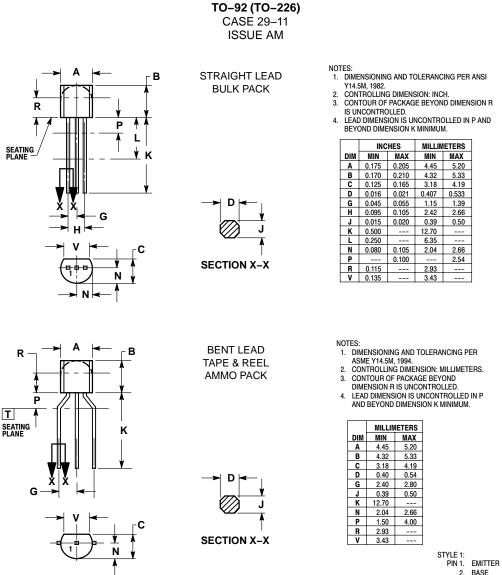
### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage ( $I_C = 1.0 \text{ mA}, I_B = 0$ )	V <sub>(BR)CEO</sub>	25	_	Vdc
Collector-Base Breakdown Voltage $(I_C = 10 \ \mu A, I_E = 0)$	V <sub>(BR)CBO</sub>	30	-	Vdc
Emitter – Base Breakdown Voltage ( $I_C = 0, I_E = 10 \mu A$ )	V <sub>(BR)EBO</sub>	5.0	-	Vdc
Collector Cutoff Current $(V_{CB} = 20 \text{ V}, I_E = 0)$	I <sub>CBO</sub>	-	50	nAdc
Emitter Cutoff Current ( $V_{EB} = 3.0 \text{ V}, I_C = 0$ )	I <sub>EBO</sub>	-	50	nAdc
ON CHARACTERISTICS				
DC Current Gain $(I_C = 2.0 \text{ mA}, V_{CE} = 1.0 \text{ V})$ $(I_C = 50 \text{ mA}, V_{CE} = 1.0 \text{ V})$	h <sub>FE</sub>	120 60	360 -	-
Collector – Emitter Saturation Voltage $(I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA})$	V <sub>CE(sat)</sub>	-	0.3	Vdc
Base – Emitter Saturation Voltage ( $I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$ )	V <sub>BE(sat)</sub>	-	0.95	Vdc
SMALL-SIGNAL CHARACTERISTICS			•	<u> </u>
Current-Gain – Bandwidth Product ( $I_C = 10$ mA, $V_{CE} = 20$ V, f = 100 MHz)	f <sub>T</sub>	170	_	MHz
Output Capacitance $(V_{CB} = 5.0 \text{ V}, I_E = 0, f = 1.0 \text{ MHz})$	C <sub>ob</sub>	_	4.0	pF
Input Capacitance ( $V_{EB} = 0.5 \text{ V}, I_C = 0, f = 1.0 \text{ MHz}$ )	C <sub>ib</sub>	-	13.5	pF
Small–Signal Current Gain ( $I_C = 2.0 \text{ mA}, V_{CE} = 1.0 \text{ V}, f = 1.0 \text{ kHz}$ )	h <sub>fe</sub>	120	480	-
Noise Figure (I_C = 100 $\mu\text{A},\text{V}_{\text{CE}}$ = 5.0 V, R_S = 1.0 kΩ, f = 1.0 kHz)	NF	_	5.0	dB



## MPS4124

#### PACKAGE DIMENSIONS



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