

Excellent Integrated System Limited

Stocking Distributor

Click to view price, real time Inventory, Delivery & Lifecycle Information:

ON Semiconductor VN0300L

For any questions, you can email us directly: <u>sales@integrated-circuit.com</u>



Distributor of ON Semiconductor: Excellent Integrated System Limited Datasheet of VN0300L - MOSFET N-CH 60V 200MA TO-92 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

VN0300L

Preferred Device

Small Signal MOSFET 200 mAmps, 60 Volts N-Channel TO-92

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|-------------------------------------|--------------|-------------|
| Drain-Source Voltage | V _{DSS} | 60 | V |
| Drain-Gate Voltage | VDGR | 60 | V |
| Gate–Source Voltage – Continuous – Non–repetitive (t _p ≤ 50 μs) | V _{GS} V _{GSM} | ± 20 ± 40 | Vdc Vpk |
| Continuous Drain Current | ۱D | 200 | mA |
| Pulsed Drain Current | IDM | 500 | mA |
| Power Dissipation @ T _C = 25°C Derate above 25°C | PD | 350 2.8 | mW mW/°C |
| Operating and Storage Temperature | TJ, Tstg | _ | °C |

THERMAL CHARACTERISTICS

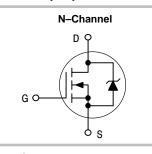
| Characteristics | Symbol | Max | Unit |
|---|-----------------|-------|------|
| Thermal Resistance, Junction to Ambient | $R_{\theta JA}$ | 312.5 | °C/W |
| Maximum Lead Temperature for Soldering Purposes, 1/16" from case for 10 seconds | Т | 300 | °C |



ON Semiconductor

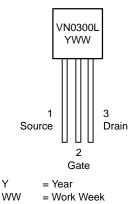
http://onsemi.com

200 mAMPS 60 VOLTS RDS(on) = 1.2 Ω





& PIN ASSIGNMENT



ORDERING INFORMATION

| Device | Package | Shipping | | |
|-------------|---------|------------------|--|--|
| VN0300L | TO-92 | 1000 Units/Box | | |
| VN0300LRLRA | TO-92 | 2000 Tape & Reel | | |
| VN0300LRLRE | TO-92 | 2000 Tape & Reel | | |

Preferred devices are recommended choices for future use and best overall value.



VN0300L

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

| Characteristic | | | Min | Max | Unit |
|--|---|-----------------------|------|------------|------|
| STATIC CHARACTERISTICS | | | | • | |
| Drain–Source Breakdown Voltage $(V_{DS} = 0, I_D = 10 \ \mu A)$ | | V _(BR) DSS | 30 | - | V |
| Zero Gate Voltage Drain Current $(V_{DS} = 48 \text{ Vdc}, V_{GS} = 0)$ $(V_{DS} = 48 \text{ Vdc}, V_{GS} = 0, T_A = 125^{\circ}\text{C})$ | | IDSS | - | 10 500 | μΑ |
| Gate–Body Leakage (V _{DS} = 0, V _{GS} = ±30 V) | IGSS | - | ±100 | nA | |
| Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = 1.0 \text{ mA})$ | VGS(th) | 0.8 | 2.5 | V | |
| On–State Drain Current (Note 1.) $(V_{DS} = V_{GS}, I_D = 1.0 \text{ mA})$ | I _{D(on)} | 1.0 | - | A | |
| Drain–Source On Resistance (Note 1.) $(V_{GS} = 5.0 \text{ V}, I_D = 0.3 \text{ A})$ $(V_{GS} = 10 \text{ V}, I_D = 1.0 \text{ A})$ | | ^r DS(on) | - | 3.3 1.2 | Ω |
| Forward Transconductance (Note 1.) $(V_{DS} = 10 \text{ V}, I_D = 0.5 \text{ A})$ | | 9fs | 200 | - | mS |
| DYNAMIC CHARACTERISTIC | S | · · · · · | | | |
| Input Capacitance | | C _{iss} | - | 100 | pF |
| Output Capacitance | (V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 MHz) | C _{oss} | - | 95 | pF |
| Reverse Transfer Capacitance | · ···································· | C _{rss} | - | 25 | pF |
| SWITCHING CHARACTERIS | FICS | | | | |
| Turn–On Time | (V _{DD} = 25 Vdc, I _D = 1.0 A, | ton | - | 30 | ns |
| Turn–Off Time | $R_L = 24 \Omega$, $RG = 25 \Omega$) | toff | _ | 30 | ns |

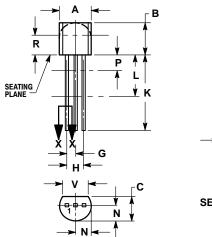
1. Pulse Test; Pulse Width < 300 μ s, Duty Cycle \leq 2.0%.



VN0300L

PACKAGE DIMENSIONS

TO-92 CASE 29-11 **ISSUE AL**





- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

| | INCHES | | MILLIN | IETERS |
|-----|--------|-------|--------|--------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.175 | 0.205 | 4.45 | 5.20 |
| В | 0.170 | 0.210 | 4.32 | 5.33 |
| С | 0.125 | 0.165 | 3.18 | 4.19 |
| D | 0.016 | 0.021 | 0.407 | 0.533 |
| G | 0.045 | 0.055 | 1.15 | 1.39 |
| Н | 0.095 | 0.105 | 2.42 | 2.66 |
| J | 0.015 | 0.020 | 0.39 | 0.50 |
| K | 0.500 | | 12.70 | |
| L | 0.250 | | 6.35 | |
| N | 0.080 | 0.105 | 2.04 | 2.66 |
| Р | | 0.100 | | 2.54 |
| R | 0.115 | | 2.93 | |
| V | 0.135 | | 3.43 | |

STYLE 22: PIN 1. SOURCE 2. GATE 3. DRAIN



VN0300L

ON Semiconductor and **W** are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights or the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products or any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

NORTH AMERICA Literature Fulfillment:

- Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA
- Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: ONlit@hibbertco.com

Fax Response Line: 303–675–2167 or 800–344–3810 Toll Free USA/Canada

N. American Technical Support: 800–282–9855 Toll Free USA/Canada

- EUROPE: LDC for ON Semiconductor European Support German Phone: (+1) 303–308–7140 (Mon–Fri 2:30pm to 7:00pm CET)
- Email: ONlit-germa @hibberto.com French Phone: (+1) 303–308–7141 (Mon–Fri 2:00pm to 7:00pm CET)
- Email: ONlit-french@hibbertco.com
- English Phone: (+1) 303–308–7142 (Mon–Fri 12:00pm to 5:00pm GMT) Email: ONlit@hibbertco.com

EUROPEAN TOLL-FREE ACCESS*: 00-800-4422-3781 *Available from Germany, France, Italy, UK, Ireland

CENTRAL/SOUTH AMERICA:

Spanish Phone: 303–308–7143 (Mon–Fri 8:00am to 5:00pm MST) Email: ONlit–spanish@hibbertco.com Toll–Free from Mexico: Dial 01–800–288–2872 for Access – then Dial 866–297–9322

- ASIA/PACIFIC: LDC for ON Semiconductor Asia Support Phone: 303–675–2121 (Tue–Fri 9:00am to 1:00pm, Hong Kong Time) Toll Free from Hong Kong & Singapore: 001–800–4422–3781 Email: ONlit–asia@hibbertco.com
- JAPAN: ON Semiconductor, Japan Customer Focus Center 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan 141–0031 Phone: 81–3–5740–2700 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local Sales Representative.

VN0300L/D