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STP15NK50Z/FP - STB15NK50Z STB15NK50Z-1 - STW15NK50Z

N-channel 500V - 0.30Ω - 14A TO-220/FP/D²PAK/I²PAK/TO-247
Zener-protected SuperMESH™ Power MOSFET

General features

Type	V _{DSS}	R _{DS(on)}	I _D	P _w
STP15NK50Z	500V	<0.34Ω	14A	160 W
STP15NK50ZFP	500V	<0.34Ω	14A	40 W
STB15NK50Z	500V	<0.34Ω	14A	160W
STB15NK50Z-1	500V	<0.34Ω	14A	160W
STW15NK50Z	500V	<0.34Ω	14A	160W

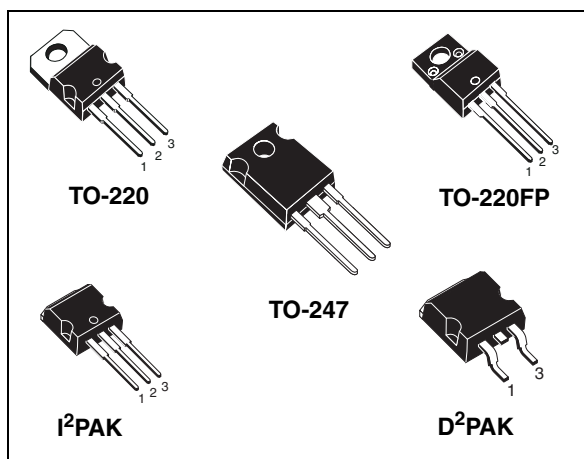
- Extremely high dv/dt capability
- 100% avalanche tested
- Gate charge minimized
- Very low intrinsic capacitances
- Very good manufacturing repeatability

Description

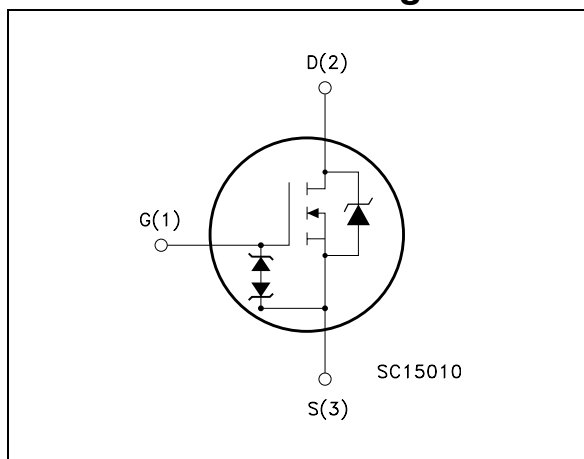
The SuperMESH™ series is obtained through an extreme optimization of ST's well established strip-based PowerMESH™ layout. In addition to pushing on-resistance significantly down, special care is taken to ensure a very good dv/dt capability for the most demanding applications. Such series complements ST full range of high voltage MOSFETs including revolutionary MDmesh™ products.

Applications

- Switching application



Internal schematic diagram



Order codes

Part number	Marking	Package	Packaging
STP15NK50Z	P15NK50Z	TO-220	Tube
STP15NK50ZFP	P15NK50ZFP	TO-220FP	Tube
STB15NK50ZT4	B15NK50Z	D ² PAK	Tape & reel
STB15NK50Z	B15NK50Z	D ² PAK	Tube
STB15NK50Z-1	B15NK50Z	I ² PAK	Tube
STW15NK50Z	W15NK50Z	TO-247	Tube

Contents

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STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z Electrical ratings

1 Electrical ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Value		Unit
		TO-220/D ² PAK I ² PAK/TO-247	TO-220FP	
V _{DS}	Drain-source voltage (V _{GS} = 0)	500		V
V _{DGR}	Drain-gate voltage (R _{GS} = 20kΩ)	500		V
V _{GS}	Gate-source voltage	± 30		V
I _D	Drain current (continuous) at T _C = 25°C	14	14 ⁽¹⁾	A
I _D	Drain current (continuous) at T _C =100°C	8.8	8.8 ⁽¹⁾	A
I _{DM} ⁽²⁾	Drain current (pulsed)	56	56 ⁽¹⁾	A
P _{TOT}	Total dissipation at T _C = 25°C	160	40	W
	Derating Factor	1.28	0.32	W/°C
I _{GS}	Gate-source current (DC)	± 20		mA
Vesd(G-S)	G-S ESD (HBM C=100pF, R=1.5kΩ)	4000		KV
dv/dt ⁽³⁾	Peak diode recovery voltage slope	4.5		V/ns
V _{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink (t=1s; T _C =25°C)	--	2500	V
T _J T _{stg}	Operating junction temperature Storage temperature	-50 to 150°C		°C

1. Limited only by maximum temperature allowed
2. Pulse width limited by safe operating area
3. I_{SD} ≤ 4A, di/dt ≤ 200A/μs, V_{DD} ≤ V_{(BR)DSS}, T_J ≤ T_{JMAX}

Table 2. Thermal data

Symbol	Parameter	Value				Unit
		TO-220 I ² PAK	D ² PAK	TO-220FP	TO-247	
R _{thj-case}	Thermal resistance junction-case Max	0.78		3.1	0.78	°C/W
R _{thj-pcb} ⁽¹⁾	Thermal resistance junction-pcb max		60			°C/W

Electrical ratings STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z

Table 2. Thermal data

R_{thj-a}	Thermal resistance junction-ambient max	62.5	50	°C/W
T_l	Maximum lead temperature for soldering purpose	300		°C

1. When mounted on minimum foot-print

Table 3. Avalanche characteristics

Symbol	Parameter	Value	Unit
I_{AR}	Avalanche current, repetitive or not-repetitive (pulse width limited by T_j Max)	14	A
E_{AS}	Single pulse avalanche energy (starting $T_j=25^{\circ}\text{C}$, $I_d=I_{AR}$, $V_{DD}=50\text{V}$)	300	mJ

Table 4. Gate-source zener diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$BV_{GSO}^{(1)}$	Gate-source breakdown voltage	$I_{gs}=\pm 1\text{mA}$ (Open Drain)	30			V

1. The built-in back-to-back Zener diodes have specifically been designed to enhance not only the device's ESD capability, but also to make them safely absorb possible voltage transients that may occasionally be applied from gate to source. In this respect the Zener voltage is appropriate to achieve an efficient and cost-effective intervention to protect the device's integrity. These integrated Zener diodes thus avoid the usage of external components.

STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z Electrical charac-

2 Electrical characteristics

($T_{CASE}=25^{\circ}C$ unless otherwise specified)

Table 5. On/off states

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_D = 1mA, V_{GS} = 0$	500			V
I_{DSS}	Zero gate voltage drain current ($V_{GS} = 0$)	$V_{DS} = \text{Max rating},$ $V_{DS} = \text{Max rating} @ 125^{\circ}C$			1 50	μA μA
I_{GSS}	Gate body leakage current ($V_{DS} = 0$)	$V_{GS} = \pm 20V$			± 10	μA
$V_{GS(th)}$	Gate threshold voltage	$V_{DS} = V_{GS}, I_D = 100\mu A$	3	3.75	4.5	V
$R_{DS(on)}$	Static drain-source on resistance	$V_{GS} = 10V, I_D = 7A$		0.30	0.34	Ω

Table 6. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$g_{fs}^{(1)}$	Forward transconductance	$V_{DS} = 15V, I_D = 7A$		12		S
C_{iss} C_{oss} C_{rss}	Input capacitance Output capacitance Reverse transfer capacitance	$V_{DS} = 25V, f = 1 \text{ MHz}, V_{GS} = 0$		2260 264 64		pF pF pF
$C_{oss \text{ eq}}^{(2)}$	Equivalent output capacitance	$V_{GS} = 0, V_{DS} = 0V \text{ to } 400V$		150		pF
Q_g Q_{gs} Q_{gd}	Total gate charge Gate-source charge Gate-drain charge	$V_{DD} = 400V, I_D = 14A$ $V_{GS} = 10V$		76 15 40	106	nC nC nC
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	Turn-on delay time Rise time Turn-off delay time Fall time	$V_{DD} = 250V, I_D = 7A,$ $R_G = 4.7\Omega, V_{GS} = 10V$ (see Figure 18)		20 23 62 15		ns ns ns ns

1. Pulsed: pulse duration=300 μs , duty cycle 1.5%

2. $C_{oss \text{ eq}}$ is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}

Electrical characteristics

STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 -

Table 7. Source drain diode

Symbol	Parameter	Test conditions	Min	Typ.	Max	Unit
I_{SD}	Source-drain current			14		A
$I_{SDM}^{(1)}$	Source-drain current (pulsed)			56		A
$V_{SD}^{(2)}$	Forward on voltage	$I_{SD}=14A$, $V_{GS}=0$			1.6	V
t_{rr}	Reverse recovery time	$I_{SD}=14A$, $di/dt = 100A/\mu s$, $V_{DD}=29V$, $T_j=150^\circ C$		428		ns
Q_{rr}	Reverse recovery charge			4.2		μC
I_{RRM}	Reverse recovery current			20		A

1. Pulse width limited by safe operating area
2. Pulsed: pulse duration=300 μs , duty cycle 1.5%

STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z Electrical charac-

2.1 Electrical characteristics (curves)

Figure 1. Safe operating area for TO-220 D2PAK/I2PAK

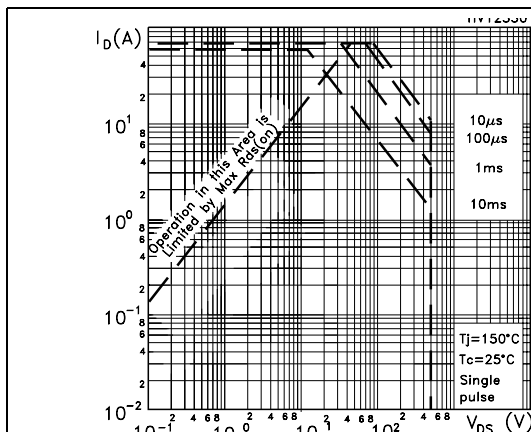


Figure 2. Thermal impedance for TO- 220 D2PAK/I2PAK

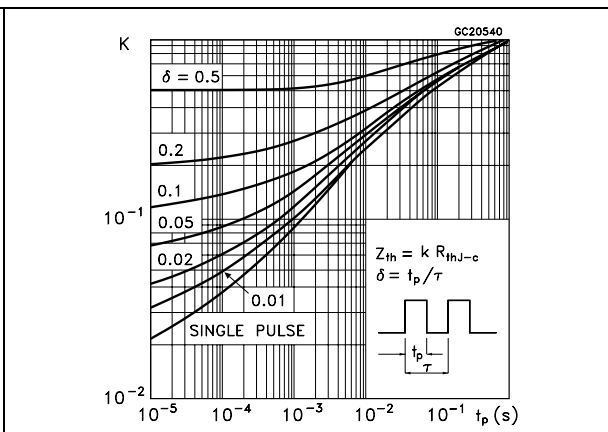


Figure 3. Safe operating area for TO-220FP

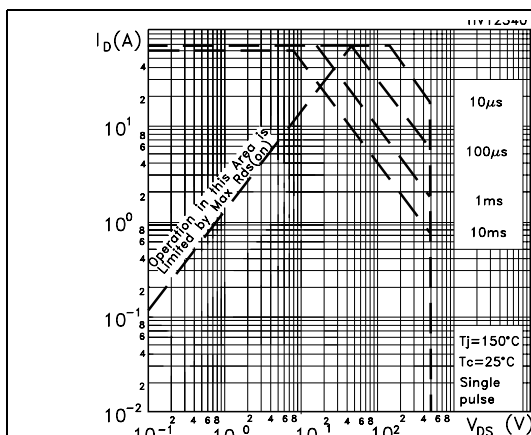


Figure 4. Thermal impedance for TO-220FP

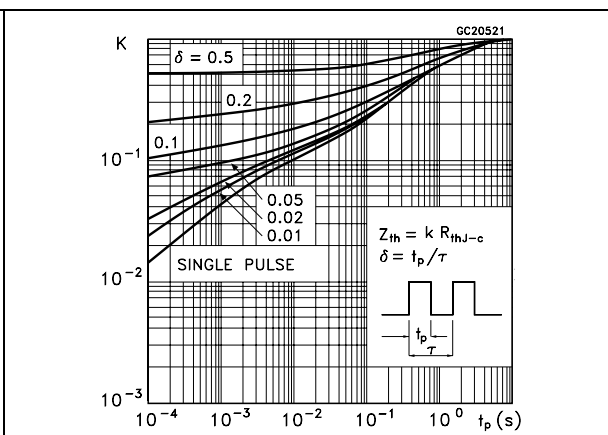


Figure 5. Safe operating area for TO-247

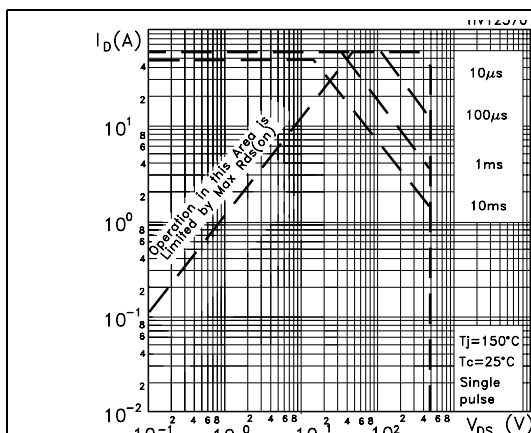
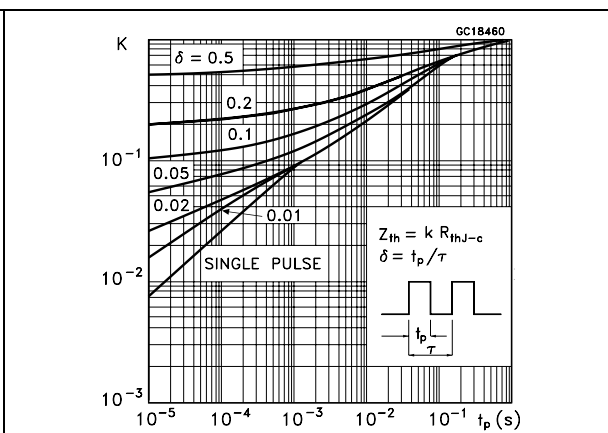


Figure 6. Thermal impedance for TO-247



Electrical characteristics

STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 -

Figure 7. Output characteristics

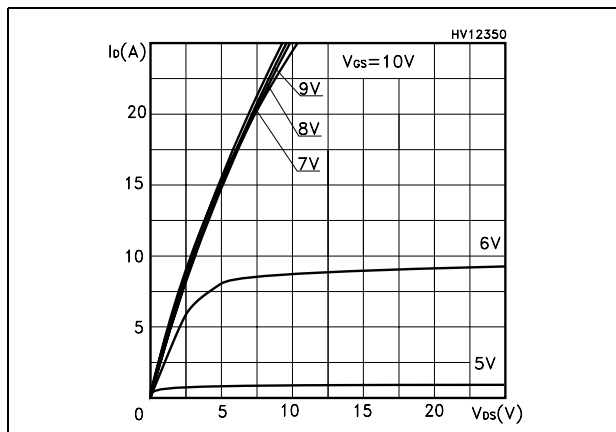


Figure 8. Transfer characteristics

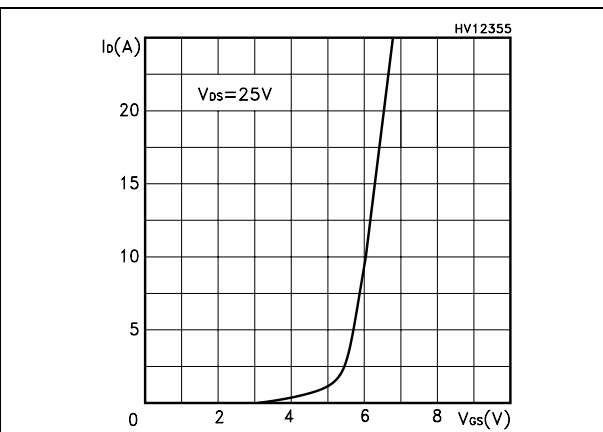


Figure 9. Transconductance

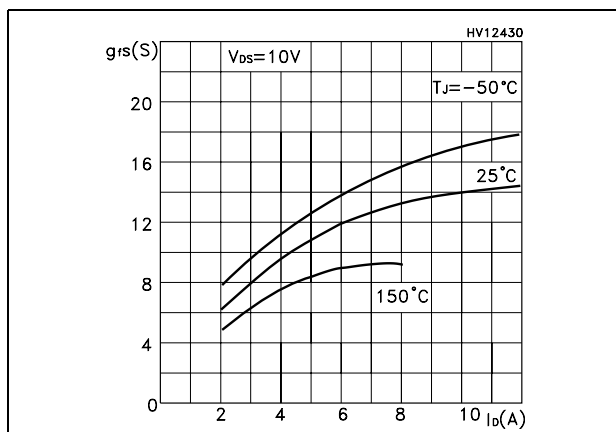


Figure 10. Static drain-source on resistance

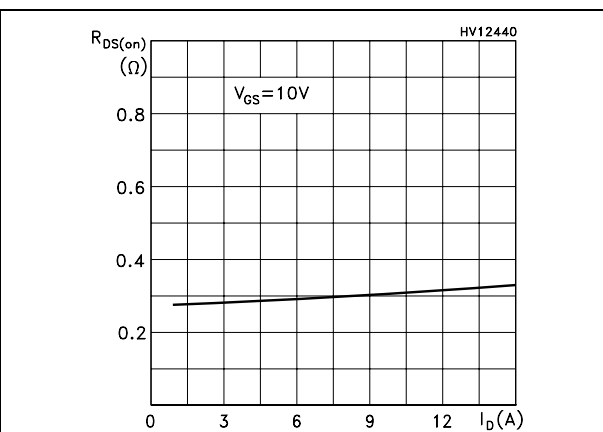


Figure 11. Gate charge vs gate-source voltage

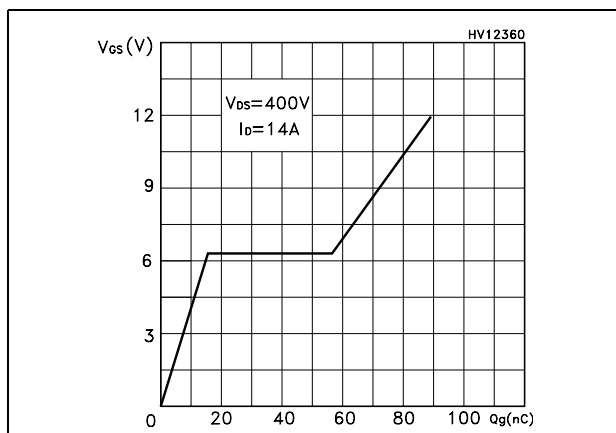
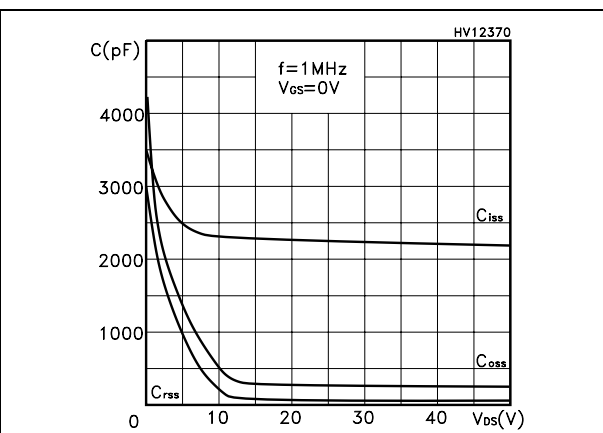


Figure 12. Capacitance variations



STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z Electrical charac-

Figure 13. Normalized gate threshold voltage vs temperature

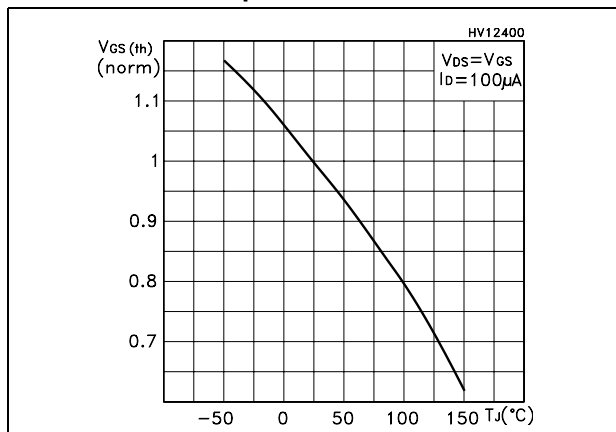


Figure 14. Normalized on resistance vs temperature

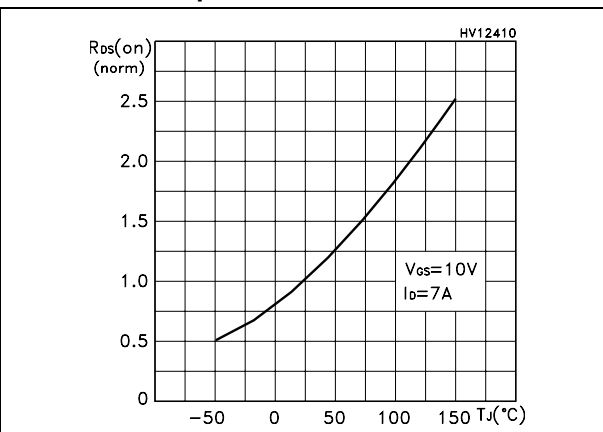


Figure 15. Source-drain diode forward characteristics

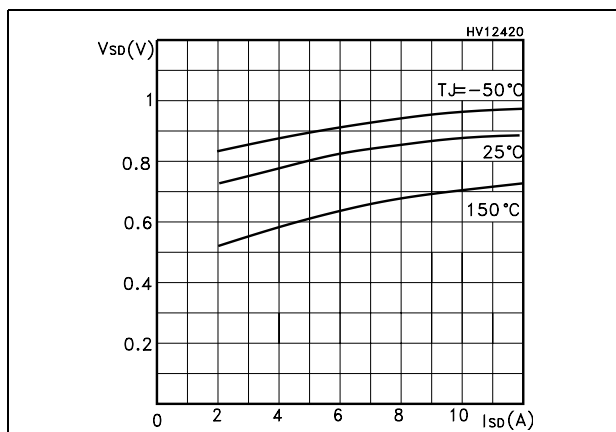


Figure 16. Normalized BV_{DSS} vs temperature

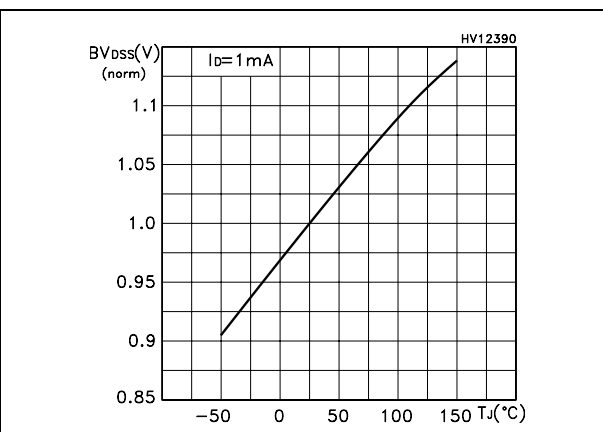
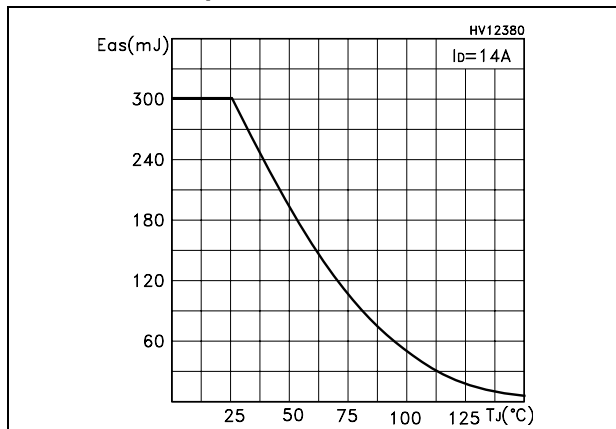


Figure 17. Maximum avalanche energy vs temperature



Test circuit **STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z**

3 Test circuit

Figure 18. Switching times test circuit for resistive load

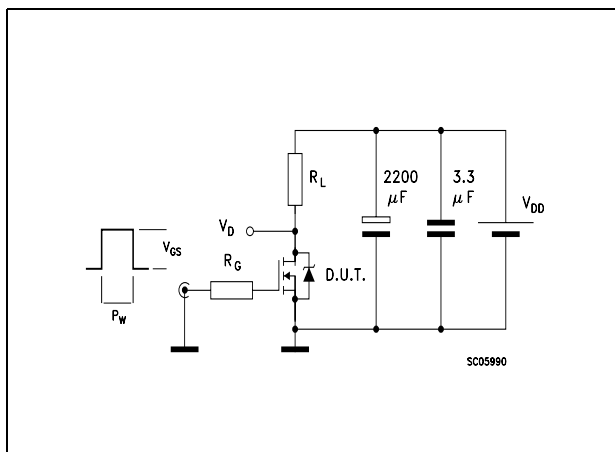


Figure 19. Gate charge test circuit

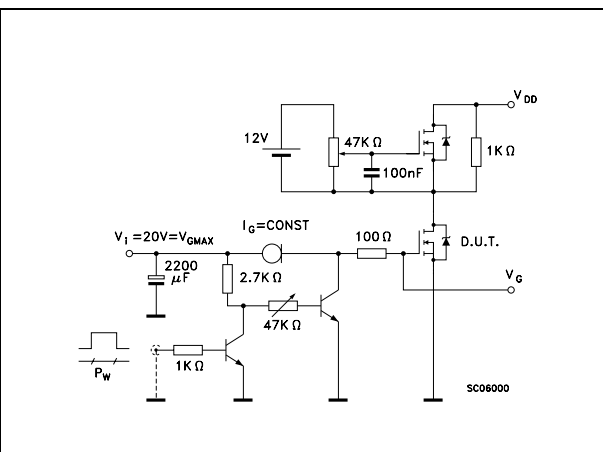


Figure 20. Test circuit for inductive load switching and diode recovery times

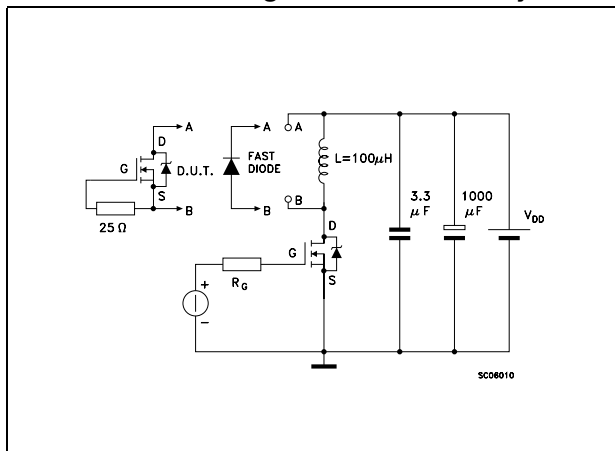


Figure 21. Unclamped Inductive load test circuit

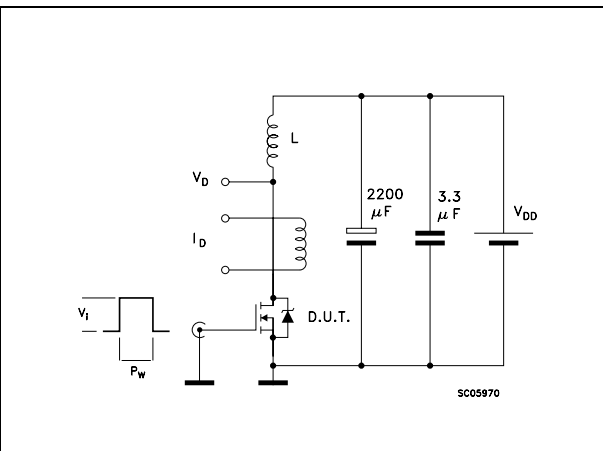


Figure 22. Unclamped inductive waveform

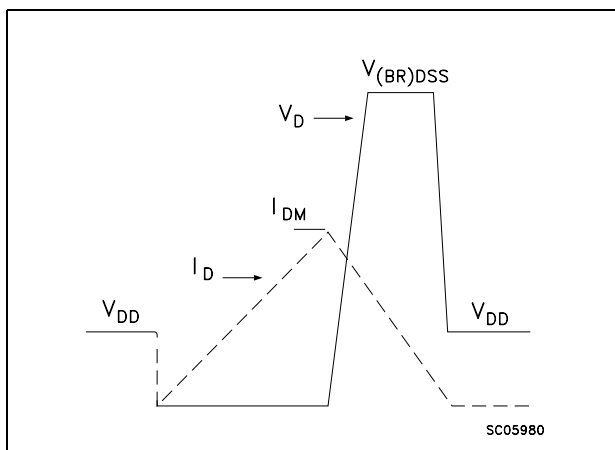
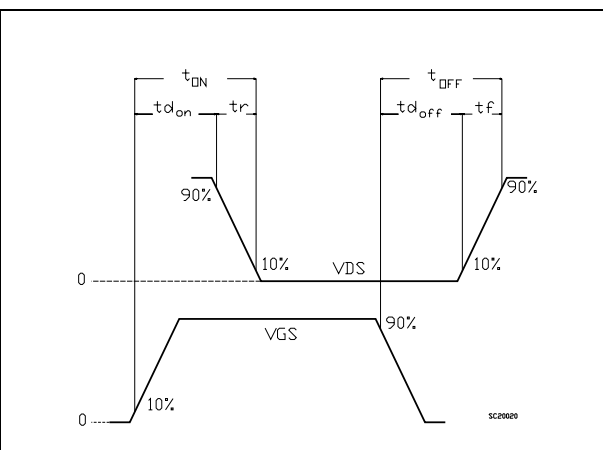


Figure 23. Switching time waveform



STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z Package mechani-

4 Package mechanical data

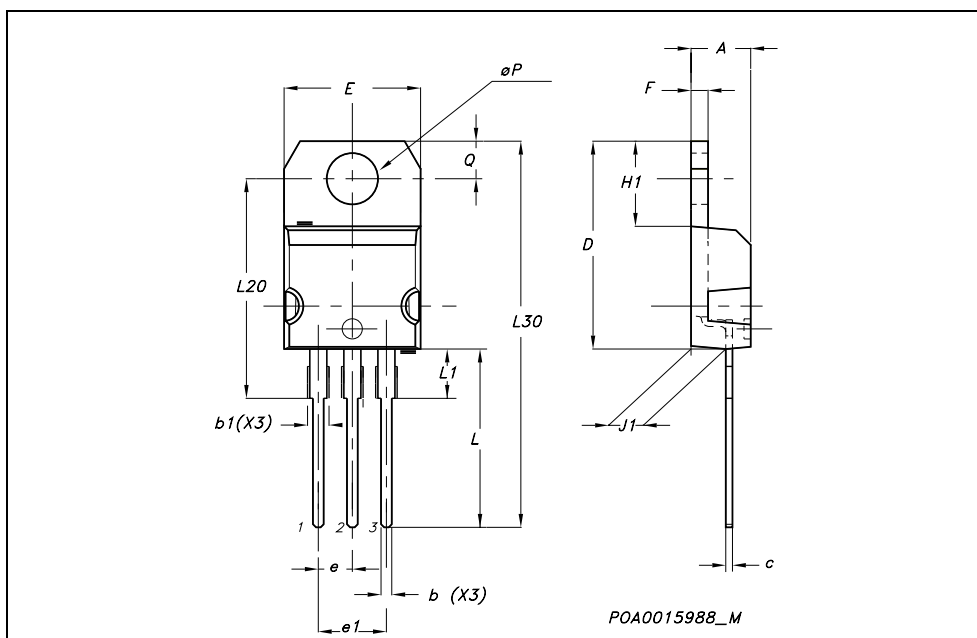
In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

Package mechanical data

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TO-220 MECHANICAL DATA

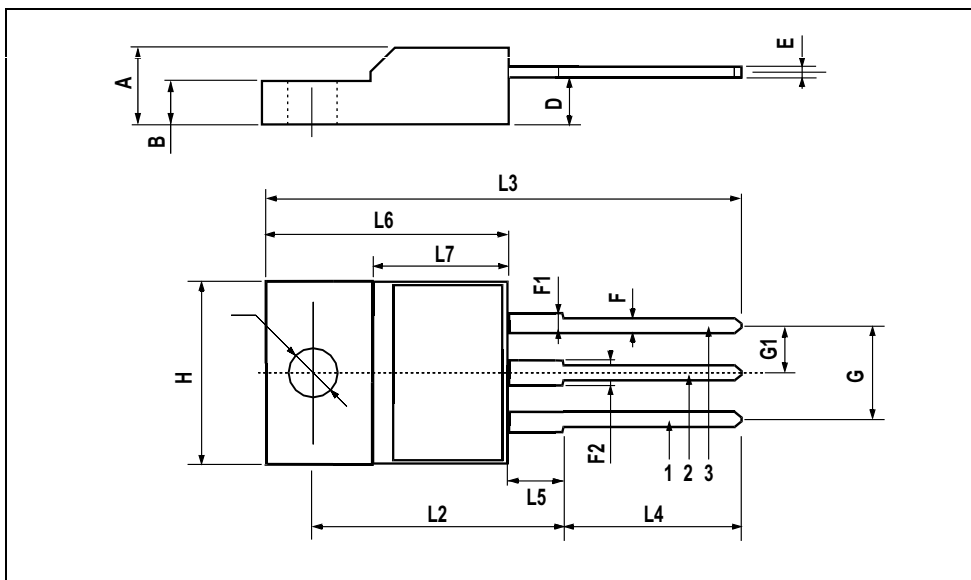
DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.15		1.70	0.045		0.066
c	0.49		0.70	0.019		0.027
D	15.25		15.75	0.60		0.620
E	10		10.40	0.393		0.409
e	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.052
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
øP	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z Package mechani-

TO-220FP MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
B	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
H	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	.0385		0.417
L5	2.9		3.6	0.114		0.141
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126

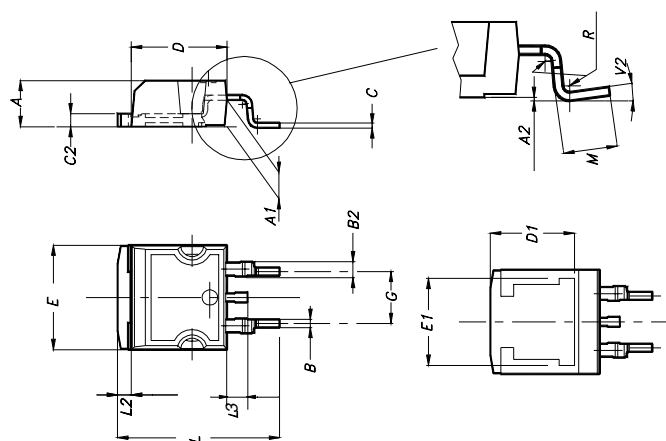


Package mechanical data

STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 -

D²PAK MECHANICAL DATA

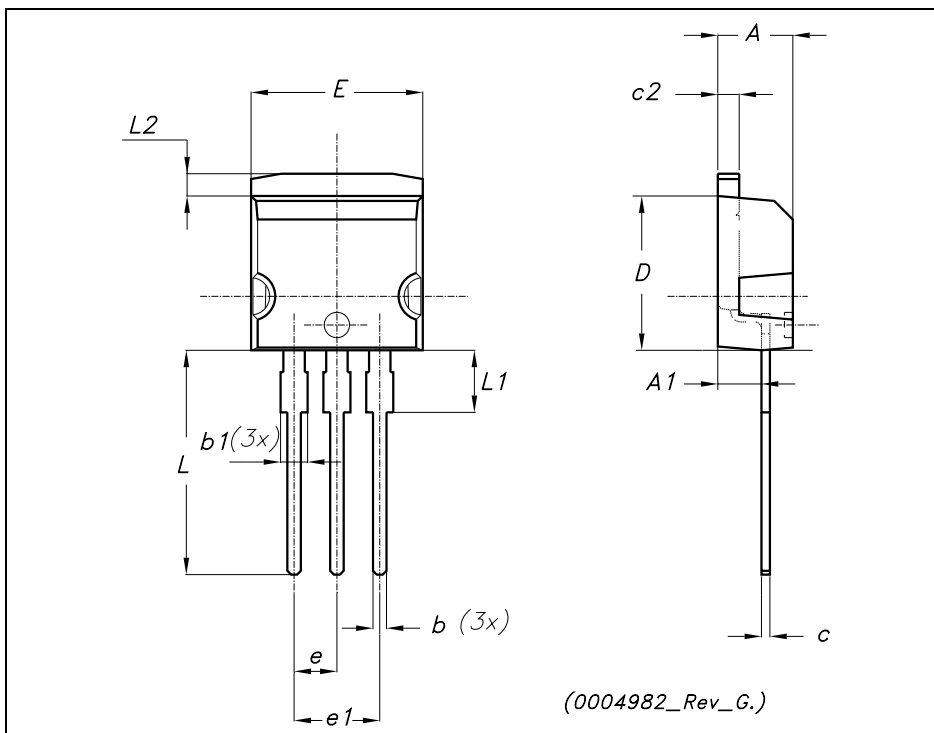
DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
B	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
C	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
M	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	0°		4°			



STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z Package mechani-

TO-262 (I²PAK) MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
A1	2.40		2.72	0.094		0.107
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
c	0.49		0.70	0.019		0.027
c2	1.23		1.32	0.048		0.052
D	8.95		9.35	0.352		0.368
e	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
E	10		10.40	0.393		0.410
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L2	1.27		1.40	0.050		0.055

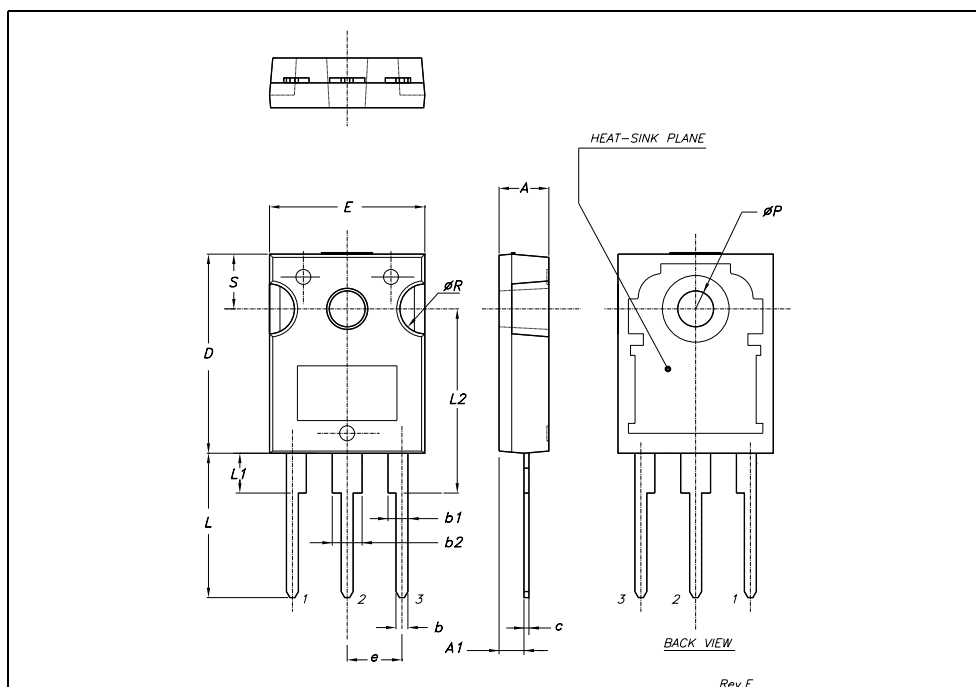


Package mechanical data

STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 -

TO-247 MECHANICAL DATA

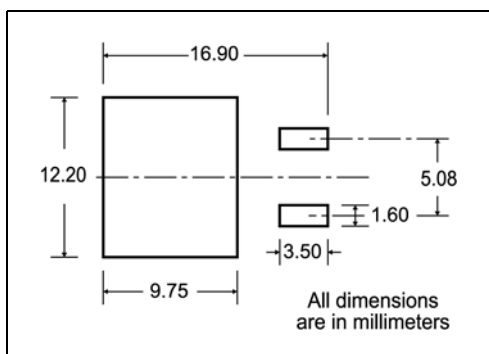
DIM.	mm.			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.85		5.15	0.19		0.20
A1	2.20		2.60	0.086		0.102
b	1.0		1.40	0.039		0.055
b1	2.0		2.40	0.079		0.094
b2	3.0		3.40	0.118		0.134
c	0.40		0.80	0.015		0.03
D	19.85		20.15	0.781		0.793
E	15.45		15.75	0.608		0.620
e		5.45			0.214	
L	14.20		14.80	0.560		0.582
L1	3.70		4.30	0.14		0.17
L2		18.50			0.728	
øP	3.55		3.65	0.140		0.143
øR	4.50		5.50	0.177		0.216
S		5.50			0.216	



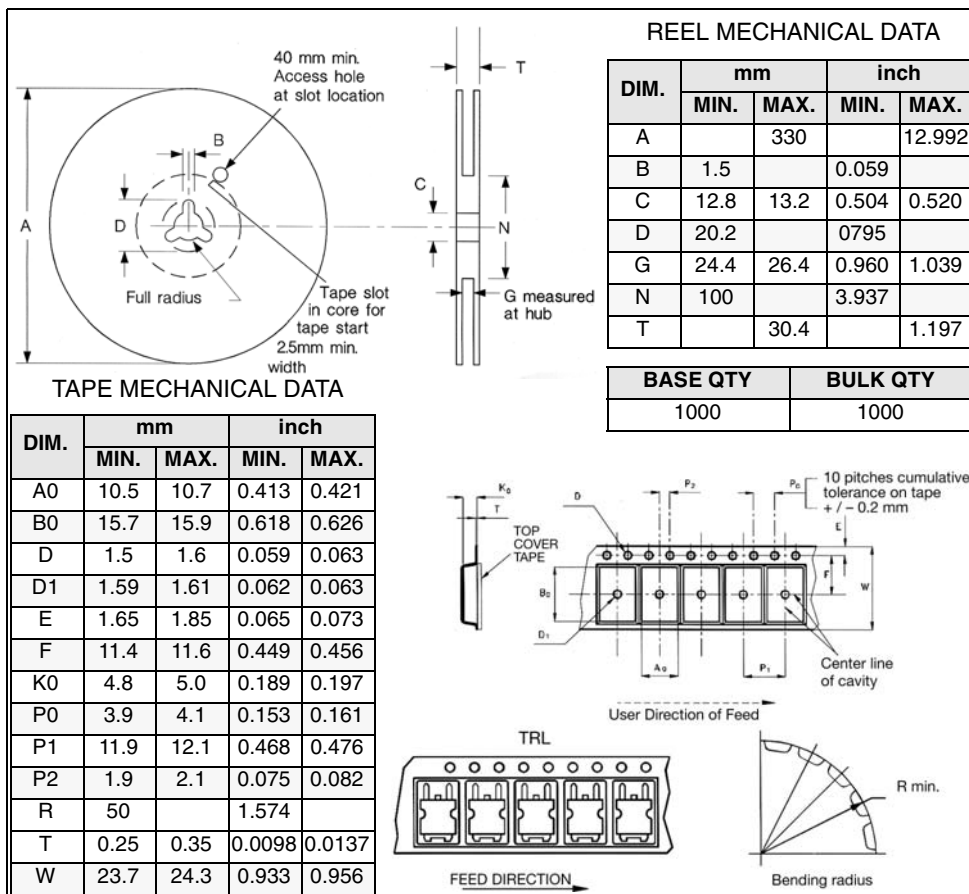
STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z Packaging me-

5 Packaging mechanical data

D²PAK FOOTPRINT



TAPE AND REEL SHIPMENT



* on sales type

Revision history STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z

6 Revision history

Table 8. Revision history

Date	Revision	Changes
21-Jun-2004	2	Complete version
20-Jul-2006	3	New template, no content change
05-Jan-2007	4	Modified unit on <i>On/off states</i>

STP15NK50Z - STP15NK50ZFP - STB15NK50Z - STB15NK50Z-1 - STW15NK50Z

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