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EPCOS (TDK) B49410B2126Q000

For any questions, you can email us directly: sales@integrated-circuit.com



Distributor of EPCOS (TDK): Excellent Integrated System Limited

Datasheet of B49410B2126Q000 - CAP 1200F -10% +30% 2.5V CHAS MT

Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



UltraCap[®]

Single cell 1200 F/ 2.5 V

Series/Type:

Ordering code: B49410B2126Q000

Date: March 2005

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UltraCap[®] B49410B2126Q000

Single cell, 1200 F/ 2.5 V

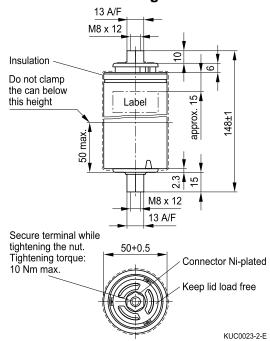
Features

- Screw terminal M8 × 12
- Power type
- Insulated with polyurethane
- Short-circuit-proof

Note

- Do not put into fire!
- Do not open the capacitor!
- To avoid health and fire hazards, do not operate the capacitor beyond the voltage or temperature limits given in the data sheet. Any excess may also result in a reduction of lifetime.
- Please pay also attention to the transport and waste disposal instructions in chapter "Cautions".

Dimensional drawing



Dimensions in mm

Electrical specifications

Rated capacitance	(T _A = 25 °C; DCC) ¹⁾	C _R	1200	F
Tolerance of C _R			-10/+30	%
Rated voltage	(T _A = 25 °C)	V_R	2.5	V
Capacity			830	mAh
Specific power	(IEC 62391-2)		3.0	kW/kg
Specific power	(IEC 62391-2)		4.3	kW/I
Stored energy	$(V = V_R)$	E	3750	J
Specific energy	$(V = V_R)$		3.2	Wh/kg
Specific energy	$(V = V_R)$		4.4	Wh/I
Surge voltage		V_{surge}	2.8	V
Maximum series resistance	$(T_A = 25 ^{\circ}C; 1 \text{kHz})$	ESR	350	$\mu\Omega$
Maximum series resistance	$(T_A = 25 ^{\circ}C; 50 \text{mHz})$	ESR _{DC}	750	$\mu\Omega$
Weight			330	g
Volume	(without terminals)		0.23	1
Operating temperature range		T_{op}	-30/+70	°C
Storage temperature	(V = 0 V)	T_{st}	-40/+70	°C
Lifetime (hours) 2)	$(T_A = 25 {}^{\circ}C; V = V_R)$		90000	h
Lifetime (cycles) 3)	$(T_A = 25 ^{\circ}C; I = 50 A)$		500000	cycles

¹⁾ DCC: discharging with constant current.

²⁾ Requirements: $|\Delta C/C_R| \le 30\%$, ESR ≤ 2 times of specified limit, $I_{leak} \le 2$ times of initial value.

³⁾ Requirements: $|\Delta C/C_R| \le 30\%$, ESR ≤ 2 times of specified limit, $I_{leak} \le 2$ times of initial value (1 cycle: charging to V_R , 30 s rest, discharging to $V_R/2$, 30 s rest).