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[MMFT2406T1](#)

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MMFT2406T1

Power MOSFET

700 mA, 240 V, N-Channel, SOT-223

This Power MOSFET is designed for high speed, low loss power switching applications such as switching regulators, converters, solenoid and relay drivers. The device is housed in the SOT-223 package which is designed for medium power surface mount applications.

- Silicon Gate for Fast Switching Speeds
- High Voltage – 240 Vdc
- Low Drive Requirement
- The SOT-223 Package can be soldered using wave or reflow. The formed leads absorb thermal stress during soldering, eliminating the possibility of damage to the die.
- Pb-Free Packages are Available

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DS}	240	Vdc
Gate-to-Source Voltage – Continuous	V _{GS}	±20	Vdc
Drain Current	I _D	700	mAdc
Total Power Dissipation @ T _A = 25°C (Note 1) Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-65 to 150	°C

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance – Junction-to-Ambient (surface mounted) (Note 1)	R _{θJA}	83.3	°C/W
Lead Temperature for Soldering Purposes, 1/16" from case Time in Solder Bath	T _L	260 10	°C Sec

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Device mounted on a glass epoxy printed circuit board 1.575 in x 1.575 in x 0.059 in; mounting pad for the collector lead min. 0.93 sq in.

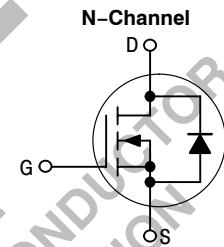


ON Semiconductor®

<http://onsemi.com>

700 mA, 240 V

R_{DS(on)} = 6.0 Ω



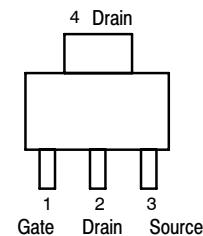
MARKING DIAGRAM

TO-223
CASE 318E
STYLE 3

T2406
AWW

A = Assembly Location
WW = Work Week

PIN ASSIGNMENT



ORDERING INFORMATION

Device	Package	Shipping [†]
MMFT2406T1	SOT-223	1000 Tape & Reel
MMFT2406T1G	SOT-223 (Pb-Free)	1000 Tape & Reel
MMFT2406T3	SOT-223	4000 Tape & Reel
MMFT2406T3G	SOT-223 (Pb-Free)	2500 Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMFT2406T1

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristics	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage ($V_{GS} = 0$, $I_D = 100 \mu\text{A}$)	$V_{(\text{BR})\text{DSS}}$	240	-	Vdc
Zero Gate Voltage Drain Current ($V_{DS} = 120 \text{ V}$, $V_{GS} = 0$)	I_{DSS}	-	10	μA dc
Gate-Body Leakage Current ($V_{GS} = 15 \text{ Vdc}$, $V_{DS} = 0$)	I_{GSS}	-	100	nAdc

ON CHARACTERISTICS (Note 2)

Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 1.0 \text{ mA}$)	$V_{GS(\text{th})}$	0.8	2.0	Vdc
Static Drain-to-Source On-Resistance ($V_{GS} = 2.5 \text{ Vdc}$, $I_D = 0.1 \text{ Adc}$) ($V_{GS} = 10 \text{ Vdc}$, $I_D = 0.5 \text{ Adc}$)	$R_{DS(\text{on})}$	- -	10 6.0	Ω
Drain-to-Source On-Voltage ($V_{GS} = 10 \text{ V}$, $I_D = 0.5 \text{ A}$)	$V_{DS(\text{on})}$	-	3.0	Vdc
Forward Transconductance ($V_{DS} = 6.0 \text{ V}$, $I_D = 0.5 \text{ A}$)	g_{FS}	300	-	mmhos

DYNAMIC CHARACTERISTICS

Input Capacitance	($V_{DS} = 25 \text{ V}$, $V_{GS} = 0$, $f = 1.0 \text{ MHz}$)	C_{iss}	-	125	pF
Output Capacitance		C_{oss}	-	50	
Transfer Capacitance		C_{rss}	-	20	

2. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

OBSOLETE

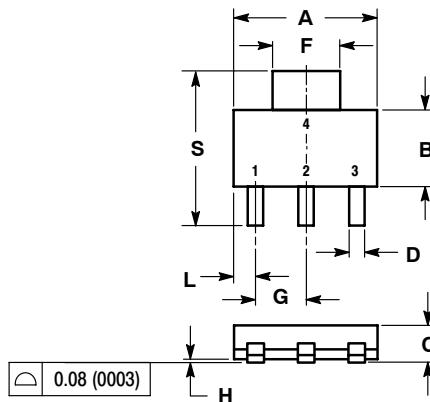
THIS DEVICE IS OBSOLETE

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MMFT2406T1

PACKAGE DIMENSIONS

SOT-223 (TO-261)
CASE 318E-04
ISSUE K

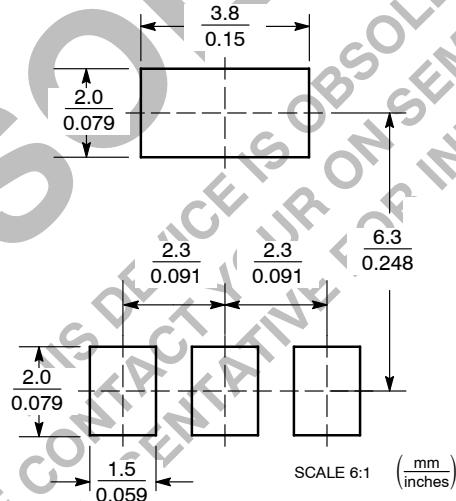


NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.249	0.263	6.30	6.70
B	0.130	0.145	3.30	3.70
C	0.060	0.068	1.50	1.75
D	0.024	0.035	0.60	0.89
F	0.115	0.126	2.90	3.20
G	0.087	0.094	2.20	2.40
H	0.008	0.0040	0.020	0.100
J	0.009	0.014	0.24	0.35
K	0.060	0.078	1.50	2.00
L	0.033	0.041	0.85	1.05
M	0 °	10 °	0 °	10 °
S	0.264	0.287	6.70	7.30

STYLE 3:
PIN 1. GATE
2. DRAIN
3. SOURCE
4. DRAIN

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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