

# **Excellent Integrated System Limited**

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Vishay Semiconductor/Diodes Division VS-30BQ060PBF

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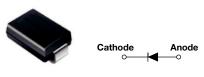




### VS-30BQ060PbF

Vishay High Power Products

# Schottky Rectifier, 3.0 A



3.0 A

60 V

SMC

**PRODUCT SUMMARY** 

I<sub>F(AV)</sub>

 $V_{\mathsf{R}}$ 

### FEATURES

• Small foot print, surface mountable



COMPLIANT

- Very low forward voltage dropHigh frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

### DESCRIPTION

The VS-30BQ060PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I <sub>F(AV)</sub>	Rectangular waveform	3.0	А		
V <sub>RRM</sub>		60	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1200	А		
V <sub>F</sub>	3.0 Apk, T <sub>J</sub> = 125 °C	0.52	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-30BQ060PbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	60	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	00	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	ARAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum avarage forward aurrent		50 % duty cycle at T <sub>L</sub> = 123 °C, rectangular waveform		3.0	
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 113 °C, rectangular waveform		4.0	
Maximum peak one cycle	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	1200	A
non-repetitive surge current at $T_{C} = 25 \ ^{\circ}C$		10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	130	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.0 A, L = 10 mH		5.0	mJ
Repetitive avalanche current $I_{AR}$ Current decaying linearly to zero in 1 µs Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		1.0	А		



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		3 A	T.I = 25 °C	0.58	v
Maximum forward voltage drop	V <sub>EM</sub> <sup>(1)</sup>	6 A	1J = 25°C 0.76	0.76	
Maximum forward voltage drop	VFM \''	3 A	T.I = 125 °C	0.52	
		6 A	1j=125 C	0.66	
	$T_{\rm J} = 2$	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.5	mA
Maximum reverse leakage current	IRM (")	T <sub>J</sub> = 125 °C		20	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to1 MHz), 25 °C		180	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	rate of change dV/dt Rated V <sub>R</sub>		10 000	V/µs	

#### Note

 $^{(1)}$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T <sub>J</sub> <sup>(1)</sup>		- 55 to 150	°C
Maximum storage temperature range	T <sub>Stg</sub>			U
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>			°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	46	0/11
An exercise at a successful			0.24	g
Approximate weight			0.008	oz.
Marking device		Case style SMC (similar to DO-214AB)	V3	H

#### Notes

<sup>(1)</sup>  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



10

1

0

10



# VS-30BQ060PbF

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T<sub>J</sub> = 150 °C

20

T<sub>J</sub> = 125 °C

= 100 °C

= 25 °C

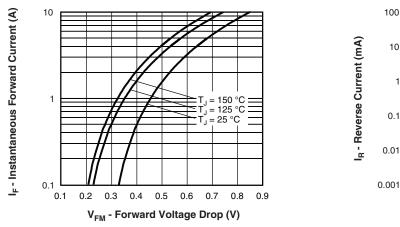
T<sub>1</sub> = 75 °C

40

Γ<sub>J</sub> = 50 °C

50

60





V<sub>R</sub> - Reverse Voltage (V) Fig. 2 - Typical Values of Reverse Current vs.

30

Reverse Voltage (Per Leg)

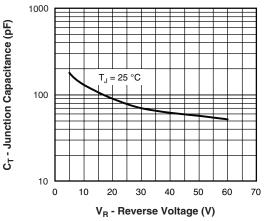


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

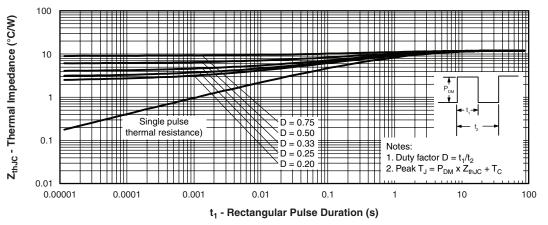


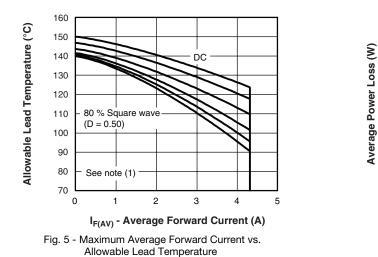
Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

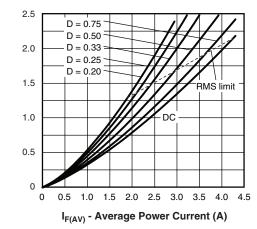


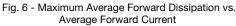
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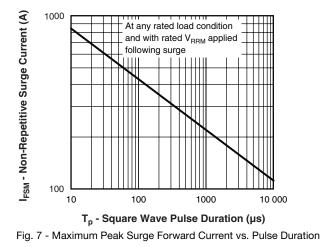
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#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 D); } \mbox{I}_{R} \mbox{ at } \mbox{V}_{R1} = \mbox{80 \% rated } \mbox{V}_{R} \end{array}$

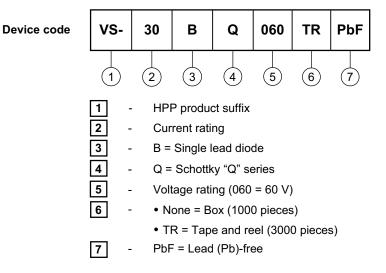




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LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95023		www.vishay.com/doc?95023	
Part marking information		www.vishay.com/doc?95029	
Packaging information	Tape and reel	www.vishay.com/doc?95034	
	Bulk	www.vishay.com/doc?95397	



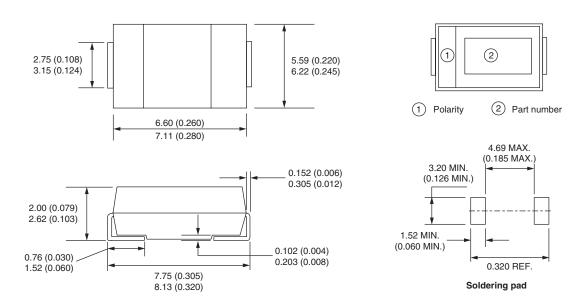


# **Outline Dimensions**

Vishay High Power Products

SMC

### **DIMENSIONS** in millimeters (inches)







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