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<u>Vishay Semiconductor/Diodes Division</u> <u>FCSP0530ETR</u>

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Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite Datasheet of FCSP0530ETR - DIODE SCHOTTKY 30V 500MA FLIPKY

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VS-FCSP0530ETR

Vishay Semiconductors

FlipKY[®], 0.5 A



FlipKY[®]

PRODUCT SUMMARY			
I _{F(AV)}	0.5 A		
V_{R}	30 V		
V _F at I _F	0.33 V		
I _{RM} max. at 25 °C	50 μΑ		
I _{RM} max. at 125 °C	15 mA		
T _J max.	150 °C		
E _{AS}	5 mJ		

FEATURES

- Ultralow V_F to footprint area
- Very low profile (< 0.6 mm)
- · Low thermal resistance
- · Supplied tested and on tape and reel
- · Designed for consumer level

APPLICATIONS

- Reverse polarity protection
- · Current steering
- · Freewheeling
- Flyback
- Oring

DESCRIPTION

FlipKY® product family utilizes wafer level chip scale packaging to deliver Schottky diodes with the lowest V_F to PCB footprint area in the industry. The three pad 0.9 mm x 1.2 mm devices can deliver up to 0.5 A and occupy only 1.08 mm² of board space. The anode and cathode connections are made through solder bump pads on one side of the silicon rather than through protruding leads enabling designers to strategically place the diodes on the PCB. This design not only minimizes board space but also reduces thermal resistance and inductance, which can improve overall circuit efficiency.

Typical applications include hand-held, portable equipment such as cell phones, MP3 players, PDAs, and portable hard disk drives where space savings and performance are crucial.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	0.5	А		
V _{RRM}		30	V		
I _{FSM}	t _p = 5 μs sine	190	А		
V _F	0.5 A _{pk} , T _J = 125 °C	0.33	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER SYMBOL		VS-FCSP0530ETR	UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V_{RWM}	30	V	

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _{PCB} = 133 °C, rectangular waveform		0.5	
Maximum peak one cycle non-repetitive surge current at T _J = 25 °C		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	190	Α
	IFSM	10 ms sine or 6 ms rect. pulse		10	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2.0 A, L = 5.0 mH		5	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		0.5	Α

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop)/ (1)	0.5 A	T _J = 25 °C	0.40	0.44	V
		1 A		0.45	0.49	
See fig. 1	V _{FM} ⁽¹⁾	0.5 A	T _J = 125 °C	0.29	0.33	
		1 A		0.36	0.39	
Maximum reverse leakage current	nt I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	10	50	μΑ
See fig. 2		T _J = 125 °C		5	15	mA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		-	90	pF
Maximum voltage rate of charge	dV/dt	Rated V _R		-	10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Typical thermal resistance, junction to PCB	R _{thJL} (2)	DC operation	35	°C/W
Typical thermal resistance, junction to ambient	R _{thJA} (2)		150	C/VV

Notes

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted on dual sided 0.58" square FR4 PCB with 0.2 square inches of 1 oz. top copper area

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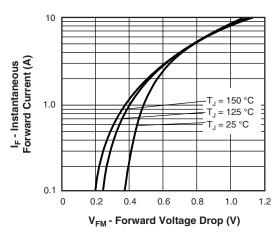


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

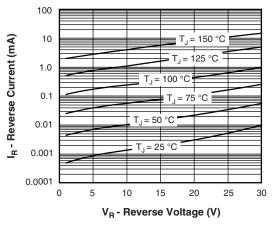


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

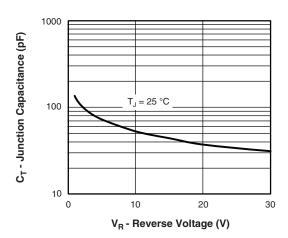


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

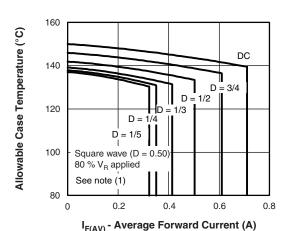


Fig. 4 - Maximum Allowable Case Temperature vs.

Average Forward Current (Per Leg)

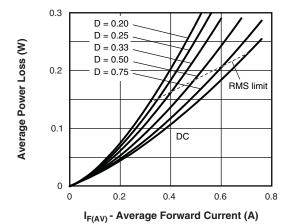


Fig. 5 - Forward Power Loss Characteristics (Per Leg)

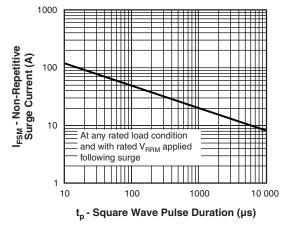


Fig. 6 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at 80 % V_R applied

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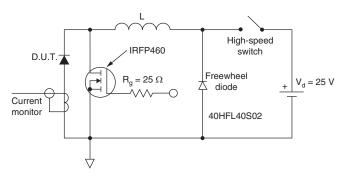


Fig. 7 - Unclamped Inductive Test Circuit

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95049</u>			
Part marking information	www.vishay.com/doc?95060		
Packaging information	www.vishay.com/doc?95062		



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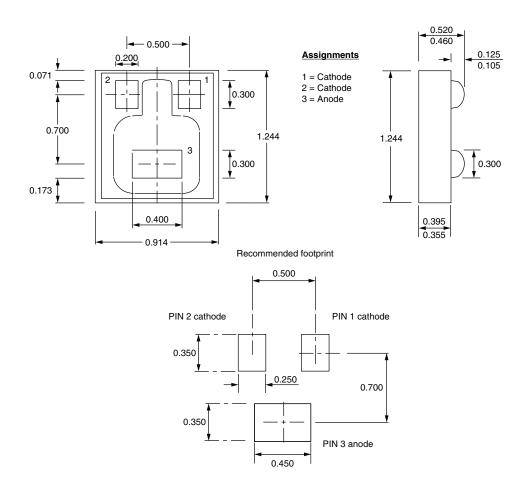


Outline Dimensions

Vishay High Power Products

FlipKY® 0.5 A/0.75 A

DIMENSIONS in millimeters



Notes

- Dimensioning and tolerancing per ASME Y14.5M-1994
- Controlling dimension: millimeter



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