# **General Purpose Transistors**

# **PNP Silicon**

#### **Features**

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CEO</sub>	-45	V
Collector – Base Voltage	V <sub>CBO</sub>	-50	V
Emitter – Base Voltage	V <sub>EBO</sub>	-5.0	V
Collector Current – Continuous	I <sub>C</sub>	-500	mAdc

# THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

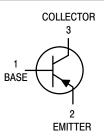
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1.  $FR-5 = 1.0 \times 0.75 \times 0.062$  in.
- 2. Alumina = 0.4 x 0.3 x 0.024 in 99.5% alumina.



# ON Semiconductor®

#### www.onsemi.com





SOT-23 CASE 318 STYLE 6

#### MARKING DIAGRAM



5xx = Device Code xx = A1, B1, or C $M = Date Code^*$ 

= Pb-Free Package(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted.)

Symbol	Min	Тур	Max	Unit
V <sub>(BR)</sub> CEO	-45	_	_	V
V <sub>(BR)</sub> CES	-50	-	-	V
V <sub>(BR)EBO</sub>	-5.0	_	_	V
Ісво	_ _	_ _	-100 -5.0	nA μA
SBC807-25L	100 160 250 40	- - -	250 400 600 –	-
V <sub>CE(sat)</sub>	-	-	-0.7	V
V <sub>BE(on)</sub>	-	-	-1.2	V
	•	•	=	
f⊤	100	-	_	MHz
C <sub>obo</sub>	-	10	-	pF
	V(BR)CEO  V(BR)CES  V(BR)EBO  ICBO  ICBO  NFE  VCE(sat)  VBE(on)	V(BR)CEO -45  V(BR)CES -50  V(BR)EBO -5.0  ICBO	V(BR)CEO -45 -  V(BR)CES -50 -  V(BR)EBO -5.0 -  ICBO  ICBO  SBC807-25L SBC807-40L 250 -  VCE(sat)  VBE(on)  T 100 -  T 100 -	V <sub>(BR)CEO</sub>

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### **ORDERING INFORMATION**

Device	Specific Marking	Package	Shipping <sup>†</sup>		
BC807-16LT1G	504		0000 /Taxa 0 David		
SBC807-16LT1G*	5A1		3000 / Tape & Reel		
BC807-16LT3G	5A1	504	40,000 / Tana 9, Daal		
SBC807-16LT3G*	5A1		10,000 / Tape & Reel		
BC807-25LT1G	5D4		2000 / Tana & Baal		
SBC807-25LT1G*	5B1	SOT-23	3000 / Tape & Reel		
BC807-25LT3G	5B1	(Pb-Free)	10 000 / Tono & Book		
SBC807-25LT3G*	351		10,000 / Tape & Reel		
BC807-40LT1G	5C		3000 / Tape & Reel		
SBC807-40LT1G*			3000 / Tape & Reel		
BC807-40LT3G	5C		10 000 / Tone & Reel		
SBC807-40LT3G*	50		10,000 / Tape & Reel		

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging

Specifications Brochure, BRD8011/D.
\*S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable.

#### **TYPICAL CHARACTERISTICS - BC807-16LT1**

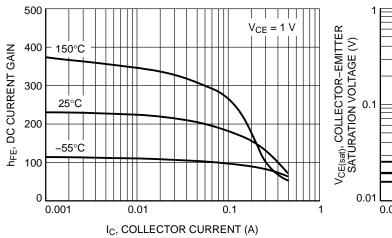


Figure 1. DC Current Gain vs. Collector Current

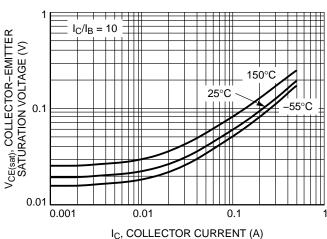


Figure 2. Collector Emitter Saturation Voltage vs. Collector Current

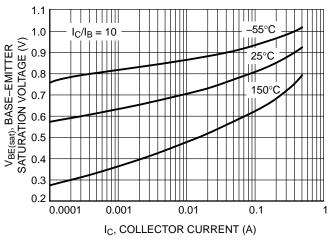


Figure 3. Base Emitter Saturation Voltage vs.
Collector Current

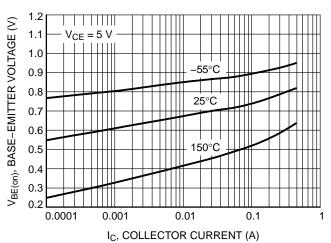


Figure 4. Base Emitter Voltage vs. Collector Current

# **TYPICAL CHARACTERISTICS - BC807-16LT1**

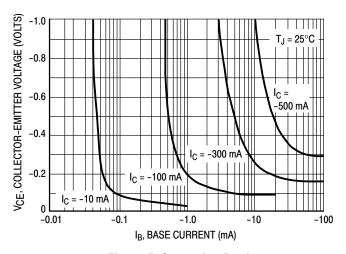
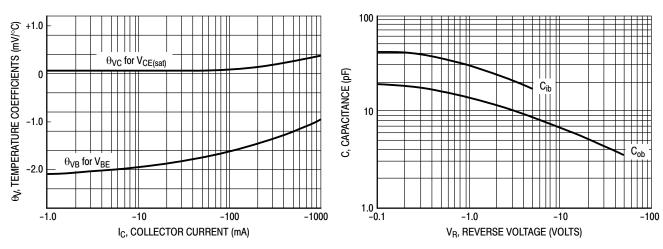


Figure 5. Saturation Region



**Figure 6. Temperature Coefficients** 

Figure 7. Capacitances

#### TYPICAL CHARACTERISTICS - BC807-25LT1

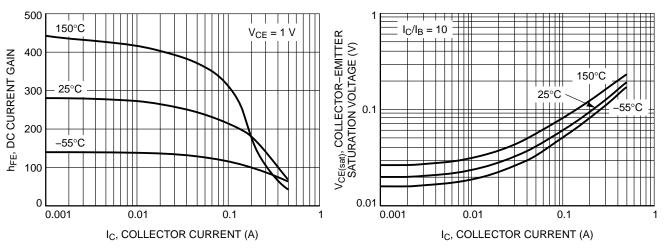


Figure 8. DC Current Gain vs. Collector Current

Figure 9. Collector Emitter Saturation Voltage vs. Collector Current

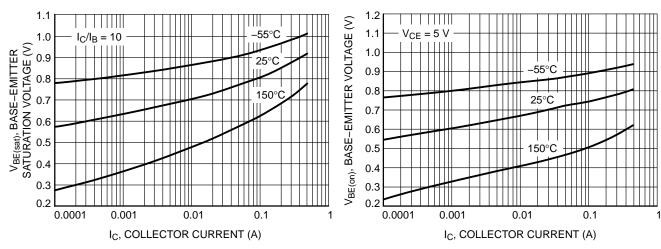


Figure 10. Base Emitter Saturation Voltage vs.
Collector Current

Figure 11. Base Emitter Voltage vs. Collector Current

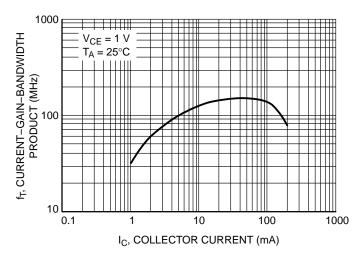


Figure 12. Current Gain Bandwidth Product vs. Collector Current

# **TYPICAL CHARACTERISTICS - BC807-25LT1**

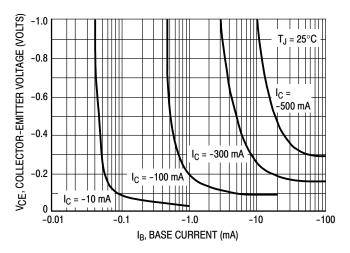


Figure 13. Saturation Region

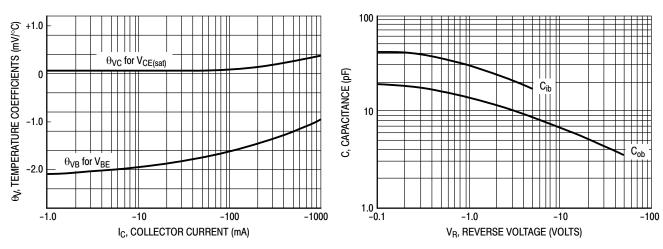


Figure 14. Temperature Coefficients

Figure 15. Capacitances

#### **TYPICAL CHARACTERISTICS - BC807-40LT1**

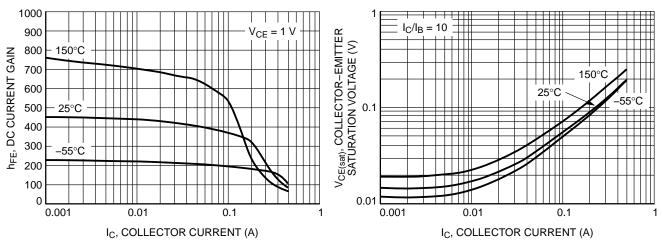


Figure 16. DC Current Gain vs. Collector Current

Figure 17. Collector Emitter Saturation Voltage vs. Collector Current

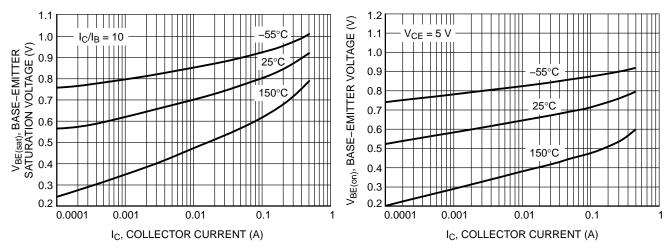


Figure 18. Base Emitter Saturation Voltage vs.
Collector Current

Figure 19. Base Emitter Voltage vs. Collector
Current

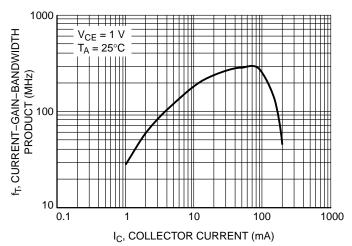


Figure 20. Current Gain Bandwidth Product vs. Collector Current

# **TYPICAL CHARACTERISTICS - BC807-40LT1**

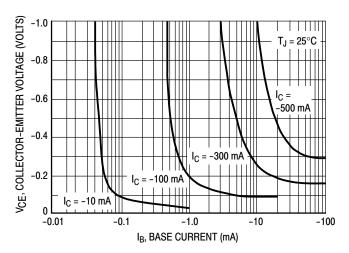


Figure 21. Saturation Region

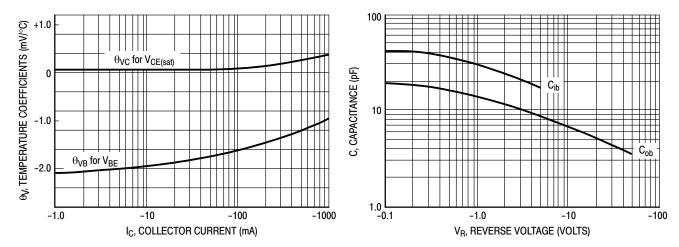


Figure 22. Temperature Coefficients

Figure 23. Capacitances

# TYPICAL CHARACTERISTICS - BC807-16LT1, BC807-25LT1, BC807-40LT1

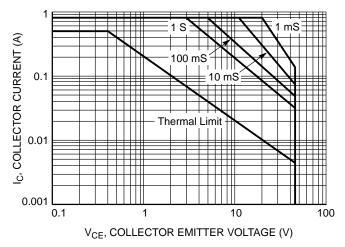
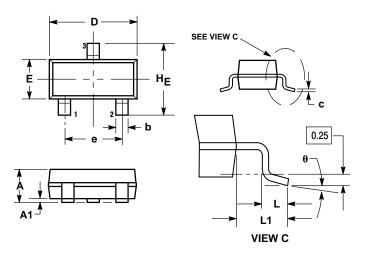


Figure 24. Safe Operating Area

#### PACKAGE DIMENSIONS

#### SOT-23 (TO-236) CASE 318-08 **ISSUE AP**



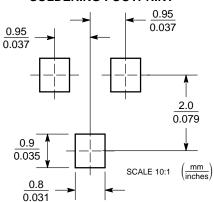
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
   CONTROLLING DIMENSION: INCH.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
- PROTRUSIONS, OR GATE BURRS

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104
θ	0°		10°	0°		10°

STYLE 6: PIN 1. BASE

**EMITTER** 2. COLLECTOR

#### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and 🕠 are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="https://www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81–3–5817–1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative