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RFMD
SBB-1089Z

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sales@integrated-circuit.com

SBB1089Z

50MHz to 850MHz, CASCADABLE ACTIVE BIAS InGaP HBT MMIC AMPLIFIER

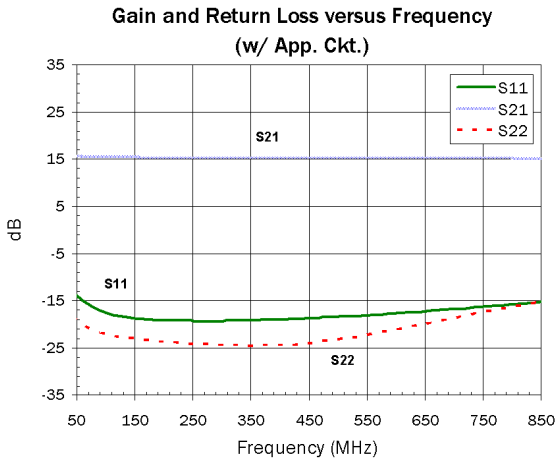
Package: SOT-89

Product Description

RFMD's SBB1089Z is a high performance InGaP HBT MMIC amplifier utilizing a Darlington configuration with an active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 5V supply, the SBB1089Z does not require a dropping resistor as compared to typical Darlington amplifiers. The SBB1089Z product is designed for high linearity 5V gain block applications that require small size and minimal external components. It is internally matched to 50Ω.

Optimum Technology Matching® Applied

- [ ] GaAs HBT
- [ ] GaAs MESFET
- [x] InGaP HBT
- [ ] SiGe BiCMOS
- [ ] Si BiCMOS
- [ ] SiGe HBT
- [ ] GaAs pHEMT
- [ ] Si CMOS
- [ ] Si BJT
- [ ] GaN HEMT
- [ ] InP HBT
- [ ] RF MEMS
- [ ] LDMOS



Features

- OIP_3=43.1dBm at 240MHz
- P_{1dB}=19.6dBm at 500MHz
- Single Fixed 5V Supply
- Robust 1000V ESD, Class 1C
- Patented Thermal Design and Bias Circuit
- Low Thermal Resistance

Applications

- Receiver IF Amplifier
- Cellular, PCS, GSM, UMTS
- Wireless Data, Satellite Terminals

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Small Signal Gain		15.5		dB	70MHz
	14.0	15.5	17.0	dB	240MHz
	14.0	15.5	17.0	dB	400MHz
Output Power at 1dB Compression		19.0		dBm	70MHz
		19.0		dBm	240MHz
	18.0	19.0		dBm	400MHz
Third Order Intercept Point		42.0		dBm	70MHz
		43.0		dBm	240MHz
	38.5	40.5		dBm	400MHz
Return Loss		50 to 850		MHz	Minimum 10dB
Input Return Loss	14.0	18.0		dB	70MHz to 5000MHz
Output Return Loss	12.0	16.0		dB	70MHz to 5000MHz
Noise Figure		3.5	4.2	dB	500MHz
Reverse Isolation		18.0		dB	70MHz to 5000MHz
Operating Temp Range (T_L)	-55		+110	°C	
Thermal Resistance		48.8		°C/W	junction - lead
Device Operating Voltage		5.0	5.3	V	
Device Operating Current	82.0	90.0	98.0	mA	

Test Conditions: V_D=5V, I_D=90mA Typ., OIP_3 Tone Spacing=1MHz, P_{OUT} per tone=0dBm, T_L=25°C, Z_S=Z_L=50Ω, Tested with Bias Tees

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Absolute Maximum Ratings

Parameter	Rating	Unit
Device Current (I_D)	110	mA
Device Voltage (V_D)	5.5	V
RF Input Power	24	dBm
Junction Temp (T_J)	+150	°C
Storage Temp	+150	°C
Power Dissipation	0.61	W
ESD Rating - Human Body Model (HBM)	Class 1C	
Moisture Sensitivity Level	MSL2	

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.
Bias Conditions should also satisfy the following expression:
$I_D V_D < (T_J - T_L)/R_{TH}$, j-l and $T_L = T_{LEAD}$

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RoHS status based on EUDirective2002/95/EC (at time of this document revision).

Typical RF Performance at Key Operating Frequencies (With 240MHz Application Circuit)

Parameter	Unit	50MHz	70MHz	100 MHz	240 MHz	400 MHz	500 MHz	850 MHz
Small Signal Gain, S_{21}	dB	16.0	15.5	15.5	15.5	15.5	15.5	15.0
Output Third Order Intercept Point, OIP_3	dBm	41.5	42.0	43.0	43.0	41.0	40.0	35.0
Output Power at 1dB Compression, P_{1dB}	dBm	19.0	19.0	19.0	19.0	19.0	19.0	18.0
Input Return Loss, S_{11}	dB	13.0	16.0	17.0	19.0	19.0	18.0	15.0
Output Return Loss, S_{22}	dB	18.0	20.0	21.0	23.0	24.0	23.0	17.0
Reverse Isolation, S_{12}	dB	18.0	18.0	18.0	18.0	18.0	18.0	18.0
Noise Figure, NF	dB	3.5	3.3	3.2	3.1	3.2	3.2	3.4

Test Conditions: V_{CC}=5V I_D=90mA Typ. OIP_3 Tone Spacing=1MHz, P_{OUT} per tone=0dBm T_L=25°C $Z_S = Z_L$=50Ω

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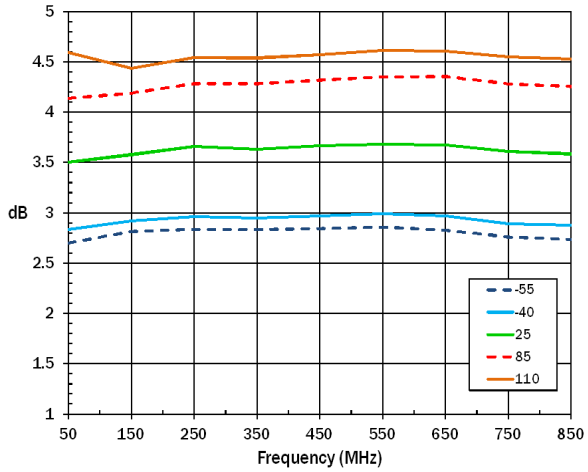
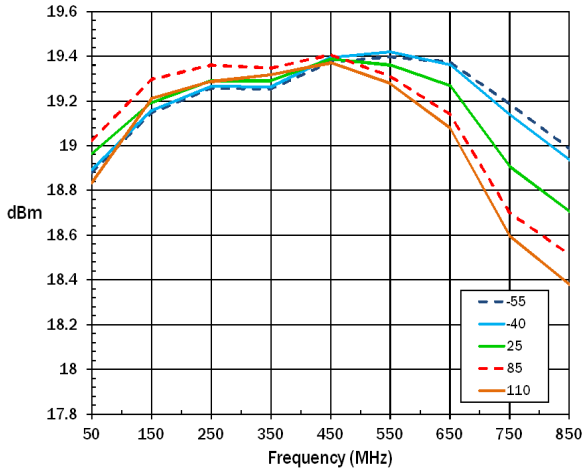
Data on Charts taken with 240 MHz App. Ckt.

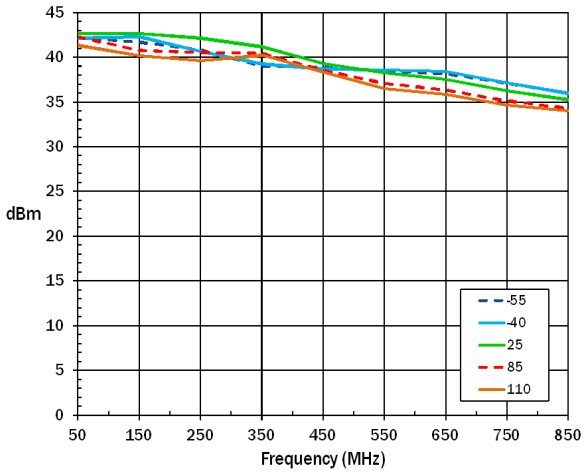
Noise Figure versus Frequency



P1dB versus Frequency



OIP3 versus Frequency



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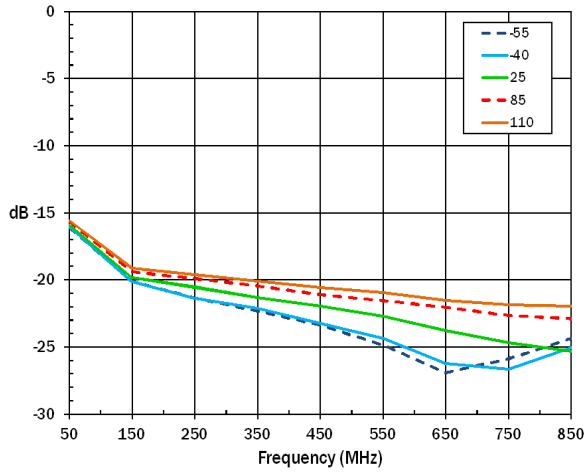
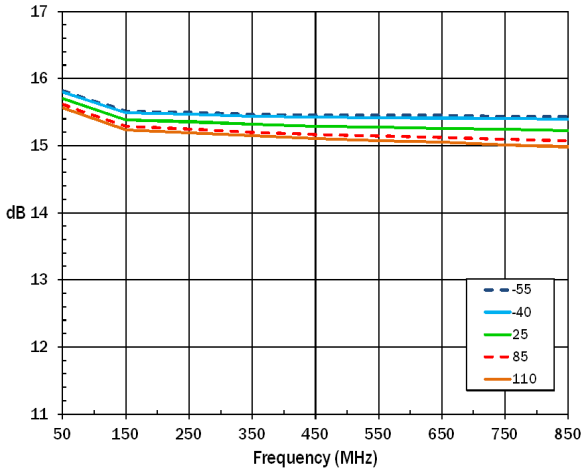
S-Parameters over Temperature (Bias Tee)

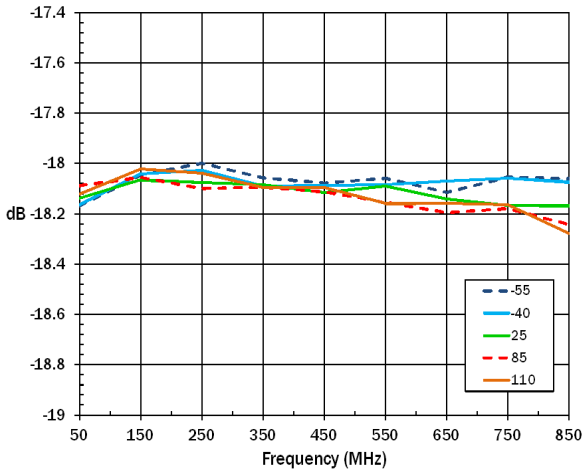
S11 versus Frequency



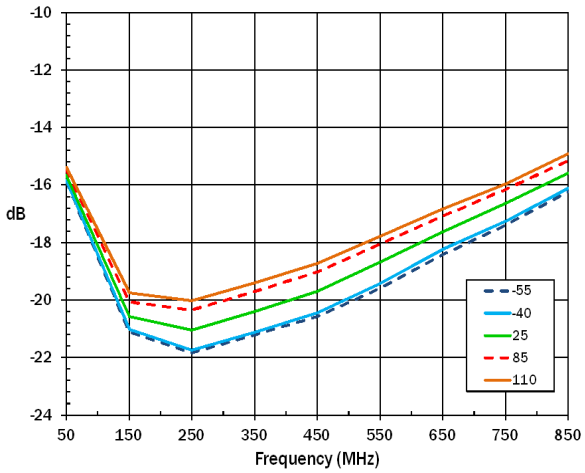
S21 versus Freuqency



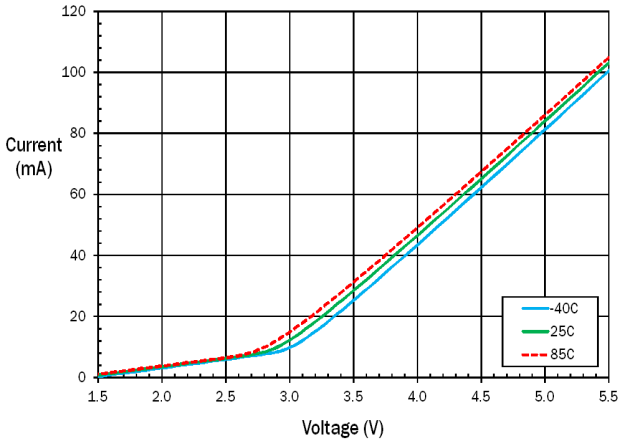
S12 versus Frequency



S22 versus Frequency



Current versus Voltage



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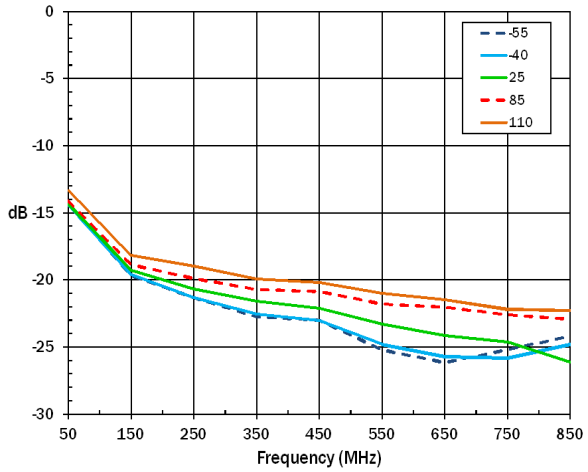
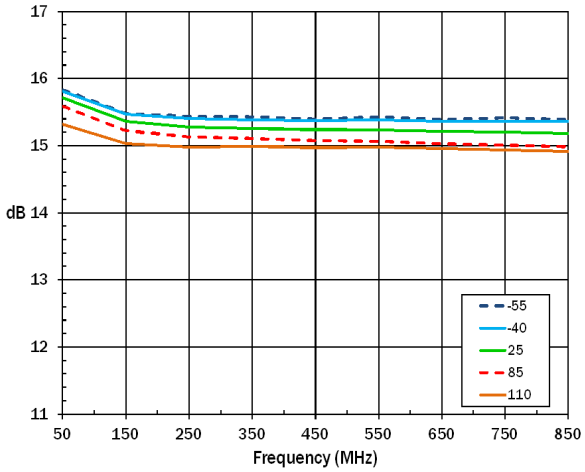
240 MHz Application Circuit S-Parameters over Temperature

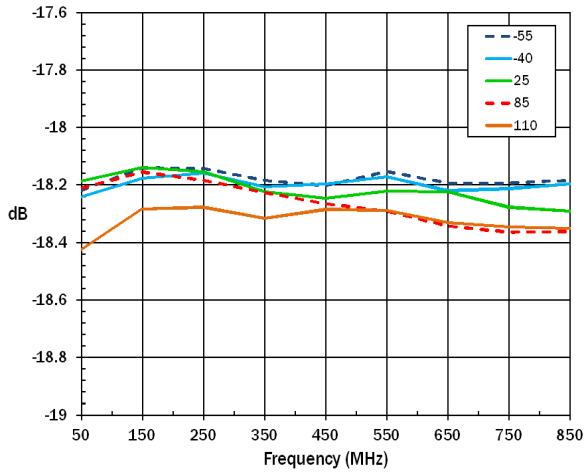
S11 versus Frequency



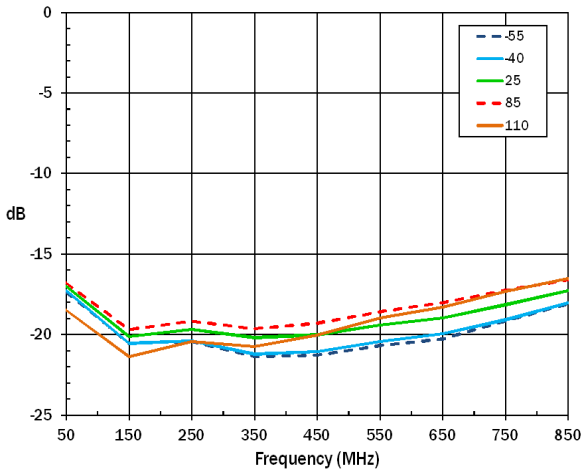
S21 versus Frequency



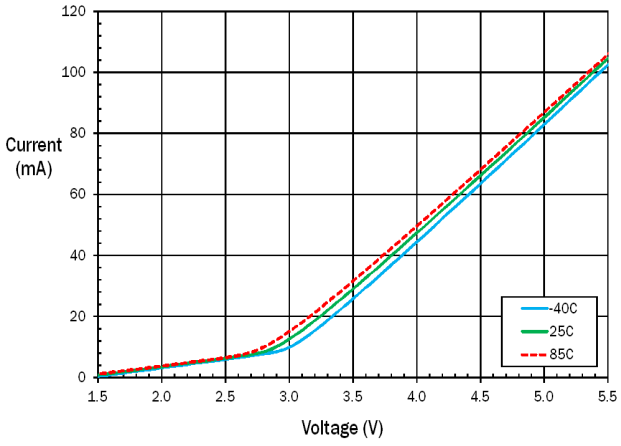
S12 versus Frequency



S22 versus Frequency



Current versus Voltage



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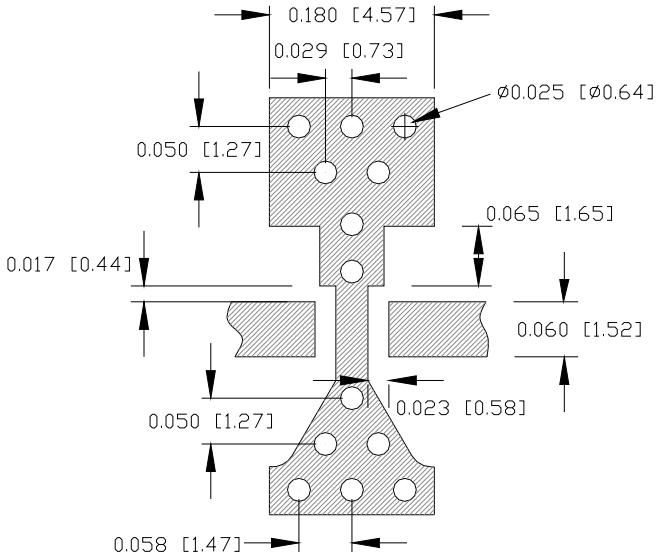
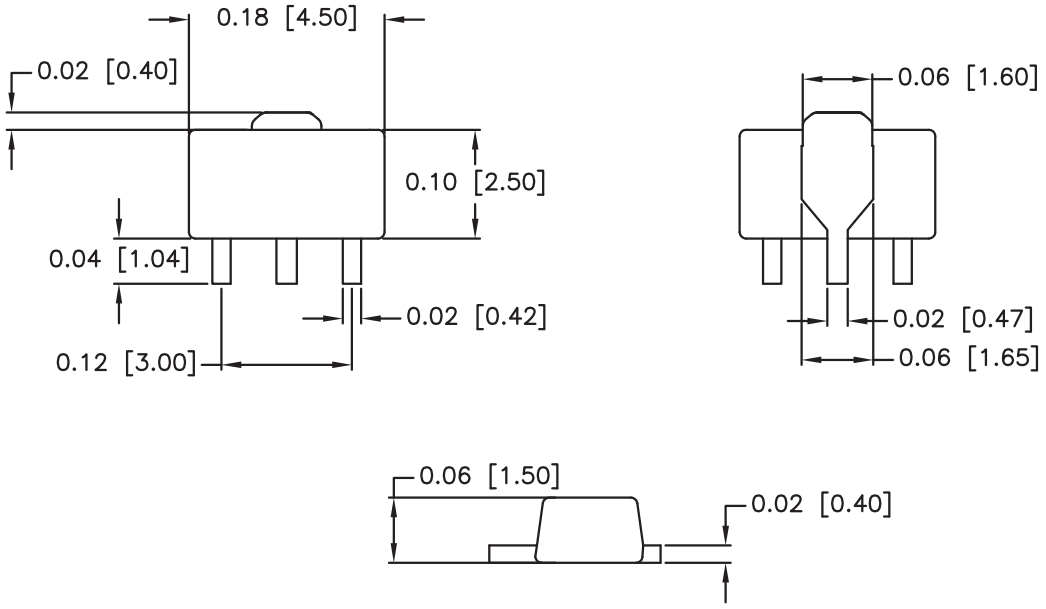
Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on tis pin, therefore a DC blocking capacitor is necessary for proper operation.

Suggested PCB Pad Layout



Package Drawing

Dimensions in inches (millimeters)
Refer to drawing posted at www.rfmd.com for tolerances.



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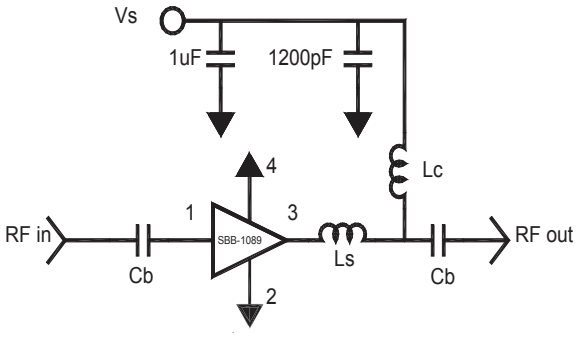
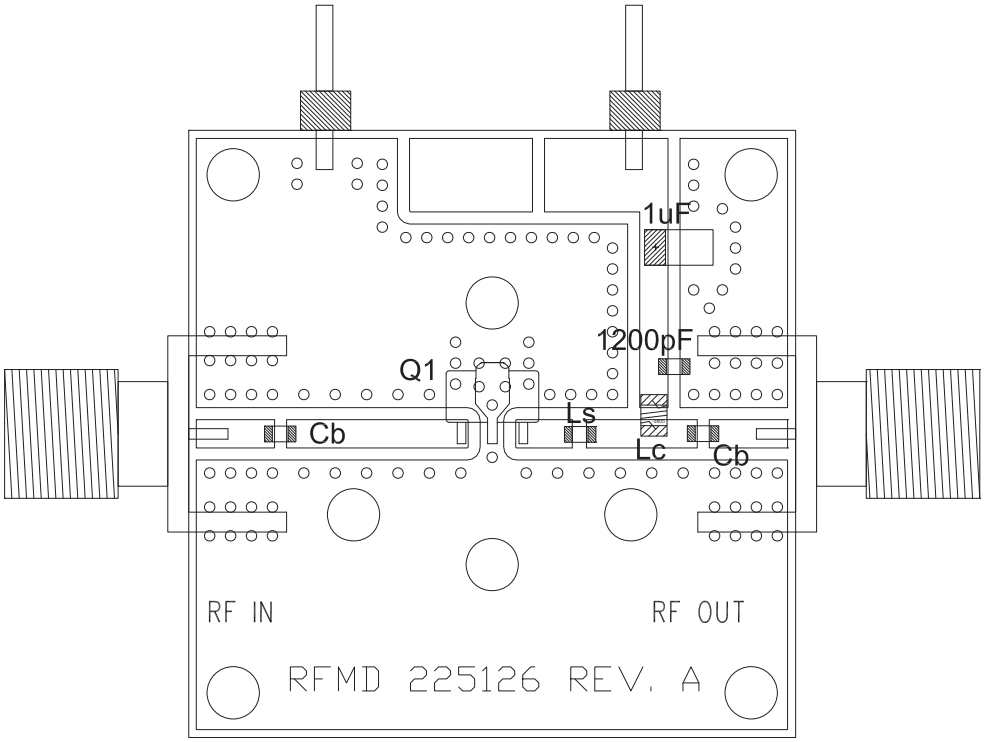
Application Circuit Element Values

Reference Designator	Frequency (MHZ) 50 to 850
C_B	8200pF
L_C	1500nH 0805LS Coilcraft
L_S	2.7nH Toko

Application Schematic



Evaluation Board Layout



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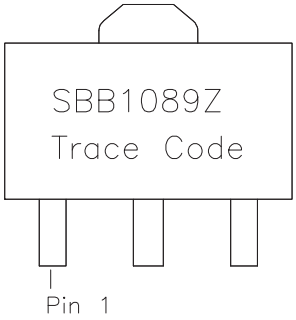
Package Marking



Ordering Information

Ordering Code	Description
SBB1089Z	7" Reel with 1000 pieces
SBB1089ZSQ	Sample bag with 25 pieces
SBB1089ZSR	7" Reel with 100 pieces
SBB1089ZPCK1	50MHz to 850MHz PCBA with 5-piece sample bag

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