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NXP Semiconductors MC9S12A256CPVE

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Distributor of NXP Semiconductors: Excellent Integrated System Limited

Datasheet of MC9S12A256CPVE - IC MCU 16BIT 256KB FLASH 16BIT

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Microcontrollers

C9S12DJ256

Target Applications

- > Automotive applications
- > Industrial control

Overview

Freescale Semiconductor's HCS12 family of microcontrollers (MCUs) is the next generation of the highly successful 68HC12 architecture. Using Freescale's industry-leading 0.25 µs Flash, the MC9S12DJ256 is part of a pin-compatible family that scales from 32 KB to 512 KB of Flash memory. The DJ256 provides an upward migration path from Freescale's 68HC08, 68HC11 and 68HC12 architectures for applications that need larger memory, more peripherals and higher performance. Also, with the increasing number of CAN/J1850-based electronic control units (ECUs), its multiple network modules support this environment by enabling highly efficient communications between different network buses.

HCS12 CPU					
2 x SCI			256 KB Flash		
3 x SPI	I ² C		12 KB RAM	4 KB EEPROM	
Vreg 5V to 2.5V			ATD0 8-ch., 10-bit	ATD1 8-ch., 10-bit	
16-Key Wake-Up IRQ Ports		sns			
BDLC J1850 Interface		Internal Bus	Enhanced Capture Timer 8-ch., 16-bit		
5 x CAN 2.0 A/B			PWM 8-ch., 8-bit/4-ch., 16-bit		

Features	Benefits
High-Performance 16-bit HCS12 CPU Core	
> 25 MHz bus operation at 5V for 40 ns minimum instruction cycle time	> Opcode compatible with the 68HC11 and 68HC12
	> C-optimized architecture produces extremely compact code
On-Chip Debug Interface	
> Dedicated serial debug interface> On-chip breakpoints	 Real-time in-circuit emulation and debug without expensive and cumbersome box emulators
	> Read/write memory and registers while running at full speed
Network Modules	
Two msCAN modules implementing the CAN 2.0 A/B protocol Five receive buffers per module with FIFO	 Ability to link modules for higher buffer count Programmable bit rate up to 1 Mbps FIFO receive approach superior for

Integrated Third-Generation Flash Memory

• Three transmit buffers per module with

- > In-application reprogrammable
- > Self-timed, fast programming

storage scheme

> One J1850 module

internal prioritization

- Fast Flash page erase—20 ms (512 bytes)
- Can program 16 bits in 20 µs while in burst mode
- > 5V Flash program/erase/read
- > Flash granularity—512 byte Flash erase/ 2 byte Flash program
- > Four independently programmable Flash arrays
- > Flexible block protection and security

> Flexibility to change code in the field

> Ability to send and receive messages across

an SAE J1850 serial communication network

event-driven networks

- > Efficient end-of-line programming
- > Total program time for 256 KB code is less than 10 seconds
- > Reduces production programming cost through ultra-fast programming
- > No external high voltage or charge pump required
- > Virtual EEPROM implementation, Flash array usable for EE extension
- > Can erase one array while executing code

4 KB Integrated EEPROM

- > Flexible protection scheme for protection against accidental program or erase
- > EEPROM can be programmed in 46 μs
- > Can erase 4 bytes at a time and program 2 bytes at a time for calibration, security, personality and diagnostic information

10-bit Analog-to-Digital Converter (ADC)

- > Two, 8-channel ADCs
- > 7 μ s, 10-bit single conversion time, scan mode available
- > Fast, easy conversion from analog inputs like temperature, pressure and fluid levels to digital values for CPU processing
- > Can effectively have 3.5 µs conversion time by sampling same signal with both ADCs





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Benefits

Clock Generation Module with Phase-Lock Loop (PLL)

- Clock monitor with self clock mode in case of no external clock
- > Programmable clock frequency with 1024 options ranging from divide by 16 to multiply by 64 from base oscillator
- > Real-time interrupt
- > Watchdog

- > Reliable, robust operation
- > Provides high performance using low-cost reference crystals
- > Reduces generated noise
- > Reduces power consumption
- > Easily able to implement real-time clock

Enhanced Capture Timer

- > 8-channel, 16-bit with input capture, output compare and pulse accumulator
- > 16-bit modulus down counter
- > Flexible, programmable timer system

8-bit or 16-bit Pulse-Width Modulation (PWM)

- > 8-channel, 8-bit or 4-channel, 16-bit PWM
- > PWM supports center-aligned operation
- > Efficiently implement motor control, battery charging or digital-to-analog (DAC) functions

Two Serial Communications Interfaces

> 8192 prescaler options

- Asynchronous communication between the MCU and a terminal, computer or a network of MCUs
- > Exact baud rate matching

Three Serial Peripheral Interfaces

> Up to 6.25 Mbps

> High-speed synchronous communication between multiple MCUs or between MCU and serial peripherals

Inter-IC (I2C) Bus

- > 256 clock-rate options
- > Provides a simple, efficient method of data exchange between devices
- > Minimizes the need for large numbers of connections between devices and eliminates he need for an address decoder

Up to 91 Input/Output (I/O) Lines

- > Programmable pull-ups/pull-downs
- > Dual drive capability

- > Reduce system cost
- > Able to tailor application for minimum EMC or high current loads

Data Sheets

9S12DP256BDGV2 MC9S12DP256 Device Guide MC9S12DP256 Port Integration S12DP256PIMV2

Module Block Guide S12BDMV4 HCS12 Background Debug (BDM)

Block Guide

S12BKVD1 HCS12 Breakpoint (BKP) Block Guide S12CPUV2 HCS12 CPU Reference Manual S12MSCANV2 HCS12 Motorola Scalable Controller

Area Network Block Guide HCS12 10-bit 8-channel Analog to

S12ATD10B8CV2 Digital Block Guide

S12CRGV3 HCS12 Clock Reset Generator

Block Guide

S12ECT16B8CV1 HCS12 16-bit 8-channel Enhanced

Capture Timer Block Guide S12FFTS4KV2 HCS12 4K EEPROM Block Guide HCS12 256K Flash Block Guide S12FT256KV2 HCS12 I2C Block Guide S12IICV2 HCS12 Interrupt (INT) Block Guide S12INTV1

HCS12 Multiplexed External Bus Interface (MEBI) Block Guide S12MEBIV3 HCS12 Module Mapping Control S12MMCV4

(MMC) Block Guide

S12PWM8B8CV1 HCS12 8-bit 8-channel Pulse-Width Modulator Block Guide

S12SCIV2 HCS12 Serial Communications Interface Block Guide

S12SPIV2 HCS12 Serial Peripheral Interface Block Guide

S12VRFGV1 HCS12 Voltage Regulator

Block Guide

Cost-Effective Development Tools

For more information on development tools, please refer to the Freescale Development Tool Selector Guide (SG1011).

M68KIT912DP256

Evaluation kit for development and evaluation of HCS12 application code that includes the M68EVB912DP256 and USBMULTILINKBDM

M68CYCLONEPRO

\$499

HC08/HCS08/HC12/HCS12 stand-alone Flash programmer or in-circuit emulator, debugger, Flash programmer; USB, serial or Ethernet interface options

USBMULTILINKBDM Universal HCS08/HCS12 in-circuit emulator, debugger, and Flash programmer; USB PC interface

CWX-H12-SE

CodeWarrior™ Special Edition for HCS12 MCUs; includes integrated development environment (IDE), linker, debugger, unlimited assembler, Processor Expert™ auto-code generator, full-chip simulation and limited Compiler. simulation and limited C compiler

Application Notes and Engineering Bulletins

AN2206	Security and Protection on the HCS12 Family
AN2213	Using Cosmic Software's M68HC12 Compiler for MC9S12DP256 Software Development
AN2216	MC9S12DP256 Software Development Using Metrowerks CodeWarrior™
AN2250	Audio Reproduction on HCS12 Microcontrollers
EB386	HCS12 D-Family Compatibility

Package Options

Part Number Package Temp. Range MC9S12DJ256BCFU MC9S12DJ256BVFU 80 QFP 80 LQFP -40°C to +85°C -40°C to +105°C -40°C to +125°C MC9S12DJ256BMFU 80 QFP MC9S12DJ256BCPV 112 LQFP 112 LQFP -40°C to +85°C -40°C to +105°C MC9S12DJ256BVPV MC9S12DJ256BMPV 112 LQFP -40°C to +125°C 112-Lead LQFP

80-Lead QFP



Learn More: For more information about Freescale products, please visit www.freescale.com.

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