

# **Excellent Integrated System Limited**

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Fairchild Semiconductor 1N4150\_T50R

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**Distributor of Fairchild Semiconductor: Excellent Integrated System Limited** Datasheet of 1N4150\_T50R - DIODE GEN PURP 50V 200MA DO35 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

1N4150 / FDLL4150

Discrete POWER & Signal FAIRCHILD **Technologies** SEMICONDUCTOR M 1N4150 / FDLL4150 COLOR BAND MARKING DEVICE 1ST BAND 2ND BAND FDLL4150 BLACK ORANGE LL-34 THE PLACEMENT OF THE EXPANSION GAP HAS NO RELATIONSHIP TO THE LOCATION OF THE CATHODE TERMINAL DO-35 **High Conductance Ultra Fast Diode** Sourced from Process 1R. See MMBD1201-1205 for characteristics. **Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted Symbol Parameter Value Units 50 WIV Working Inverse Voltage V Average Rectified Current 200 mΑ  $I_{O}$ DC Forward Current  $I_{\rm F}$ 400 mΑ Recurrent Peak Forward Current 600 mΑ İf Peak Forward Surge Current İf(surge) Pulse width = 1.0 second 1.0 A Pulse width = 1.0 microsecond 4.0 А Storage Temperature Range -65 to +200 °C  $\mathsf{T}_{\mathsf{stg}}$ **Operating Junction Temperature** 175 °C ТJ \*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired. NOTES: These ratings are based on a maximum junction temperature of 200 degrees C.
These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations. **Thermal Characteristics** TA = 25°C unless otherwise noted Cumhal Max l lmita

Symbol	Characteristic	Iviax	Units
		1N / FDLL 4150	
P <sub>D</sub>	Total Device Dissipation	500	mW
	Derate above 25°C	3.33	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	300	°C/W

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## High Conductance Ultra Fast Diode

(continued)

Electr	Electrical Characteristics TA = 25°C unless otherwise noted							
Symbol	Parameter	Test Conditions	Min	Max	Units			
B <sub>V</sub>	Breakdown Voltage	$I_R = 5.0 \ \mu A$	75		V			
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 50 V V <sub>R</sub> = 50 V, T <sub>A</sub> = 150°C		100 100	nA μA			
V <sub>F</sub>	Forward Voltage	$I_{F} = 1.0 \text{ mA}$ $I_{F} = 10 \text{ mA}$ $I_{F} = 50 \text{ mA}$ $I_{F} = 100 \text{ mA}$ $I_{F} = 200 \text{ mA}$	540 660 760 820 0.87	620 740 860 920 1.0	mV mV mV mV V			
Co	Diode Capacitance	V <sub>R</sub> = 0, f = 1.0 MHz		2.5	pF			
T <sub>RR</sub>	Reverse Recovery Time	$I_F = I_R = 10 \text{ mA-}200 \text{ mA}, R_L = 100\Omega$ $I_F = I_R = 200 \text{ mA-}400 \text{ mA}, R_L = 100\Omega$		4.0 6.0	nS nS			
T <sub>FR</sub>	Forward Recovery Time	$I_F = 200 \text{ mA}, V_{FR} = 1.0 \text{ V}$		10	nS			

# 1N4150 / FDLL4150



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