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Fairchild Semiconductor FDC6325L

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Datasheet of FDC6325L - IC LOAD SWITCH INT 8VIN SSOT-6 Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com

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August 1998

FDC6325L Integrated Load Switch

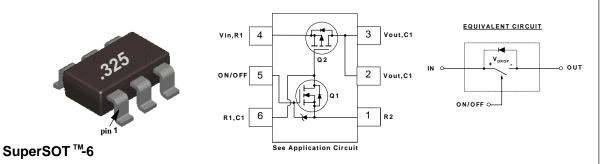
General Description

This device is particularly suited for compact power management in portable electronic equipment where 2.5V to 8V input and 1.8A output current capability are needed. This load switch integrates a small N-Channel power MOSFET (Q1) which drives a large P-Channel power MOSFET (Q2) in one tiny SuperSOTTM-6 package.

Features

- SuperSOTTM-6 package design using copper lead frame for superior thermal and electrical capabilities.





Absolute Maximum Ratings T_a = 25°C unless otherwise noted

Symbol	Parameter	FDC6325L	Units	
V _{IN}	Input Voltage Range	2.5 - 8	V	
V _{ONOFF}	On/Off Voltage Range	1.5 - 8	V	
L	Load Current - Continuous (Note 1)	1.8	А	
	- Pulsed (Note 1 & 3)	5		
D D	Maximum Power Dissipation (Note 2)	0.7	W	
J,T _{STG}	Operating and Storage Temperature Range	-55 to 150	℃	
SD	Electrostatic Discharge Rating MIL-STD-883D Human Body Model (100pf/1500Ohm)	6	kV	
THERMA	L CHARACTERISTICS		·	
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient (Note 2)	180	°C/W	
R _{euc}	Thermal Resistance, Junction-to-Case (Note 2)	60	°C/W	

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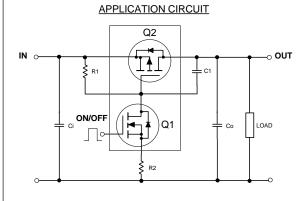
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Symbol	Parameter	Conditions	Min	Тур	Max	Units
OFF CHA	RACTERISTICS	·				
I _{FL}	Forward Leakage Current	V _{IN} = 8 V, V _{ON/OFF} = 0 V			1	μA
ON CHAR	ACTERISTICS (Note 3)					
V _{DROP}	Conduction Voltage Drop	$V_{IN} = 5 \text{ V}, \ V_{ON/OFF} = 3.3 \text{ V}, \ I_{L} = 1.5 \text{ A}$		0.15	0.2	V
		$V_{IN} = 3.3 \text{ V}, \ V_{ONOFF} = 3.3 \text{ V}, \ I_L = 1.2 \text{ A}$		0.145	0.2	
		$V_{IN} = 2.5 \text{ V}, \ V_{ONOFF} = 3.3 \text{ V}, \ I_L = 1 \text{ A}$		0.13	0.2	
R _(ON)	Q ₂ - Static On-Resistance	$V_{GS} = -5 \text{ V}, I_{D} = -1.8 \text{ A}$		0.115	0.13	Ω
		$V_{GS} = -3.3 \text{ V}, I_{D} = -1.6 \text{ A}$		0.13	0.16	
		$V_{GS} = -2.5 \text{ V}, I_D = -1.5 \text{ A}$		0.155	0.18	
I _L	Load Current	$V_{DROP} = 0.13 \text{ V}, V_{IN} = 5 \text{ V}, V_{ONOFF} = 3.3 \text{ V}$	1			Α
		$V_{DROP} = 0.16 \text{ V}, V_{IN} = 3.3 \text{ V}, V_{ON/OFF} = 3.3 \text{ V}$	1			
		$V_{DROP} = 0.2 \text{ V}, V_{IN} = 2.5 \text{V}, V_{ON/OFF} = 3.3 \text{ V}$	1			

Notes:

- 1. V_{IN} =8V, $V_{ON/OFF}$ =8V, T_A =25°C
- 2. R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJC} is guaranteed by design while R_{BCA} is determined by the user's board design.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

FDC6325L Load Switch Application



External Component Recommendation

For Co £ 1uF applications:

First select R2, 100 - 1kW, for Slew Rate control. C1 £ 1000pF can be added in addition to R2 for further In-rush current control.

Then select R1 such that R1/R2 ratio maintains between 10 - 100. R1 is required to turn Q2 off. For SPICE simulation, users can download a "FDC6325L.MOD" Spice model from Fairchild Web Site at www.fairchildsemi.com





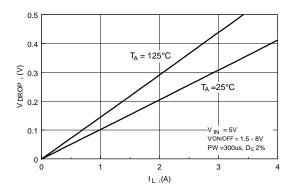


Figure 1. Conduction Voltage Drop Variation with Load Current.

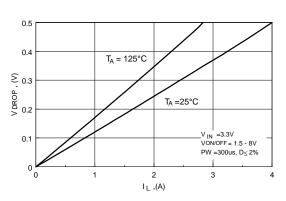


Figure 2. Conduction Voltage Drop Variation with Load Current.

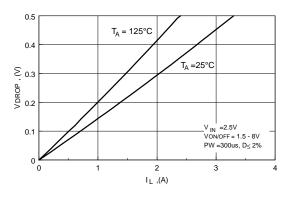


Figure 3. Conduction Voltage Drop
Variation with Load Current.

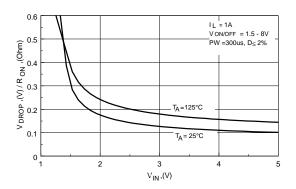


Figure 4. On-Resistance Variation with Input Voltage.

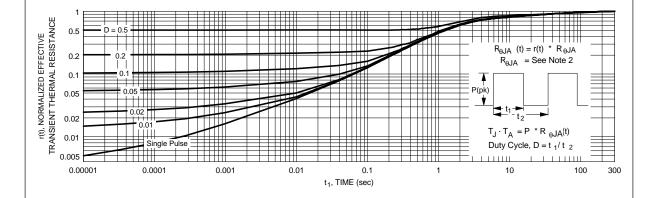


Figure 5. Transient Thermal Response Curve.

Thermal characterization performed on the conditions described in Note 2. Transient thermal response will change depends on the circuit board design.



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